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**A Comprehensive Bibliography of
Distributed Shared Memory**

by

M. Rasit Eskicioglu

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**DEPARTMENT OF COMPUTING SCIENCE
University of Alberta
Edmonton, Alberta, Canada**

A Comprehensive Bibliography of Distributed Shared Memory

M. Rasit Eskicioglu
Department of Computing Science
University of Alberta
Edmonton, AB T6G 2H1
rasit@cs.ualberta.ca
<http://www.cs.ualberta.ca/~rasit>

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Abstract

Shared memory is an attractive programming model for designing parallel and distributed applications. In the past decade, a popular research topic has been the design of systems to provide the shared memory abstraction on physically distributed memory machines. This abstraction is commonly known as *Distributed Shared Memory (DSM)*. DSM has been implemented both in software (e.g., to provide the shared memory programming model on networks of workstations) and in hardware (e.g., using cache consistency protocols to support shared memory across physically distributed main memories). This bibliography identifies the results of research on DSM and related topics.

In this bibliography, we broadly classify papers describing previous DSM research into the following categories:

1. Concepts and origins,
2. Consistency models,
3. Memory coherence protocols and algorithms,
4. Hardware implementations,
5. Software implementations,
6. Language support for DSM (including support for distributed shared objects),
7. Performance evaluation and analysis,
8. Related issues (e.g., synchronization, fault tolerance, heterogeneity, and persistence),
9. Miscellaneous (papers that do not fall into the previous categories, e.g., surveys.)

Determining the most appropriate classification for existing DSM research was a difficult task and placing papers into a single category sometimes required making seemingly arbitrary decisions. Each paper was placed into the category which the author felt was most relevant to the work described in that paper—the author apologizes in advance to researchers who feel that their work was incorrectly categorized or omitted entirely. Any feedback is greatly appreciated.

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Currently, there are 1327 entries in this bibliography.

Concepts and Origins

[Abramson 1981] Abramson, D. A. Hardware Memory Management of a Large Virtual Memory. In *Proc. of the 4th Australian Computer Science Conf. (ACSC-4)*, pages 1–13, January 1981.

[Abramson and Keedy 1985] Abramson, D. A. and Keedy, J. L. Implementing a Large Virtual Memory in a Distributed Computer System. In *Proc. of the 18th Hawaii Int'l Conf. on System Sciences (HICSS-18)*, pages 515–522, January 1985.

[Adve *et al.* 1999] Adve, S. V., Pai, V. S., and Ranganathan, P. Recent Advances in Memory Consistency Models for Hardware Shared Memory Systems. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):445–455, March 1999.

- [Ashihara *et al.* 1989] Ashihara, H., Shimizu, K., and Maekawa, M. A New Unified Data Sharing Mechanism in the GALAXY Distributed Operating System. Technical Report TR-89-019, Dept. of Information Science, University of Tokyo, Japan, June 1989.
- [Bell 1994] Bell, G. Scalable, Parallel Computers: Alternatives, Issues, and Challenges. *Int'l Journal of Parallel Programming*, 22(1):3–46, January 1994.
- [Bell and van Ingen 1999] Bell, G. and van Ingen, C. DSM Perspective: Another Point of View. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):412–417, March 1999.
- [Birman *et al.* 1986] Birman, K., Joseph, T., and Stevenson, P. Programming with Shared Bulletin Boards in Asynchronous Distributed Systems. Technical Report TR-86-772, Dept. of Computer Science, Cornell University, August 1986.
- [Carter *et al.* 1998] Carter, J. B., Ranganathan, A., and Susarla, S. Building Clustered Services and Applications Using a Global Memory System. In *Proc. of the 18th Int'l Conf. on Distributed Computing Systems (ICDCS-18)*, May 1998.
- [Cheriton 1985] Cheriton, D. R. Preliminary Thoughts on Problem-oriented Shared Memory: A Decentralized Approach to Distributed Systems. *ACM Operating Systems Review*, 19(4):26–33, October 1985.
- [Cheriton 1986] Cheriton, D. R. Problem-oriented Shared Memory: A Decentralized Approach to Distributed System Design. In *Proc. of the 6th Int'l Conf. on Distributed Computing Systems (ICDCS-6)*, pages 190–197, May 1986.
- [Cheriton 1992] Cheriton, D. R. Problem Oriented Shared Memory Revisited. In *Proc. of the 5th ACM SIGOPS European Workshop*, September 1992.
- [Chiou *et al.* 1995] Chiou, D., Ang, B. S., Greiner, R., Arvind, Hoe, J. C., Beckerle, M. J., Hicks, J. E., and Boughton, S. StarT-NG: Delivering Seamless Parallel Computing. In *Proc. of the First Int'l Euro-Par Conf.*, pages 101–116, August 1995.
- [Comer and Griffioen 1990] Comer, D. and Griffioen, J. A New Design for Distributed Systems: The Remote Memory Model. In *Proc. of the Summer 1990 USENIX Conference*, pages 127–135, June 1990.
- [Covaci and Popescu-Zeletin 1993] Covaci, S. and Popescu-Zeletin, R. The Network-Memory in a Global Distributed Processing System. In *Proc. of the 4th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'93)*, pages 368–374, September 1993.
- [Cox *et al.* 1994] Cox, A. L., Dwarkadas, S., Keleher, P., and Zwaenepoel, W. An Integrated Approach to Distributed Shared Memory. In *Proc. of the First Workshop on Parallel Processing*, December 1994.
- [Falsafi and Wood 1997] Falsafi, B. and Wood, D. A. Reactive NUMA: A Design for Unifying S-COMA and CC-NUMA. In *Proc. of the 24th Annual Int'l Symp. on Computer Architecture (ISCA'97)*, pages 229–240, June 1997.
- [Fleisch 1990] Fleisch, B. D. Using Distributed Shared Memory as a Framework for Distributed Blackboard. In *Proc. of the AAAI-90 Workshop on Blackboard Systems*, July 1990.
- [Hamano *et al.* 1987] Hamano, J., Maekawa, M., and Shimizu, K. Sharable Memory-Mapped Files: An Approach to Shared Memory in a Distributed System. Technical Report 87-25, Dept. of Information Science, University of Tokyo, November 1987.
- [Henskens *et al.* 1991] Henskens, F. A., Rosenberg, J., and Keedy, J. L. A Capability-based Distributed Shared Memory. In *Proc. of the 14th Australian Computer Science Conf. (ACSC-14)*, pages 29.1–29.12, January 1991.
- [Hill *et al.* 1993] Hill, M. D., Larus, J. R., Reinhardt, S. K., and Wood, D. A. Cooperative Shared Memory: Software and Hardware for Scalable Multiprocessors. *ACM Trans. on Computer Systems*, 11(4):300–318, November 1993.

- [Irlenbusch and Kaiser 1995] Irlenbusch, B. and Kaiser, J. Towards a Resilient Shared Memory Concept for Distributed Persistent Object Systems. In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, volume II, pages 675–684, January 1995.
- [Jaja and Ryu 1994] Jaja, J. F. and Ryu, K. W. The Block Distributed Memory Model for Shared Memory Multiprocessors. In *Proc. of the 8th Int'l Parallel Processing Symp. (IPPS'94)*, pages 752–756, April 1994.
- [Keleher 1996] Keleher, P. Sparks: Coherence as an Abstract Type. In *Proc. of the Fifth Int'l Workshop on Object Orientation in Operating Systems (IWOOS'96)*, October 1996.
- [Kristensen and Low 1995] Kristensen, A. and Low, C. Problem-Oriented Object Memory: Customizing Consistency. In *Proc. of the Tenth Annual Conf. on Object-Oriented Programming Systems, Languages, and Applications (OOPSLA'95)*, pages 399–413, October 1995.
- [Kulkarni *et al.* 1993] Kulkarni, D. C., Banerji, A., Casey, M. R., and Cohn, D. L. Structuring Distributed Shared Memory with the “Pi” Architecture. In *Proc. of the 13th Int'l Conf. on Distributed Computing Systems (ICDCS-13)*, pages 93–100, May 1993.
- [Li and Hudak 1986] Li, K. and Hudak, P. Memory Coherence in Shared Virtual Memory Systems. In *Proc. of the 5th Annual ACM Symp. on Principles of Distributed Computing (PODC'86)*, pages 229–239, August 1986.
- [Li 1986] Li, K. Shared Virtual Memory on Loosely Coupled Multiprocessors. PhD thesis, Department of Computer Science, Yale University, September 1986.
- [Mac *et al.* 1992] Mac, S.-C., Shieh, C.-K., Hou, T.-W., Hung, C.-F., and Ueng, J.-C. System Architectures of Distributed Shared Memory Systems. In *Proc. of the Int'l Computer Symp.*, pages 158–165, December 1992.
- [Oguchi *et al.* 1995] Oguchi, M., Aida, H., and Saito, T. A Proposal for a DSM Architecture Suitable for a Widely Distributed Environment and Its Evaluation. In *Proc. of the Fourth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-4)*, pages 32–39, August 1995.
- [Polze and Malek 1995] Polze, A. and Malek, M. Parallel Computing in a World of Workstations. In *Proc. of the Seventh IASTED/ISMM Int'l Conf. on Parallel and Distributed Computing and Systems*, pages 72–74, October 1995.
- [Ramanujan *et al.* 1995] Ramanujan, R. S., Bonney, J. C., and Thurber, K. J. Network-Shared Memory: A New Approach for Clustering Workstations for Parallel Processing. In *Proc. of the Fourth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-4)*, pages 48–56, August 1995.
- [Reinhardt *et al.* 1994] Reinhardt, S. K., Larus, J. R., and Wood, D. A. Tempest and Typhoon: User-Level Shared Memory. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 325–337, April 1994.
- [Rosenberg and Keedy 1981] Rosenberg, J. and Keedy, J. L. Software Management of a Large Virtual Memory. In *Proc. of the 4th Australian Computer Science Conf. (ACSC-4)*, pages 173–181, January 1981.
- [Sandhu *et al.* 1992] Sandhu, H. S., Gamsa, B., and Zhou, S. Region-Oriented Memory Management in Shared-Memory Multiprocessors. Technical Report TR-CSRI-269, Computer Systems Research Institute, University of Toronto, April 1992.
- [Saulsbury *et al.* 1995a] Saulsbury, A., Wilkinson, T., Carter, J. B., and Landin, A. An Argument for Simple COMA. In *Proc. of the 1st IEEE Symp. on High-Performance Computer Architecture (HPCA-1)*, pages 276–285, January 1995.
- [Saulsbury *et al.* 1995b] Saulsbury, A., Wilkinson, T., Carter, J. B., and Landin, A. An Argument for Simple COMA. *Future Generation Computer Systems*, 11(6):553–566, November 1995.

- [Shavit and Touitou 1995] Shavit, N. and Touitou, D. Software Transactional Memory. In *Proc. of the 14th Annual ACM Symp. on Principles of Distributed Computing (PODC'95)*, August 1995.
- [Shavit and Touitou 1997] Shavit, N. and Touitou, D. Software Transactional Memory. *Distributed Computing*, 10(2):99–116, 1997.
- [Wood *et al.* 1996] Wood, D. A., Hill, M. D., and Larus, J. R. The Tempest Approach to Distributed Shared Memory. In *Proc. of the Int'l Conf. on Computer Design—VLSI in Computers and Processors*, pages 63–64, October 1996.
- [Young *et al.* 1987] Young, M., Tevanian, A., Rashid, R., Golub, D., Eppinger, J., Bolosky, W., Black, D., and Baron, R. The Duality of Memory and Communication in the Implementation of a Multiprocessor Operating System. In *Proc. of the 11th ACM Symp. on Operating Systems Principles (SOSP-11)*, pages 63–76, November 1987.

Consistency Models

- [Adve and Hill 1990a] Adve, S. V. and Hill, M. D. Weak Ordering—A New Definition. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 2–14, May 1990.
- [Adve and Hill 1990b] Adve, S. V. and Hill, M. D. Implementing Sequential Consistency in Cache-Based Systems. In *Proc. of the 1990 Int'l Conf. on Parallel Processing (ICPP'90)*, pages 47–50, August 1990.
- [Adve *et al.* 1991] Adve, S. V., Hill, M. D., Miller, B. P., and Netzer, R. H. B. Detecting Data Races on Weak Memory Systems. In *Proc. of the 18th Annual Int'l Symp. on Computer Architecture (ISCA'91)*, pages 234–243, May 1991.
- [Adve and Hill 1992] Adve, S. V. and Hill, M. D. Sufficient Conditions for Implementing the Data-Race-Free-1 Memory Model. Technical Report TR #1107, Computer Sciences Department, University of Wisconsin-Madison, September 1992.
- [Adve and Hill 1993] Adve, S. V. and Hill, M. D. A Unified Formalization of Four Shared-Memory Models. *IEEE Trans. on Parallel and Distributed Systems*, 4(6):613–624, June 1993.
- [Adve 1993] Adve, S. V. Designing Memory Consistency Models for Shared-Memory Multiprocessors. PhD thesis, Computer Sciences Department, University of Wisconsin-Madison, December 1993.
- [Adve *et al.* 1993] Adve, S. V., Gharachorloo, K., Gupta, A., Hennessy, J. L., and Hill, M. D. Sufficient System Requirements for Supporting PLpc Memory Model. Technical Report TR #1200, Computer Sciences Department, University of Wisconsin-Madison, December 1993.
- [Adve and Gharachorloo 1996] Adve, S. V. and Gharachorloo, K. Shared Memory Consistency Models: A Tutorial. *IEEE Computer*, 29(12):66–76, December 1996.
- [Adve and Hill 1998] Adve, S. V. and Hill, M. D. A Retrospective on “Weak Ordering—A New Definition”, pages 60–62. Selected Papers from the First 25 International Symposia on Computer Architecture. ACM Press, 1998.
- [Afek *et al.* 1993] Afek, Y., Brown, G., and Merritt, M. Lazy Caching. *ACM Trans. on Programming Languages and Systems*, 15(1):182–205, January 1993.
- [Agrawal *et al.* 1994] Agrawal, D., Choy, M., Leong, H.-V., and Singh, A. K. Mixed Consistency: A Model for Parallel Programming. In *Proc. of the 13th Annual ACM Symp. on Principles of Distributed Computing (PODC'94)*, pages 101–110, August 1994.
- [Ahamad *et al.* 1991] Ahamad, M., Burns, J. E., Hutto, P. W., and Neiger, G. Casual Memory. In *Proc. of the 5th Int'l Workshop on Distributed Algorithms (WDAG'91)*, number 579 in Lecture Notes in Computer Science, pages 9–30. Springer-Verlag, October 1991.

- [Ahamad *et al.* 1993] Ahamad, M., Bazzi, R. A., John, R., Kohli, P., and Neiger, G. The Power of Processor Consistency (Extended Abstract). In *Proc. of the 5th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'93)*, pages 251–260, June 1993.
- [Ahamad *et al.* 1994] Ahamad, M., John, R., Kohli, P., and Neiger, G. Casual Memory Meets the Consistency and Performance Needs of Distributed Applications! In *Proc. of the 6th ACM SIGOPS European Workshop*, September 1994.
- [Ahamad *et al.* 1995] Ahamad, M., Neiger, G., Kohli, P., Burns, J. E., and Hutto, P. W. Casual Memory: Definitions, Implementation and Programming. *Distributed Computing*, 9:37–49, 1995.
- [Amaral *et al.* 1992] Amaral, P., Jacquemot, C., and Lea, R. A Model for Persistent Shared Memory Addressing in Distributed Systems. In *Proc. of the Second Int'l Workshop on Object Orientation in Operating Systems (IWOOS'92)*, pages 2–12, September 1992.
- [Attiya and Friedman 1992] Attiya, H. and Friedman, R. A Correctness Condition for High Performance Multiprocessors. In *Proc. of the 24th ACM Annual Symp. on the Theory of Computing*, pages 679–690, 1992.
- [Attiya and Welch 1994] Attiya, H. and Welch, J. L. Sequential Consistency versus Linearizability. *ACM Trans. on Computer Systems*, 12(2):91–122, May 1994.
- [Attiya 1995] Attiya, H. Consistency Conditions for Distributed Shared Memory. *Newsletter of the IEEE CS Technical Committee on Computer Architecture*, August 1995.
- [Bataller and Bernabeu 1997] Bataller, J. and Bernabeu, J. Synchronized DSM Models. In *Proc. of the Third Int'l Euro-Par Conf.*, pages 468–475, August 1997.
- [Bataller and Bernabeu 1998] Bataller, J. and Bernabeu, J. Constructive and Adaptable Distributed Shared Memory. In *Proc. of the 3rd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'98)*, pages 15–22, March 1998.
- [Bernabeu-Auban and Cholvi-Juan 1994] Bernabeu-Auban, J. M. and Cholvi-Juan, V. Formalizing Memory Coherence Models. In *Proc. of the 6th Int'l Conf. on Computing and Information (ICCI'94)*, pages 653–672, May 1994.
- [Bershad and Zekauskas 1991] Bershad, B. N. and Zekauskas, M. J. Shared Memory Parallel Programming with Entry Consistency for Distributed Memory Multiprocessors. Technical Report CMU-CS-91-170, School of Computer Science, Carnegie-Mellon University, September 1991.
- [Bershad *et al.* 1993] Bershad, B. N., Zekauskas, M. J., and Sawdon, W. A. The Midway Distributed Shared Memory System. In *Proc. of the 38th IEEE Int'l Computer Conf. (COMPCON Spring'93)*, pages 528–537, February 1993.
- [Bitar 1992] Bitar, P. The Weakest Memory-Access Order. *Journal of Parallel and Distributed Computing*, 15(4):305–331, August 1992.
- [Borrmann and Herdieckerhoff 1990] Borrmann, L. and Herdieckerhoff, M. A Coherency Model for Virtually Shared Memory. In *Proc. of the 1990 Int'l Conf. on Parallel Processing (ICPP'90)*, volume II, pages 252–257, August 1990.
- [Borrmann and Istavrinos 1991] Borrmann, L. and Istavrinos, P. Store Coherency in a Parallel Distributed-Memory Machine. In *Proc. of the 2nd European Distributed Memory Computing Conf. (EDMCC2)*, pages 32–41, April 1991.
- [Carter 1993] Carter, J. B. Efficient Distributed Shared Memory Based on Multi-Protocol Release Consistency. PhD thesis, Department of Computer Science, Rice University, September 1993.
- [Carter *et al.* 1995] Carter, J. B., Bennett, J. K., and Zwaenepoel, W. Techniques for Reducing Consistency-Related Communication in Distributed Shared Memory Systems. *ACM Trans. on Computer Systems*, 13(3):205–243, August 1995.

- [Chin and McColl 1994] Chin, A. and McColl, W. F. Virtual Shared Memory: Algorithms and Complexity. *Information and Computation*, 113(2):199–219, September 1994.
- [Cholvi-Juan and Bernabeu-Auban 1994] Cholvi-Juan, V. and Bernabeu-Auban, J. M. A Memory Management System that Provides N-Mixed Coherency. In *Proc. of the IEEE/USP Int'l Workshop on High Performance Computing*, March 1994.
- [Dubois *et al.* 1986] Dubois, M., Scheurich, C., and Briggs, F. A. Memory Access Buffering in Multiprocessors. In *Proc. of the 13th Annual Int'l Symp. on Computer Architecture (ISCA'86)*, pages 434–442, June 1986.
- [Dubois *et al.* 1988] Dubois, M., Scheurich, C., and Briggs, F. Synchronization, Coherence and Event Ordering in Multiprocessors. *IEEE Computer*, 21(2):9–21, February 1988.
- [Dubois and Scheurich 1990] Dubois, M. and Scheurich, C. Memory Access Dependencies in Shared-Memory Multiprocessors. *IEEE Trans. on Software Engineering*, 16(6):660–673, June 1990.
- [Dubois *et al.* 1991] Dubois, M., Wang, J.-C., Barroso, L. A., Lee, K., and Chen, Y.-S. Delayed Consistency and Its Effects on the Miss Rate of Parallel Programs. In *Proc. of Supercomputing'91*, pages 197–206, November 1991.
- [Dubois 1992] Dubois, M. Delayed Consistency. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 207–218. Kluwer Academic Publishers, 1992.
- [Fisler and Girault 1998] Fisler, K. and Girault, C. Modelling and Model Checking a Distributed Shared Memory Consistency Protocol. In *Proc. of the 19th Int'l Conf. on Application and Theory of Petri Nets 1998 (ICATPN'98)*, pages 84–103, June 1998.
- [Fortune and Wyllie 1978] Fortune, S. and Wyllie, J. Parallelism in Random Access Memories. In *Proc. of the 10th ACM Symp. on the Theory of Computing*, pages 114–118, May 1978.
- [Friedman 1994] Friedman, R. Consistency Conditions for Distributed Shared Memories. PhD thesis, Computer Science Department, Technion-Israel Institute of Technology, June 1994.
- [Friedman 1995a] Friedman, R. Using Virtual Synchrony to Develop Efficient Fault Tolerant Distributed Shared Memories. Technical Report TR-95-1506, Dept. of Computer Science, Cornell University, March 1995.
- [Friedman 1995b] Friedman, R. Implementing Hybrid Consistency with High-Level Synchronization Operations. *Distributed Computing*, 9(3):119–129, December 1995.
- [Frigo 1998] Frigo, M. The Weakest Reasonable Memory. Master's thesis, Department of Electrical Engineering and Computer Science, MIT, 1998.
- [Frigo and Luncangco 1998] Frigo, M. and Luncangco, V. Computation-Centric Memory Models. In *Proc. of the 10th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'98)*, pages 240–249, June 1998.
- [Fu and Tzeng 1997] Fu, S. S. and Tzeng, N.-F. Aggressive Release Consistency for Software Distributed Shared Memory. In *Proc. of the 17th Int'l Conf. on Distributed Computing Systems (ICDCS-17)*, pages 288–295, May 1997.
- [Gao and Sarkar 1993] Gao, G. R. and Sarkar, V. Location Consistency: Stepping Beyond the Barriers of Memory Coherence and Serializability. Technical Report ACAPS Technical Memo 78, School of Computer Science, McGill University, December 1993.
- [Gao and Sarkar 1995] Gao, G. R. and Sarkar, V. Location Consistency: Stepping Beyond the Memory Coherence Barrier. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, August 1995.

- [Gharachorloo *et al.* 1990] Gharachorloo, K., Lenoski, D. E., Laudon, J., Gibbons, P., Gupta, A., and Hennessy, J. L. Memory Consistency and Event Ordering in Scalable Shared-Memory Multiprocessors. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 15–26, May 1990.
- [Gharachorloo and Gibbons 1991] Gharachorloo, K. and Gibbons, P. Detecting Violations of Sequential Consistency. In *Proc. of the 3rd Annual ACM Symp. on Parallel Algorithms and Architectures (SPAA'91)*, July 1991.
- [Gharachorloo *et al.* 1991] Gharachorloo, K., Gupta, A., and Hennessy, J. L. Two Techniques to Enhance the Performance of Memory Consistency Models. In *Proc. of the 1991 Int'l Conf. on Parallel Processing (ICPP'91)*, volume I, pages 355–364, August 1991.
- [Gharachorloo *et al.* 1992] Gharachorloo, K., Adve, S. V., Gupta, A., Hennessy, J. L., and Hill, M. D. Programming for Different Memory Consistency Models. *Journal of Parallel and Distributed Computing*, 15(4):399–407, August 1992.
- [Gharachorloo *et al.* 1993a] Gharachorloo, K., Gupta, A., and Hennessy, J. L. Revision to “Memory Consistency and Event Ordering in Scalable Shared-Memory Multiprocessors”. Technical Report CSL-TR-93-568, Computer Systems Laboratory, Stanford University, April 1993.
- [Gharachorloo *et al.* 1993b] Gharachorloo, K., Adve, S. V., Gupta, A., Hennessy, J. L., and Hill, M. D. Specifying System Requirements for Memory Consistency Models. Technical Report CSL-TR-93-594, Computer Systems Laboratory, Stanford University, December 1993.
- [Gharachorloo 1995] Gharachorloo, K. Memory Consistency Models for Shared-Memory Multiprocessors. PhD thesis, Stanford University, 1995.
- [Gibbons 1989] Gibbons, P. B. A More Practical PRAM Model. In *Proc. of the 1st ACM Symp. on Parallel Algorithms and Architectures (SPAA'89)*, pages 158–168, June 1989.
- [Gibbons *et al.* 1991] Gibbons, P. B., Merritt, M., and Gharachorloo, K. Proving Sequential Consistency of High-Performance Shared Memories (Extended Abstract). In *Proc. of the 3rd Annual ACM Symp. on Parallel Algorithms and Architectures (SPAA'91)*, pages 292–303, July 1991.
- [Gibbons and Korach 1992] Gibbons, P. B. and Korach, E. The Complexity of Sequential Consistency. In *Proc. of the Fourth IEEE Symp. on Parallel and Distributed Processing*, pages 317–325, December 1992.
- [Gibbons 1993] Gibbons, A. M. An Introduction to Distributed Memory Models of Computation. In Gibbons, A. M. and Spirakis, P. G., editors, *Lectures on Parallel Computation*, pages 197–226. Cambridge University Press, Cambridge, U.K., 1993.
- [Goodman 1989] Goodman, J. R. Cache Consistency and Sequential Consistency. Technical Report 61, IEEE Scalable Coherence Interface Working Group, March 1989.
- [Heddaya and Sinha 1992] Heddaya, A. and Sinha, H. Coherence, Non-coherence and Local Consistency in Distributed Shared Memory for Parallel Computing. Technical Report BU-CS-92-004, Computer Science Department, Boston University, May 1992.
- [Herlihy and Wing 1990] Herlihy, M. P. and Wing, J. M. Linearizability: A Correctness Condition for Concurrent Objects. *ACM Trans. on Programming Languages and Systems*, 12(3):463–492, July 1990.
- [Higham *et al.* 1997] Higham, L., Kawash, J., and Verwaal, N. Defining and Comparing Memory Consistency Models. In *Proc. of the 10th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-97)*, pages 349–356, October 1997.
- [Higham and Kawash 1997] Higham, L. and Kawash, J. Critical Sections and Producer/Consumer Queues in Weak Memory Systems. In *Proc. of the 3rd Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'97)*, pages 56–63, December 1997.

- [Higham *et al.* 1998a] Higham, L., Kawash, J., and Verwaal, N. Weak Memory Consistency Models Part I: Definitions and Comparisons. Technical Report 98/612/03, Computer Science Department, University of Calgary, January 1998.
- [Higham *et al.* 1998b] Higham, L., Kawash, J., and Verwaal, N. Weak Memory Consistency Models Part I: Definitions and Comparisons. Technical Report 98/613/04, Computer Science Department, University of Calgary, January 1998.
- [Higham and Kawash 1998] Higham, L. and Kawash, J. Java: Memory Consistency and Processor Coordination. In *Proc. of the 12th Int'l. Symp. on Distributed Computing (DISC98)*, pages 201–215, September 1998.
- [Hu *et al.* 1998] Hu, W., Shi, W., and Tang, Z. A Framework of Memory Consistency Models. *Journal of Computer Science and Technology*, 13(2):110–124, March 1998.
- [Hutto and Ahamad 1990] Hutto, P. W. and Ahamad, M. Slow Memory: Weakening Consistency to Enhance Concurrency in Distributed Shared Memories. In *Proc. of the 10th Int'l Conf. on Distributed Computing Systems (ICDCS-10)*, pages 302–311, May 1990.
- [Ibel *et al.* 1998] Ibel, M., Schmitt, M., Schauser, K., and Acharya, A. An Efficient Global Address Space Model with SCI. In *Proc. of the European Multimedia, Multiprocessor Systems and Electronic Commerce Conference (SCI Europe '98)*, September 1998.
- [Iftode *et al.* 1996] Iftode, L., Singh, J. P., and Li, K. Scope Consistency: A Bridge between Release Consistency and Entry Consistency. In *Proc. of the 8th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA '96)*, pages 277–287, June 1996.
- [Iftode *et al.* 1998] Iftode, L., Singh, J. P., and Li, K. Scope Consistency: A Bridge between Release Consistency and Entry Consistency. *Theory of Computing Systems*, 31(4):451–473, July/August 1998.
- [Iftode 1998] Iftode, L. Home-based Shared Virtual Memory. PhD thesis, Dept. of Computer Science, Princeton University, June 1998.
- [Istavrinou and Borrmann 1990] Istavrinou, P. and Borrmann, L. A Process and Memory Model for Parallel Distributed-Memory Machine. In *Proc. of the 1st Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'90)*, September 1990.
- [James and Singh 1995] James, J. and Singh, A. K. Complete Implementations for Shared Memory Consistency Conditions. In *Proc. of the 14th Annual ACM Symp. on Principles of Distributed Computing (PODC'95)*, page 273, August 1995.
- [James and Singh 1997] James, J. and Singh, A. K. Fault Tolerance Bounds of Memory Consistency. In *Proc. of the 16th Annual ACM Symp. on Principles of Distributed Computing (PODC'97)*, page 285, August 1997.
- [Kawash and Higham 1996] Kawash, J. and Higham, L. Memory Consistency Models of Bus-Based Multiprocessors. Technical Report 96/594/14, Computer Science Department, University of Calgary, October 1996.
- [Kazimierzak and Andersen 1995] Kazimierzak, C. K. and Andersen, B. Object Consistency: A New Model for Distributed Memory Systems. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (WOOOS'95)*, pages 33–36, August 1995.
- [Keleher *et al.* 1992] Keleher, P., Cox, A. L., and Zwaenepoel, W. Lazy Release Consistency for Software Distributed Shared Memory. In *Proc. of the 19th Annual Int'l Symp. on Computer Architecture (ISCA'92)*, pages 13–21, May 1992.
- [Keleher 1994] Keleher, P. Lazy Release Consistency for Distributed Shared Memory. PhD thesis, Department of Computer Science, Rice University, December 1994.

- [Kindler *et al.* 1996] Kindler, E., Listl, A., and Walter, R. A Specification Method for Transaction Models with Data Replication. Technical Report 56, Humboldt-Universität zu Berlin, March 1996.
- [Kindler 1998] Kindler, E. The Interplay of Transaction Models and Memory Models. In Ozsu, T., Dogac, A., and Ulusoy, O., editors, *Integrated Design and Process Technology*, volume IDPT-Vol. 2, pages 39–46. Society for Design and Process Science, July 1998.
- [Koch *et al.* 1994] Koch, P. T., Fowler, R. J., and Jul, E. B. Message-Driven Relaxed Consistency in a Software Distributed Shared Memory. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 75–85, November 1994.
- [Kohli *et al.* 1993] Kohli, P., Neiger, G., and Ahamad, M. A Characterization of Scalable Shared Memories. In *Proc. of the 1993 Int'l Conf. on Parallel Processing (ICPP'93)*, August 1993.
- [Kontothanassis and Scott 1995a] Kontothanassis, L. I. and Scott, M. L. Software Cache Coherence for Large Scale Multiprocessors. In *Proc. of the 1st IEEE Symp. on High-Performance Computer Architecture (HPCA-1)*, pages 286–295, January 1995.
- [Kontothanassis and Scott 1995b] Kontothanassis, L. I. and Scott, M. L. High Performance Software Coherence for Current and Future Architectures. *Journal of Parallel and Distributed Computing*, 29(2):179–195, September 1995.
- [Kontothanassis *et al.* 1995] Kontothanassis, L. I., Scott, M. L., and Bianchini, R. Lazy Release Consistency for Hardware-Coherent Multiprocessors. In *Proc. of Supercomputing'95*, December 1995.
- [Kontothanassis and Scott 1995] Kontothanassis, L. I. and Scott, M. L. Memory Models. In Zomaya, A., editor, *The Parallel and Distributed Computing Handbook*, chapter 24, pages 699–722. McGraw-Hill, Inc., 1995.
- [Krishnamoorthy and Choudhary 1994] Krishnamoorthy, S. and Choudhary, A. A Scalable Distributed Shared Memory Architecture. *Journal of Parallel and Distributed Computing*, 22(3):547–554, September 1994.
- [Lai and Lei 1994] Lai, A. I.-C. and Lei, C.-L. The Formalization and Hierarchy of Memory Consistency Models. In *Proc. of the 1994 Int'l Computer Symp.*, pages 1060–1066, December 1994.
- [Lamport 1979] Lamport, L. How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs. *IEEE Transactions on Computers*, C-28(9):690–691, September 1979.
- [Larus *et al.* 1993] Larus, J. R., Chandra, S., and Wood, D. A. CICO: A Practical Shared-Memory Programming Performance Model. In Ferrente and Hey, editors, *Portability and Performance for Parallel Processors*. John Wiley & Sons, Ltd., 1993.
- [Li and Girard 1999] Li, H. F. and Girard, G. A Hierarchy of View Consistencies and Exact Implementations. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Li *et al.* 1997] Li, Q., Ji, H., and Xie, L. Group Consistency Model which Separates the Intra-Group Consistency Maintenance from the Inter-Group Consistency Maintenance in Large Scale DSM Systems. *ACM Operating Systems Review*, 31(2):23–35, April 1997.
- [Lindemann and Schoen 1995] Lindemann, C. and Schoen, F. Modeling Relaxed Memory Consistency Protocols. In *Proc. of the 8th Int'l Conf. on Modeling Techniques and Tools for Computer Performance Evaluation (Performance Tools '95)*, pages 385–400, September 1995.
- [Lipton and Sandberg 1988] Lipton, R. J. and Sandberg, J. S. PRAM: A Scalable Shared Memory. Technical Report CS-TR-180-88, Dept. of Computer Science, Princeton University, September 1988.
- [Lipton and Serpanos 1990] Lipton, R. J. and Serpanos, D. N. Uniform-Cost Communications in Scalable Multiprocessors. In *Proc. of the 1990 Int'l Conf. on Parallel Processing (ICPP'90)*, pages 429–432, August 1990.

- [Luchangco 1997] Luchangco, V. Precedence-Based Memory Models. In *Proc. of the 11th Int'l Workshop on Distributed Algorithms (WDAG'97)*, pages 215–229, September 1997.
- [Maxim 1995] Maxim, G. Weak Consistency Distributed Shared Memory Design. Master's thesis, Computer Science Department, Technion–Israel Institute of Technology, November 1995.
- [Melo 1997] Melo, A. Multiple Memory Consistency Models in a DSM Programming Environment. In *Proc. of the Int'l Conf. on Principles of Distributed Systems (OPODIS'97)*, pages 249–260, December 1997.
- [Melo 1999] Melo, A. Defining Uniform and Hybrid Memory Consistency Models on a Unified Framework. In *Proc. of the 32st Hawaii Int'l Conf. on System Sciences (HICSS-32) CD-ROM*, January 1999.
- [Merali 1996] Merali, S. Designing and Implementing Memory Consistency Models for Shared-Memory Multiprocessors. Master's thesis, School of Computer Science, McGill University, April 1996.
- [Misra 1986] Misra, J. Axioms for Memory Access in Asynchronous Hardware Systems. *ACM Transactions on Programming Languages and Systems*, 8(1):142–153, January 1986.
- [Mittal and Garg 1998] Mittal, N. and Garg, V. K. Consistency Conditions for Multi-Object Distributed Operations. In *Proc. of the 18th Int'l Conf. on Distributed Computing Systems (ICDCS-18)*, pages 582–589, May 1998.
- [Mizuno *et al.* 1992] Mizuno, M., Singh, G., Raynal, M., and Neilsen, M. L. Communication Efficient Distributed Shared Memories. Technical Report PI-691, IRISA, France, December 1992.
- [Mizuno *et al.* 1994] Mizuno, M., Raynal, M., Singh, G., and Neilsen, M. L. An Efficient Implementation of Sequentially Consistent Distributed Shared Memories. In *Proc. of the IFIP WG10.3 Int'l Conf. on Applications of Parallel and Distributed Computing*, pages 145–154, April 1994.
- [Mizuno *et al.* 1995a] Mizuno, M., Raynal, M., and Zhou, J. Z. Sequential Consistency in Distributed Systems: Theory and Implementation. Technical Report RR-2437, INRIA, France, March 1995.
- [Mizuno *et al.* 1995b] Mizuno, M., Raynal, M., and Zhou, J. Z. Sequential Consistency in Distributed Systems. In Birman, K., Mattern, F., and Schiper, A., editors, *Proc. of the Int'l Workshop on Theory and Practice in Distributed Systems*, number 938 in Lecture Notes in Computer Science, pages 224–241. Springer-Verlag, July 1995.
- [Mosberger 1993] Mosberger, D. Memory Consistency Models. *ACM Operating Systems Review*, 27(1):18–26, January 1993.
- [Navarro and Campos 1995] Navarro, J. E. and Campos, A. E. Coherent Casual Consistency in Distributed Shared Memory. In *Proc. of the 15th Int'l Conf. of the Chilean Computer Science Society*, pages 328–338, October 1995.
- [Nebro *et al.* 1997] Nebro, A. J., Pimentel, E., and Troya, J. M. Integrating an Entry Consistency Memory Model and Concurrent Object-Oriented Programming. In *Proc. of the Third Int'l Euro-Par Conf.*, pages 567–571, August 1997.
- [Petersen and Li 1993] Petersen, K. and Li, K. Cache Coherence for Shared Memory Multiprocessors Based on Virtual Memory Support. In *Proc. of the 7th Int'l Parallel Processing Symp. (IPPS'93)*, pages 49–55, April 1993.
- [Petry 1997] Petry, H. Comparison of SC-Derived Memory Models and Location Consistency on Shared Memory Architectures. Master's thesis, School of Computer Science, McGill University, July 1997.
- [Piquer 1996] Piquer, J. M. Distributed Shared Memory Based on Group Large Casuality. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 532–537, August 1996.
- [Probst 1994] Probst, D. K. Programming, Compiling and Executing Partially-Ordered Instruction Streams on Scalable Shared-Memory Multiprocessors. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 584–593, January 1994.

- [Protic and Milutinovic 1997] Protic, J. and Milutinovic, V. Entry Consistency versus Lazy Release Consistency in DSM Systems: Analytical Comparison and a New Hybrid Solution. In *Proc. of the 6th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'97)*, pages 78–83, October 1997.
- [Ranade 1987] Ranade, A. G. How to Emulate Shared Memory. In *Proc. of the 28th IEEE Symp. on Foundations of Computer Science*, pages 185–194, 1987.
- [Ravindran and Shah 1994] Ravindran, K. and Shah, K. Causal Broadcasting and Consistency of Distributed Shared Data. In *Proc. of the 14th Int'l Conf. on Distributed Computing Systems (ICDCS-14)*, pages 40–47, June 1994.
- [Raynal and Mizuno 1993] Raynal, M. and Mizuno, M. How to find his way in the jungle of consistency criteria for distributed shared memories (or how to escape from Minos' labyrinth). In *Proc. of the 4th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'93)*, pages 340–346, 1993.
- [Raynal and Schiper 1995] Raynal, M. and Schiper, A. From Casual Consistency to Sequential Consistency in Shared Memory Systems. In *Proc. of the 15th Int'l Conf. on Foundation of Software Technology and Theoretical Computer Science*, number 1026 in Lecture Notes in Computer Science, pages 180–194, December 1995.
- [Raynal and Schiper 1996] Raynal, M. and Schiper, A. A Suite of Formal Definitions for Consistency Criteria in Shared Memories. In *Proc. of the 9th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS'96)*, pages 125–131, September 1996.
- [Raynal and Schiper 1997] Raynal, M. and Schiper, A. A Suite of Definitions for Consistency Criteria in Distributed Shared Memories. *Annales Des Telecommunications*, 52(11-12):652–661, Nov-Dec 1997.
- [Raynal and Ahamad 1998] Raynal, M. and Ahamad, M. Exploiting Write Semantics in Implementing Partially Replicated Causal Objects. In *Proc. of the 6th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'98)*, pages 157–163, January 1998.
- [Sandberg 1990] Sandberg, J. Design of the PRAM Network. Technical Report CS-TR-254-90, Dept. of Computer Science, Princeton University, April 1990.
- [Sandhu 1993] Sandhu, H. S. Consistency and Event Ordering in the Shared Regions Model. In *Proc. of CASCON'93*, pages 971–984, October 1993.
- [Sandhu 1995] Sandhu, H. S. Shared Regions: A Strategy for Efficient Cache Management in Share-Memory Multiprocessors. PhD thesis, Graduate Department of Computer Science, University of Toronto, July 1995.
- [Sandhu 1999] Sandhu, H. S. A Extensible Framework for Coherence in Distributed Shared Data Systems. In *Proc. of the Int'l Sym. on Parallel Architectures, Algorithms and Networks*, 1999.
- [Saulsbury 1995] Saulsbury, A. Simple Delayed Release Consistency. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Scheurich and Dubois 1987] Scheurich, C. and Dubois, M. Correct Memory Operation of Cache-Based Multiprocessors. In *Proc. of the 14th Annual Int'l Symp. on Computer Architecture (ISCA'87)*, pages 234–243, June 1987.
- [Shah *et al.* 1995] Shah, G., Ramachandran, U., Sivasubramaniam, A., and Yanasak, I. Architectural Mechanisms for Explicit Communication in Shared Memory Multiprocessors. In *Proc. of Supercomputing'95*, December 1995.
- [Sindhu *et al.* 1992] Sindhu, P. S., Frailong, J.-M., and Cekleov, M. Formal Specification of Memory Models. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 25–41. Kluwer Academic Publishers, 1992.

- [Sinha 1993] Sinha, H. S. Non-Coherent Distributed Shared Memory for Parallel Computing. PhD thesis, Department of Computer Science, Boston University, May 1993.
- [Takata *et al.* 1998] Takata, S., Taguchi, K., Jou, K., and Fukuda, A. Specification and Verification of Memory Consistency Models for Shared-Memory Multiprocessor Systems. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 923–930, July 1998.
- [Torres-Rojas *et al.* 1999] Torres-Rojas, F. J., Ahamad, M., and Raynal, M. Timed Consistency for Shared Distributed Objects. In *Proc. of the 18th Annual ACM Symp. on Principles of Distributed Computing (PODC'99)*, May 1999.
- [Vinter *et al.* 1999] Vinter, B., Anshus, O. J., and Larsen, T. Data Distribution Models for a Structured Distributed Shared Memory System. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, June 1999.
- [Wang and Chang 1994a] Wang, H.-H. and Chang, R.-C. A Distributed Shared Memory System with Self-Adjusting Coherence Scheme. *Parallel Computing*, 20(7):1007–1025, July 1994.
- [Wang and Chang 1994b] Wang, H.-H. and Chang, R.-C. A Hybrid Coherence Scheme for Software Distributed Shared Memory. *Int'l Journal of High Speed Computing*, 6(4):519–536, December 1994.
- [Zambonelli *et al.* 1995] Zambonelli, F., Corradi, A., and Leonardi, L. A Scalable Tuple Space Model for Structured Parallel Programming. In *Proc. of the 1995 2nd Int'l Conf. on Programming Models for Massively Parallel Computers*, October 1995.
- [Zhou *et al.* 1993] Zhou, J. Z., Mizuno, M., and Singh, G. A Sequentially Consistent Distributed Shared Memory. In *Proc. of the 5th Int'l Conf. on Computing and Information (ICCI'93)*, pages 165–169, May 1993.
- [Zucker 1992] Zucker, R. N. Relaxed Consistency and Synchronization in Parallel Processors. PhD thesis, Department of Computer Science and Engineering, University of Washington, December 1992.

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- [Al-Khoury *et al.* 1997] Al-Khoury, A. N. M., Yamazaki, T., Yonezawa, N., Yamagiwa, S., Kulkasem, P., Ono, M., and Wada, K. Fine-grain Update Control Protocol for a Distributed Shared Memory System. In *Proc. of the 1997 IEEE Pacific Rim Conf. on Fault Tolerant Systems (PRFTS'97)*, volume 1, pages 125–129, August 1997.
- [Amza *et al.* 1997] Amza, C., Cox, A. L., Dwarkadas, S., and Zwaenepoel, W. Software DSM Protocols that Adapt between Single Writer and Multiple Writer. In *Proc. of the 3rd IEEE Symp. on High-Performance Computer Architecture (HPCA-3)*, pages 261–271, February 1997.
- [Amza 1997] Amza, C. Software Distributed Shared Memory Protocols that Adapt Between Single Writer and Multiple Writer. Master's thesis, Department of Computer Science, Rice University, March 1997.
- [Amza *et al.* 1999] Amza, C., Cox, A. L., Dwarkadas, S., Jin, L.-J., Rajamani, K., and Zwaenepoel, W. Adaptive Protocols for Software Distributed Shared Memory. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):467–475, March 1999.
- [Ananthanarayanan *et al.* 1992] Ananthanarayanan, R., Ahamad, M., and LeBlanc, Jr., R. J. Application Specific Coherence Control for High Performance Distributed Shared Memory. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-III)*, pages 109–128, March 1992.
- [Anderson and Baer 1993] Anderson, C. and Baer, J.-L. A Multi-Level Hierarchical Cache Coherence Protocol for Multiprocessors. In *Proc. of the 7th Int'l Parallel Processing Symp. (IPPS'93)*, pages 142–148, April 1993.

- [Anderson and Karlin 1996] Anderson, C. and Karlin, A. R. Two Adaptive Hybrid Cache Coherency Protocols. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, pages 303–313, February 1996.
- [Bellew *et al.* 1990] Bellew, M., Hsu, M., and Tam, V.-O. Update Propagation in Distributed Memory Hierarchy. In *Proc. of the 6th Int'l Conf. on Data Engineering*, February 1990.
- [Bianchini and LeBlanc 1994] Bianchini, R. and LeBlanc, T. J. Eager Combining: A Coherency Protocol for Increasing Effective Network and Memory Bandwidth in Shared-Memory Multiprocessors. In *Proc. of the Sixth IEEE Symp. on Parallel and Distributed Processing*, pages 204–213, October 1994.
- [Bilir *et al.* 1999] Bilir, E., Dickson, R., Hu, Y., Plakal, M., Sorin, D., Hill, M., and Wood, D. Multicast Snooping: A New Coherence Method Using a Multicast Address Network. In *Proc. of the 26th Annual Int'l Symp. on Computer Architecture (ISCA'99)*, May 1999.
- [Bisiani *et al.* 1989] Bisiani, R., Nowatzky, A., and Ravishankar, M. Coherent Shared Memory on a Distributed Memory Machine. In *Proc. of the 1989 Int'l Conf. on Parallel Processing (ICPP'89)*, volume I, pages 133–141, August 1989.
- [Black *et al.* 1989] Black, D. L., Gupta, A., and Weber, W.-D. Competitive Management of Distributed Shared Memory. In *Proc. of the 34th IEEE Int'l Computer Conf. (COMPCON Spring'89)*, pages 184–190, February 1989.
- [Brzezinski and Wawrzyniak 1997] Brzezinski, J. and Wawrzyniak, D. Multi-Criterion Coherence Protocol for Distributed Shared Memory. In *Proc. of the 6th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'97)*, October 1997.
- [Bunjevac 1993] Bunjevac, H. Adaptive Algorithm for Distributed Shared Memory. In *Proc. of the 15th Int'l Conf. on Information Technology Interfaces (ITI'93)*, pages 254–250, June 1993.
- [Castro and Muntean 1992] Castro, H. and Muntean, T. Correct Management of Objects in PAROS: Application to Shared Memory. Technical Report WP2-IMAG-9204, IMAG-LGI, France, April 1992.
- [Censier and Feautrier 1978] Censier, L. and Feautrier, P. A New Solution to Coherence Problems in Multicache Systems. *IEEE Transactions on Computers*, C-27(12):1112–1118, December 1978.
- [Chaiken *et al.* 1990] Chaiken, D., Fields, C., Kurihara, K., and Agarwal, A. Directory-Based Cache-Coherence in Large-Scale Multiprocessors. *IEEE Computer*, 23(6):49–58, June 1990.
- [Chaiken 1990] Chaiken, D. Cache Coherence Protocols for Large-Scale Multiprocessors. Master's thesis, Department of Electrical Engineering and Computer Science, MIT, September 1990.
- [Chaiken *et al.* 1991] Chaiken, D., Kubiawicz, J., and Agarwal, A. LimitLESS Directories: A Scalable Cache Coherence Scheme. In *Proc. of the 4th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOS-IV)*, pages 224–234, April 1991.
- [Chaiken 1994] Chaiken, D. Mechanisms and Interfaces for Software-Extended Coherent Shared Memory. PhD thesis, Department of Electrical Engineering and Computer Science, MIT, September 1994.
- [Cheong and Veidenbaum 1988] Cheong, H. and Veidenbaum, A. V. A Cache Coherence Scheme with Fast Selective Invalidation. In *Proc. of the 15th Annual Int'l Symp. on Computer Architecture (ISCA'88)*, June 1988.
- [Cheung *et al.* 1999] Cheung, B. W.-L., Wang, C.-L., and Hwang, K. A Migrating-Home Protocol for Implementing Scope Consistency Model on a Cluster of Workstations. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, June 1999.
- [Cordsen and Schroder-Preikschat 1997] Cordsen, J. and Schroder-Preikschat, W. On the Coexistence of Shared-Memory and Message Passing in the Programming of Parallel Applications. In *Proc. of the High Performance Computer Networks Europe'97*, pages 718–727, April 1997.

- [Cornilleau and Gressier 1996] Cornilleau, T. and Gressier, E. A Combined-consistency Approach: Sequential & Casual-Consistency. *ACM Operating Systems Review*, 30(4):33–44, October 1996.
- [Cox and Fowler 1993] Cox, A. L. and Fowler, R. J. Adaptive Cache Coherency for Detecting Migratory Shared Data. In *Proc. of the 20th Annual Int'l Symp. on Computer Architecture (ISCA'93)*, pages 98–108, May 1993.
- [Davis, Jr. and Ramachandran 1991] Davis, Jr., M. H. and Ramachandran, U. Optical Bus Protocol for a Distributed Shared Memory Multiprocessor. In *Proc. of the SPIE—The Int'l Society for Optical Engineering*, pages 176–187, July 1991.
- [Dowd and Sivalingam 1994] Dowd, P. W. and Sivalingam, K. M. A Multi-level WDM Access Protocol for an Optically Interconnected Parallel Computer. In *Proc. of IEEE INFOCOM'94*, volume 1, pages 400–408, June 1994.
- [Dubnicki and LeBlanc 1992] Dubnicki, C. and LeBlanc, T. Adjustable Block Size Coherent Caches. In *Proc. of the 19th Annual Int'l Symp. on Computer Architecture (ISCA'92)*, pages 170–180, May 1992.
- [Falsafi *et al.* 1994] Falsafi, B., Lebeck, A. R., Reinhardt, S. K., Schoinas, I., Hill, M. D., Larus, J. R., Rogers, A., and Wood, D. A. Application-Specific Protocols for User-Level Shared Memory. In *Proc. of Supercomputing'94*, pages 380–389, November 1994.
- [Girard and Li 1999] Girard, G. and Li, H. F. Evaluation of Two Optimized Protocols for Sequential Consistency. In *Proc. of the 32st Hawaii Int'l Conf. on System Sciences (HICSS-32) CD-ROM*, January 1999.
- [Glasco *et al.* 1994] Glasco, D. B., Delagi, B. A., and Flynn, M. J. Update-Based Cache Coherence Protocols for Scalable Shared-Memory Multiprocessors. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 534–545, January 1994.
- [Gniady *et al.* 1999] Gniady, K., Falsafi, B., and Vijaykumar, T. Is SC + ILP = RC? In *Proc. of the 26th Annual Int'l Symp. on Computer Architecture (ISCA'99)*, May 1999.
- [Graham and Sui 1999] Graham, P. and Sui, Y. A Simple DSM Consistency Protocol for Nested Object Transactions. In *Proc. of the 18th Annual ACM Symp. on Principles of Distributed Computing (PODC'99)*, pages 153–162, May 1999.
- [Grahm and Stenstrom 1995a] Grahm, H. and Stenstrom, P. Towards a Software-Only Directory Protocol. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Grahm and Stenstrom 1995b] Grahm, H. and Stenstrom, P. Efficient Strategies for Software-Only Directory Protocols in Shared-Memory Multiprocessors. In *Proc. of the 22nd Annual Int'l Symp. on Computer Architecture (ISCA'95)*, pages 38–47, June 1995.
- [Grahm *et al.* 1995] Grahm, H., Stenstrom, P., and Dubois, M. Implementation and Evaluation of Update-Based Cache Protocols Under Relaxed Memory Consistency Models. *Future Generation Computer Systems*, 11(3):247–271, June 1995.
- [Griffioen *et al.* 1994] Griffioen, J., Yavatkar, R., and Finkel, R. Extending the Dimensions of Consistency: Spatial Consistency and Sequential Segment. Technical Report CS248-94, Department of Computer Science, University of Kentucky, April 1994.
- [Hagersten *et al.* 1990] Hagersten, E., Haridi, S., and Warren, D. H. D. The Data Diffusion Machine and its Data Coherency Protocols. In *Proc. of IFIP Conf. on Declarative Systems*, pages 127–148, 1990.
- [Han and Cho 1997] Han, B. and Cho, Y. A Memory Coherence Protocol using Dynamic Page State Transition in Distributed Shared Memory. In *Proc. of the 10th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-97)*, pages 344–348, October 1997.
- [Hu *et al.* 1998] Hu, W., Shi, W., and Tang, Z. A Lock-based Cache Coherence Protocol for Scope Consistency. *Journal of Computer Science and Technology*, 13(2):97–109, March 1998.

- [Irlenbusch 1993] Irlenbusch, B. Type Specific Fault Tolerating Protocols for a Distributed Object Memory—*Position Paper*. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOS'93)*, pages 203–207, December 1993.
- [Jou *et al.* 1994] Jou, C.-J., Alkhatib, H. S., and Li, Q. Coherency Protocol and Algorithm for the DICE Distributed Shared Memory. In *Proc. of the Seventh Int'l Conf. on Parallel and Distributed Systems*, pages 796–801, October 1994.
- [Keleher 1999a] Keleher, P. Symmetry and Performance in Consistency Protocols. In *Proc. of the 13th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'99)*, June 1999.
- [Keleher 1999b] Keleher, P. Update Protocols and Cluster-based Shared Memory. *Computer Communications*, 1999.
- [Kermarrec and Morin 1995] Kermarrec, A.-M. and Morin, C. Control of Data Replication for Efficiency in a Highly Available Cache Only Memory Architecture. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Kessler and Livny 1989] Kessler, R. E. and Livny, M. An Analysis of Distributed Shared Memory Algorithms. In *Proc. of the 9th Int'l Conf. on Distributed Computing Systems (ICDCS-9)*, pages 498–505, June 1989.
- [Kim and Vaidya 1995] Kim, J.-H. and Vaidya, N. H. Towards an Adaptive Distributed Shared Memory. Technical Report 95-037, Dept. of Computer Science, Texas A&M University, September 1995.
- [Kontothanassis and Scott 1995] Kontothanassis, L. I. and Scott, M. L. Efficient Shared Memory with Minimal Hardware Support. *Computer Architecture News*, 23(4):29–35, September 1995.
- [Kumar *et al.* 1994] Kumar, A., Mannava, P., and Bhuyan, L. N. Efficient and Scalable Cache Coherence Schemes for Shared Memory Hypercube Multiprocessors. In *Proc. of Supercomputing'94*, pages 498–507, November 1994.
- [Lai and Lei 1993] Lai, A. I.-C. and Lei, C.-L. A New Consistency Protocol for Distributed Shared Memory. In *Proc. of the National Computer Symp.*, pages 725–733, December 1993.
- [Lebeck and Wood 1995] Lebeck, A. R. and Wood, D. A. Dynamic Self-Invalidation: Reducing Coherence Overhead in Shared-Memory Multiprocessors. In *Proc. of the 22nd Annual Int'l Symp. on Computer Architecture (ISCA'95)*, pages 48–59, June 1995.
- [Lenoski *et al.* 1990] Lenoski, D. E., Laudon, J., Gharachorloo, K., Gupta, A., and Hennessy, J. L. The Directory-Based Cache Coherence Protocol for the DASH Multiprocessor. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 148–159, May 1990.
- [Leong and Agarwal 1992] Leong, H.-V. and Agarwal, D. Type-Specific Coherence Protocols for Distributed Shared Memory. In *Proc. of the 12th Int'l Conf. on Distributed Computing Systems (ICDCS-12)*, pages 434–441, June 1992.
- [Li and Hudak 1989] Li, K. and Hudak, P. Memory Coherence in Shared Virtual Memory Systems. *ACM Trans. on Computer Systems*, 7(4):321–359, November 1989.
- [Lim *et al.* 1995] Lim, K. H., Lai, A. C., Ueng, J. C., and Shieh, C. K. Design and Implementation of Lazy Release Consistency Protocol on Cohesion. In *Proc. of the 1995 Workshop on High Performance Multiprocessor Systems*, pages 184–189, July 1995.
- [Mannava 1994] Mannava, P. K. A Scalable New Cache Coherence Protocol for Hierarchical Distributed Shared Memory. Technical Report TR-94-53, Dept. of Computer Science, Texas A&M University, 1994.
- [Mannava *et al.* 1995a] Mannava, P. K., Kumar, A., and Bhuyan, L. N. A New Limited Directory Cache Coherence Scheme for Shared Memory Multiprocessors. Technical Report TR-95-14, Dept. of Computer Science, Texas A&M University, 1995.

- [Mannava *et al.* 1995b] Mannava, P. K., Kumar, A., and Bhuyan, L. N. An Efficient Implementation of Limited Directory Cache Coherence Schemes for Shared Memory Multiprocessors. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Mentre *et al.* 1999] Mentre, D., Le Metayer, D., and Priol, T. Towards Designing SVM Coherence Protocols Using High-level Specifications and Aspect-oriented Translations. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Monnerat and Bianchini 1997] Monnerat, L. R. and Bianchini, R. ADSM: A Hybrid DSM Protocol that Efficiently Adapts to Sharing Patterns. Technical Report ES-425/97, Federal University of Rio de Janeiro, COPPE Systems Engineering Computer Science Department, March 1997.
- [Mukherjee and Hill 1994] Mukherjee, S. S. and Hill, M. D. An Evaluation of Directory Protocols for Medium-Scale Shared-Memory Multiprocessors. In *Proc. of the 8th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 64–74, July 1994.
- [Nakajo *et al.* 1992] Nakajo, H., Yoshinaga, T., Miura, N. K., Wada, K., and Kaneda, Y. Ring-Connected Parallel Computer KORP Coherence Protocol for Distributed Shared-Memory. In *Proc. of the 1992 Int'l Conf. on Parallel and Distributed Systems*, pages 504–511, December 1992.
- [Neves *et al.* 1994] Neves, N., Castro, M., and Guedes, P. A Checkpoint Protocol for an Entry Consistent Shared Memory System. In *Proc. of the 13th Annual ACM Symp. on Principles of Distributed Computing (PODC'94)*, pages 121–129, August 1994.
- [Ng and Wong 1999] Ng, M. C. and Wong, W. F. Adaptive Schemes for Home-based DSM Systems. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Omran and Aboelaze 1994] Omran, R. A. and Aboelaze, M. A. An Efficient Single Copy Cache Coherence Protocol for Multiprocessors with Multistage Interconnection Networks. In *Proc. of the Scalable High-Performance Computing Conf. (SHPCC'94)*, pages 1–8, May 1994.
- [Pinkston and Ha 1995] Pinkston, T. M. and Ha, J.-H. The SPEED Cache Coherence Protocol for an Optical Multi-Access Interconnect Architecture (OMIA). In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Piscitello 1993] Piscitello, J. D. A Software Cache Coherence Protocol for Alewife. Master's thesis, Department of Electrical Engineering and Computer Science, MIT, May 1993.
- [Pong and Dubois 1993] Pong, F. and Dubois, M. The Verification of Cache Coherence Protocols. In *Proc. of the 5th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'93)*, pages 11–20, June 1993.
- [Pong *et al.* 1995] Pong, F., Nowatzky, A., Aybay, G., and Dubois, M. Verifying Distributed Directory-based Cache Coherence Protocols: S3.mp, a Case Study. In *Proc. of the First Int'l Euro-Par Conf.*, pages 287–300, August 1995.
- [Pong and Dubois 1996] Pong, F. and Dubois, M. Formal Verification of Delayed Consistency Protocols. In *Proc. of the 10th Int'l Parallel Processing Symp. (IPPS'96)*, April 1996.
- [Raynal *et al.* 1992] Raynal, M., Mizuno, M., and Neilsen, M. L. Causality Oriented Shared Memory for Distributed Systems. Technical Report RR-1680, INRIA, France, May 1992.
- [Raynaud *et al.* 1996] Raynaud, A., Zhang, Z., and Torrellas, J. Distance-Adaptive Update Protocols for Scalable Shared-Memory Multiprocessors. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Reisner and Wailes 1996] Reisner, J. and Wailes, T. S. A Cache Coherence Protocol for Optically Connected Parallel Computer Systems. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.

- [Samanta *et al.* 1998] Samanta, R., Bilas, A., Iftode, L., and Singh, J. P. Home-based SVM protocols for SMP clusters: Design and Performance. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, February 1998.
- [Sandhu *et al.* 1993] Sandhu, H. S., Gamsa, B., and Zhou, S. The Shared Region Approach to Software Cache Coherence on Multiprocessors. In *Proc. of the Fourth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'93)*, pages 229–238, July 1993.
- [Sandhu 1995] Sandhu, H. S. Algorithms for Dynamic Software Cache Coherence. *Journal of Parallel and Distributed Computing*, 29(2):142–157, September 1995.
- [Sandhu *et al.* 1998] Sandhu, H. S., Brecht, T., and Moscoso, D. Multiple Writers Entry Consistency. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 355–362, July 1998.
- [Sandhu *et al.* 1999] Sandhu, H., Brecht, T., and Moscoso, D. Multiple-Writer Entry Consistency. *Parallel and Distributed Computing Practices*, 1999.
- [Sane *et al.* 1990] Sane, A., MacGregor, K., and Campbell, R. Distributed Virtual Memory Consistency Protocols: Design and Performance. In *Proc. of the 2nd IEEE Workshop on Experimental Distributed Systems*, pages 91–96, October 1990.
- [Sarkar *et al.* 1998] Sarkar, V., Gao, G. R., and Amaral, J. N. A Cache Consistency Protocol for the Location Consistent Memory Model. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Scales and Gharachorloo 1997] Scales, D. J. and Gharachorloo, K. Design and Performance of the Shasta Distributed Shared Memory Protocol. In *Proc. of the 11th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'97)*, pages 245–252, July 1997.
- [Seidel *et al.* 1997] Seidel, C. B., Bianchini, R., and Amorim, C. L. The Affinity Entry Consistency Protocol. In *Proc. of the 1997 Int'l Conf. on Parallel Processing (ICPP'97)*, pages 208–217, August 1997.
- [Shi *et al.* 1997] Shi, W., Hu, W., and Tang, Z. An Interaction of Coherence Protocols and Memory Consistency Models in DSM Systems. *ACM Operating Systems Review*, 31(4):41–54, October 1997.
- [Sivalingam 1995] Sivalingam, K. M. Hybrid Media Access Protocols for a DSM System Based on Optical WDM Networks. In *Proc. of the Fourth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-4)*, pages 40–47, August 1995.
- [Sivalingam and Dowd 1996] Sivalingam, K. M. and Dowd, P. W. A Lightweight Media Access Protocol for a WDM-Based Distributed Shared Memory System. In *Proc. of IEEE INFOCOM'96*, volume 3, pages 946–953, March 1996.
- [Skeppstedt and Stenstrom 1994] Skeppstedt, J. and Stenstrom, P. Simple Compiler Algorithms to Reduce Ownership Overhead in Cache Coherence Protocols. In *Proc. of the 6th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVI)*, pages 286–296, October 1994.
- [Skeppstedt and Stenstrom 1995] Skeppstedt, J. and Stenstrom, P. A Compiler Algorithm that Reduces Read Latency in Ownership-Cache Coherence Protocols. In *Proc. of the Int'l Conf. on Parallel Architectures and Compilation Techniques*, June 1995.
- [Srblijic 1993] Srblijic, S. An Asynchronous Ownership Control Algorithm for Berkeley Protocol. In *Proc. of the 15th Int'l Conf. on Information Technology Interfaces (ITI'93)*, pages 287–294, June 1993.
- [Stenstrom 1990] Stenstrom, P. A Cache Consistency Protocol for Multiprocessors with Multistage Networks. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 2–14, May 1990.

- [Stenstrom *et al.* 1993] Stenstrom, P., Brorsson, M., and Sandberg, L. An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing. In *Proc. of the 20th Annual Int'l Symp. on Computer Architecture (ISCA'93)*, pages 108–118, May 1993.
- [Stumm and Lewis 1988] Stumm, M. and Lewis, D. Memory Coherence for Large-Scale Multiprocessors with Distributed Shared Memory. In *Proc. of the IFIP WG10.3 Working Conf. on Parallel Processing*, 1988.
- [Stumm and Zhou 1990] Stumm, M. and Zhou, S. Algorithms Implementing Distributed Shared Memory. *IEEE Computer*, 23(5):54–64, May 1990.
- [Takahashi *et al.* 1996] Takahashi, M., Takano, H., Kaneko, E., and Suzuki, S. A Shared-bus Control Mechanism and a Cache Coherency Protocol for High-Performance On-Chip Multiprocessor. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Tamir and Janakiraman 1991] Tamir, Y. and Janakiraman, G. Multi-Level Coherency Management for High-Performance Shared Virtual Memory Multicomputers. In *Proc. of the 10th Annual IEEE Int'l Phoenix Conf. on Computers and Communications*, pages 174–181, March 1991.
- [Tamir and Janakiraman 1992] Tamir, Y. and Janakiraman, G. Hierarchical Coherency Management for Shared Virtual Memory Multicomputers. *Journal of Parallel and Distributed Computing*, 15(4):408–419, August 1992.
- [Thapar *et al.* 1992] Thapar, M., Delagi, B. A., and Flynn, M. J. Scalable Cache Coherence for Shared Memory Multiprocessors. In *Proc. of the 1st Int'l ACPC Conference*, pages 1–12, September 1992.
- [Theel and Fleisch 1995a] Theel, O. E. and Fleisch, B. D. Design and Analysis of Highly Available and Scalable Coherence Protocols for Distributed Shared Memory Systems Using Stochastic Modeling. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 126–130, August 1995.
- [Theel and Fleisch 1995b] Theel, O. E. and Fleisch, B. D. Analysis of a Fault-Tolerant Coherence Protocol for Distributed Shared Memory under Heavy Write Loads. In *Proc. of the 1995 IEEE Pacific Rim Conf. on Fault Tolerant Systems (PRFTS'95)*, December 1995.
- [Theel and Fleisch 1996] Theel, O. E. and Fleisch, B. D. A Dynamic Coherence Protocol for Distributed Shared Memory Enforcing High Data Availability at Low Costs. *IEEE Trans. on Parallel and Distributed Systems*, 7(9):915–930, 1996.
- [Theel and Raynal 1996] Theel, O. E. and Raynal, M. Static and Dynamic Adaptation of Transactional Consistency. Technical Report RR-2999, INRIA, France, October 1996.
- [Theel and Fleisch 1996] Theel, O. E. and Fleisch, B. D. The Boundary-Restricted Coherence Protocol for Scalable and Highly Available Distributed Shared Memory Systems. *The Computer Journal*, 39(6):496–510, 1996.
- [Turk and Fleisch 1999] Turk, J. and Fleisch, B. D. DBRpc: A Highly Adaptable Protocol for Reliable DSM Systems. In *Proc. of the 19th Int'l Conf. on Distributed Computing Systems (ICDCS-19)*, May 1999.
- [Veenstra and Fowler 1994] Veenstra, J. E. and Fowler, R. J. The Prospects for On-Line Hybrid Coherency Protocols on Bus-Based Multiprocessors. Technical Report TR-490, Dept. of Computer Science, The University of Rochester, 1994.
- [Wood *et al.* 1993] Wood, D. A., Chandra, S., Falsafi, B., Hill, M. D., Larus, J. R., Lebeck, A. R., Lewis, J. C., Mukherjee, S. S., Palacharla, S., and Reinhardt, S. K. Mechanisms for Cooperative Shared Memory. In *Proc. of the 20th Annual Int'l Symp. on Computer Architecture (ISCA'93)*, pages 156–168, May 1993.
- [Xie and Han 1999] Xie, X. and Han, C. Adjusting Single-/Multiple-writer to False Sharing in Software DSMs. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, June 1999.

- [Yamazaki *et al.* 1998] Yamazaki, T., Yonezawa, N., Kulkasem, P., Yamagiwa, S., Ono, M., Al-Khoury, A. N. M., and Wada, K. SVCP: A Cache Coherency Protocol with Explicit Update Subscription. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 899–906, July 1998.
- [Zhiyi and Jiubin 1996] Zhiyi, F. and Jiubin, J. NONH: A New Cache-based Coherence Protocol for Linked List Structure DSM System and Its Performance Evaluation. *Journal of Computer Science and Technology (English Language Edition)*, 11(4):405–415, July 1996.
- [Zhou *et al.* 1994] Zhou, J., Li, X., Dai, F., and Sun, Z. Adaptive Memory Coherence Algorithms in DSVM. *Journal of Computer Science and Technology [English Language Edition]*, 9(4):365–372, October 1994.
- [Zhou *et al.* 1996] Zhou, Y., Iftode, L., and Li, K. Performance Evaluation of Two Home-Based Lazy Release Consistency Protocols for Shared Memory Virtual Memory Systems. In *Proc. of the 2nd Symp. on Operating Systems Design and Implementation (OSDI'96)*, pages 75–88, October 1996.

Hardware Implementations

- [Abramson 1982] Abramson, D. A. Computer Hardware to Support Capability Based Addressing in a Large Virtual Memory. PhD thesis, Dept. of Computer Science, Monash University, 1982.
- [Agarwal *et al.* 1990] Agarwal, A., Lim, B.-H., Kranz, D., and Kubiawicz, J. APRIL: A Processor Architecture for Multiprocessing. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 14–110, May 1990.
- [Agarwal *et al.* 1992] Agarwal, A., Chaiken, D., Johnson, K., Kranz, D., Kubiawicz, J., Kurihara, K., Lim, B.-H., Maa, G., and Nussbaum, D. The MIT Alewife Machine: A Large-Scale Distributed-Memory Multiprocessor. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 239–261. Kluwer Academic Publishers, 1992.
- [Agarwal *et al.* 1995] Agarwal, A., Bianchini, R., Chaiken, D., Johnson, K., Kranz, D., Kubiawicz, J., Lim, B.-H., Mackenzie, K., and Yeung, D. The MIT Alewife Machine: Architecture and Performance. In *Proc. of the 22nd Annual Int'l Symp. on Computer Architecture (ISCA'95)*, pages 2–13, June 1995.
- [Agarwal *et al.* 1999] Agarwal, A., Bianchini, R., Chaiken, D., Chong, F. T., Johnson, K. L., Kranz, D., Kubiawicz, J. D., Lim, B.-H., Mackenzie, K., and Yeung, D. The MIT Alewife Machine. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):430–444, March 1999.
- [Akgun and Harmanci 1994] Akgun, B. T. and Harmanci, A. E. A Digital Bus Configuration for a Multiprocessor System with Square Pyramid Structure. In *Proc. of the 7th Mediterranean Electrotechnical Conference*, volume 1, pages 332–335, April 1994.
- [Basu and Torrellas 1998] Basu, S. and Torrellas, J. Enhancing Memory Use in Simple Coma: Multiplexed Simple Coma. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, February 1998.
- [Beltrametti *et al.* 1988] Beltrametti, M., Bobey, K., and Zorbas, J. The Control Mechanism for the Myrias Parallel Computer. *ACM Computer Architecture News*, 16(4):21–30, April 1988.
- [Bennett *et al.* 1992] Bennett, J. K., Dwarkadas, S., Greenwood, J. A., and Speight, E. Willow: A Scalable Shared Memory Multiprocessor. In *Proc. of Supercomputing'92*, pages 336–345, November 1992.
- [Bisiani and Ravishankar 1990a] Bisiani, R. and Ravishankar, M. PLUS: A Distributed Shared-Memory System. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 115–124, May 1990.

- [Bisiani and Ravishankar 1990b] Bisiani, R. and Ravishankar, M. Programming the PLUS Distributed Memory System. In *Proc. of the 5th IEEE Distributed Memory Computing Conference*, April 1990.
- [Bisiani and Ravishankar 1992] Bisiani, R. and Ravishankar, M. Local-Area Memory in PLUS. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 301–311. Kluwer Academic Publishers, 1992.
- [Bisiani and Martin 1992] Bisiani, R. and Martin, O. A Distributed-Memory, High-Performance Workstation. *Future Generation Computer Systems*, 8(1–3):83–91, July 1992.
- [Blumrich *et al.* 1994a] Blumrich, M. A., Li, K., Alpert, R., Dubnicki, C., Felten, E. W., and Sandberg, J. Virtual Memory Mapped Network Interface for the SHRIMP Multicomputer. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 142–153, April 1994.
- [Blumrich *et al.* 1994b] Blumrich, M. A., Dubnicki, C., Felten, E. W., Li, K., and Mesarina, M. R. Two Virtual Memory Mapped Network Interface Designs. In *Proc. of the Hot Interconnects Symp.*, pages 134–142, August 1994.
- [Blumrich *et al.* 1995a] Blumrich, M. A., Dubnicki, C., Felten, E. W., Li, K., and Mesarina, M. R. Virtual-Memory-Mapped Network Interfaces. *IEEE Micro*, 15(2):21–28, February 1995.
- [Blumrich *et al.* 1995b] Blumrich, M. A., Dubnicki, C., Felten, E. W., and Li, K. User-Level DMA for the SHRIMP Network Interface. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Blumrich *et al.* 1996] Blumrich, M. A., Dubnicki, C., Felten, E. W., and Li, K. Protected, User-level DMA for the SHRIMP Network Interface. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Blumrich *et al.* 1998] Blumrich, M. A., Albert, R. D., Chen, Y., Clark, D. W., Damianakis, S. N., Dubnicki, C., Felten, E. W., Iftode, L., Li, K., Martonosi, M., and Shillner, R. A. Design Choices in the SHRIMP System: An Empirical Study. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Bonney *et al.* 1997a] Bonney, J., Ramanujan, R., and Thurber, K. Design of a Network Shared Memory Based Workstation Cluster. In *Proc. of the 22nd Conference on Local Computer Networks (LCN'97)*, November 1997.
- [Bonney *et al.* 1997b] Bonney, J., Ramanujan, R., Ahamad, A., Takkella, S., and Thurber, K. Distributed Hardware Support for Process Synchronization in NSM Workstation Clusters. In *Proc. of the First Int'l Workshop on Communication and Architectural Support for Network-Based Parallel Computing (CANPC'97)*, pages 145–157, February 1997.
- [Brantley *et al.* 1985] Brantley, W. C., McAuliffe, K. P., and Weiss, J. RP3 Processor-Memory Element. In *Proc. of the 1985 Int'l Conf. on Parallel Processing (ICPP'85)*, pages 772–781, August 1985.
- [Brewer and Astfalk 1997] Brewer, T. and Astfalk, G. The Evolution of HP/Convex Exemplar. In *Proc. of the 42nd IEEE Int'l Computer Conf. (COMPCON Spring '97)*, pages 81–86, February 1997.
- [Burkhardt *et al.* 1992] Burkhardt, H., Frank, S., Knobe, B., and Rothnie, J. Overview of the KSR1 Computer System. Technical Report KSR-TR-9202001, Kendall Square Research, February 1992.
- [Byrd and Flynn 1999] Byrd, G. T. and Flynn, M. K. Producer-Consumer Communication in Distributed Shared Memory Multiprocessors. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):456–466, March 1999.
- [Carter *et al.* 1994] Carter, J. B., Kuramkote, R., and Kuo, C.-C. Reducing Consistency Traffic and Cache Misses in the Avalanche Multiprocessor. Technical report, Computer Science Department, University of Utah, 1994.

- [Carter *et al.* 1995] Carter, J. B., Davis, A., Kuramkote, R., Kuo, C.-C., Stoller, L. B., and Swanson, M. Avalanche: A Communication and Memory Architecture for Scalable Parallel Computing. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Carter *et al.* 1996] Carter, J. B., Davis, A., Kuramkote, R., and Swanson, M. The Avalanche Multiprocessor: An Overview. In *Proc. of the Sixth Workshop on Scalable Shared Memory Multiprocessors*, October 1996.
- [Carter *et al.* 1999] Carter, J. B., Hsieh, W., Stroller, L., Swanson, M., Zhang, L., Brunvand, E., Davis, A., Kuo, C.-C., and Kuramkote, R. Impulse: Building a Smarter Memory Controller. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Chapin *et al.* 1995] Chapin, J., Rosenblum, M., Devine, S., Lahiri, T., Teodosiu, D., and Gupta, A. Hive: Fault Containment for Shared-Memory Multiprocessors. In *Proc. of the 15th ACM Symp. on Operating Systems Principles (SOSP-15)*, pages 12–25, December 1995.
- [Cheng *et al.* 1994] Cheng, J., Finger, U., and O'Donnell, C. A New Hardware Cache Coherence Model. In *Proc. of EUROMICRO '94*, pages 117–124, September 1994.
- [Cheriton *et al.* 1986] Cheriton, D. R., Slavenburg, G., and Boyle, P. D. Software-Controlled Caches in the VMP Multiprocessor. In *Proc. of the 13th Annual Int'l Symp. on Computer Architecture (ISCA'86)*, pages 366–374, June 1986.
- [Cheriton *et al.* 1988] Cheriton, D. R., Gupta, A., Boyle, P. D., and Goosen, H. A. The VMP Multiprocessor: Initial Experience, Refinements and Performance Evaluation. In *Proc. of the 15th Annual Int'l Symp. on Computer Architecture (ISCA'88)*, pages 410–421, June 1988.
- [Cheriton *et al.* 1989] Cheriton, D. R., Goosen, H. A., and Boyle, P. D. Multi-Level Shared Caching Techniques for Scalability in VMP-MC. In *Proc. of the 16th Annual Int'l Symp. on Computer Architecture (ISCA'89)*, pages 16–24, May 1989.
- [Cheriton and Goosen 1991] Cheriton, D. R. and Goosen, H. A. Paradigm: A Highly-Scalable Shared-Memory Multicomputer Architecture. *IEEE Computer*, 24(2):33–46, February 1991.
- [Clayton *et al.* 1992] Clayton, S., Duckworth, R. J., Michalson, W., and Wilson, A. Determining Update Latency Bounds in Galactica Net. In *Proc. of the First IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-1)*, pages 104–111, September 1992.
- [Con 1993] Convex Computer Corp. Convex Exemplar Architecture, dhw-014 edition, November 1993.
- [Cra 1993] Cray Research, Inc. CRAY T3D System Architecture Overview, hr-04033 edition, September 1993.
- [Dagenais 1995] Dagenais, M. LIGHTNING—a WDM based Distributed Shared Memory Clustering Testbed: Network and Physical Layer. In *Proc. of the Annual Meeting of Lasers and Electro-Optics Society*, volume 1, pages 195–197, October 1995.
- [Delp and Farber 1986] Delp, G. S. and Farber, D. J. MemNet: An Experiment on High-Speed Memory Mapped Network Interface. Technical Report 85-11-IR, Dept. of Electrical Engineering, University of Delaware, 1986.
- [Delp 1988] Delp, G. S. The Architecture and Implementation of MemNet: a High-Speed Shared-Memory Computer Communication Network. PhD thesis, Department of Electrical Engineering, University of Delaware, 1988.
- [Delp *et al.* 1988] Delp, G. S., Sethi, A. S., and Farber, D. J. An Analysis of MemNet: An Experiment in High-Speed Shared-Memory Local Networking. In *Proc. of the ACM Symp. on Communications Architectures, Protocols and Applications (SIGCOMM'88)*, pages 165–174, August 1988.

- [Delp *et al.* 1991] Delp, G. S., Farber, D. J., Minnich, R. G., Smith, J. M., and Tam, M. C. Memory as a Network Abstraction. *IEEE Network Magazine*, 5(4):34–41, July 1991.
- [Delp *et al.* 1994] Delp, G. S., Farber, D. J., Minnich, R. G., Smith, J. M., and Tam, M. C. Memory as a Network Abstraction. In Casavant, T. and Singhal, M., editors, *Readings in Distributed Computing Systems*, pages 409–423. IEEE Computer Society Press, 1994.
- [Ding and Bhuyan 1992] Ding, J. and Bhuyan, L. N. Cache Coherent Shared Memory Hypercube Processors. In *Proc. of the Fourth IEEE Symp. on Parallel and Distributed Processing*, pages 515–520, December 1992.
- [Dowd and Hwang 1992] Dowd, P. W. and Hwang, I.-S. Memory and Network Architecture Interaction in an Optically Interconnected Distributed Shared Memory System. *Journal of Parallel and Distributed Computing*, 25(2):144–161, March 1992.
- [Dowd *et al.* 1995] Dowd, P. W., Perreault, J. A., Chu, J., Chu, J. C., Hoffmeister, D. C., and Crouse, D. LIGHTNING: a Scalable Dynamically Reconfigurable Hierarchical WDM Network for High-performance Clustering. In *Proc. of the Fourth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-4)*, pages 220–229, August 1995.
- [Dowd 1995] Dowd, P. W. LIGHTNING: a WDM-based Distributed Shared Memory Clustering Testbed: System Architecture and Protocols. In *Proc. of the Annual Meeting of Lasers and Electro-Optics Society*, volume 1, page 184, October 1995.
- [Dowd *et al.* 1996] Dowd, P. W., Perreault, J., Chu, J., Hoffmeister, D. C., Minnich, R., Burns, D., Hady, F., Chen, Y.-J., Dagenais, M., and Stone, D. LIGHTNING Network and Systems Architecture. *Journal of Lightwave Technology*, 14(6):1371–1387, June 1996.
- [Ekanadham *et al.* 1998] Ekanadham, K., Lim, B.-H., Pattnaik, P., and Snir, M. PRISM: An Integrated Architecture for Scalable Shared Memory. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, February 1998.
- [Farkas *et al.* 1992] Farkas, K., Vranesic, Z., and Stumm, M. Cache Consistency in hierarchical-Ring-Based Multiprocessors. In *Proc. of Supercomputing'92*, pages 348–357, November 1992.
- [Felten *et al.* 1996] Felten, E. W., Alpert, R. D., Bilas, A., Blumrich, M. A., Clark, D. W., Damianakis, S. M., Dubnicki, C., Iftode, L., and Li, K. Early Experience with Message-passing on the SHRIMP Multicomputer. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 296–307, May 1996.
- [Ghose 1995] Ghose, K. SNOW: Hardware Supported Distributed Shared Memory Over a Network of Workstations. In *Proc. of the 1995 ICPP Workshop on Challenges for Parallel Processing*, pages 148–154, August 1995.
- [Gillett 1996] Gillett, R. B. Memory Channel Network for PCI. *IEEE Micro*, 16(1):12–18, February 1996.
- [Goodman and Woest 1988] Goodman, J. R. and Woest, P. J. The Wisconsin Multicube: A New Large-Scale Cache-Coherent Multiprocessor. In *Proc. of the 15th Annual Int'l Symp. on Computer Architecture (ISCA'88)*, pages 422–431, May 1988.
- [Gupta 1992] Gupta, S. Stanford DASH Multiprocessor: the Hardware and Software. In *Proc. of Parallel Architectures and Languages Europe (PARLE'92)*, pages 802–805, June 1992.
- [Gustavson 1992] Gustavson, D. B. The Scalable Coherent Interface and Related Standards Projects. *IEEE Micro*, 12(1):10–22, February 1992.
- [Gustavson 1989] Gustavson, D. S. Scalable Coherent Interface. In *Proc. of the 34th IEEE Int'l Computer Conf. (COMPCON Spring'89)*, pages 536–544, February 1989.

- [Gustavson and Kristiansen 1992] Gustavson, D. S. and Kristiansen, E. Scalable Coherent Interface: Links to the Future. In *Proc. of the 37th IEEE Int'l Computer Conf. (COMPCON Spring'92)*, pages 322–327, February 1992.
- [Hagersten 1992] Hagersten, E. Towards Scalable Cache Only Memory Architecture. PhD thesis, Royal Institute of Technology, Stockholm/Swedish Institute of Computer Science, 1992.
- [Hagersten *et al.* 1992] Hagersten, E., Landin, A., and Haridi, S. DDM—A Cache-Only Memory Architecture. *IEEE Computer*, 25(9):44–54, September 1992.
- [Hagersten *et al.* 1993] Hagersten, E., Grindal, M., Landin, A., Saulsbury, A., Werner, B., and Haridi, S. Simulating the Data Diffusion Machine. In *Proc. of Parallel Architectures and Languages Europe (PARLE'93)*, pages 24–41, June 1993.
- [Hagersten *et al.* 1994] Hagersten, E., Saulsbury, A., and Landin, A. Simple COMA Node Implementations. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 522–533, January 1994.
- [Heinrich *et al.* 1994] Heinrich, M., Kuskin, J., Ofelt, D., Heinlein, J., Baxter, J., Singh, J. P., Simoni, R., Gharachorloo, K., Nakahira, D., Horowitz, M., Gupta, A., Rosenblum, M., and Hennessy, J. L. The Performance Impact of Flexibility in the Stanford FLASH Multiprocessor. In *Proc. of the 6th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVI)*, pages 274–285, October 1994.
- [Heinrich *et al.* 1998] Heinrich, M., Verghese, B., Gharachorloo, K., Gupta, A., and Hennessy, J. L. Flexible Use of Memory for Replication/Migration in Cache-coherent DSM Multiprocessors. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Hennessy *et al.* 1999] Hennessy, J., Heinrich, M., and Gupta, A. Cache-Coherent Distributed Shared Memory: Perspectives on Its Development and Future Challenges. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):418–429, March 1999.
- [Henskens *et al.* 1990] Henskens, F. A., Rosenberg, J., and Hannaford, M. R. Stability in a Network of MONADS-PC Computers. In *Proc. of the Int'l Workshop on Computer Architectures to Support Security and Persistence of Information*, pages 246–256, 1990.
- [Henskens *et al.* 1994] Henskens, F. A., Koch, D. M., Jalili, R., and Rosenberg, J. Hardware Support for Stability in a Persistent Architecture. In *Proc. of the Sixth Int'l Workshop on Persistent Object Systems*, pages 381–393, September 1994.
- [IEEE 1993] IEEE. IEEE Std 1596–1992: IEEE Standard for Scalable Coherent Interface. IEEE, Inc., August 1993.
- [James *et al.* 1990] James, D. V., Laundrie, A. T., Gjessing, S., and Sohi, G. S. Distributed-Directory Scheme: Scalable Coherent Interface. *IEEE Computer*, 23(6):74–77, June 1990.
- [James 1994] James, D. V. The Scalable Coherent Interface: Scaling to High-Performance Systems. In *Proc. of the 39th IEEE Int'l Computer Conf. (COMPCON Spring'94)*, pages 64–71, February 1994.
- [Jamil and Lee 1995] Jamil, S. and Lee, G. Unallocated Memory Space in COMA Multiprocessors. In *Proc. of the 8th Int'l Conf. on Parallel and Distributed Computer Systems*, September 1995.
- [Johnson 1989] Johnson, E. E. The Virtual Port Memory GMMP Multiprocessor. In *Proc. of the 2nd Int'l Conf. on Computing and Information (ICCI'89)*, volume 2, pages 127–130, May 1989.
- [Johnson 1991] Johnson, E. E. A Global-memory Message Passing Multiprocessor. *Journal of Microprocessors and Microsystems*, 15(8):403–410, October 1991.

- [Jou and Enbody 1992] Jou, T.-S. and Enbody, R. A Scalable Cnoopy Cache Coherence Scheme on Distributed Shared-Memory Multiprocessors. In *Proc. of Supercomputing'92*, pages 652–660, November 1992.
- [Kong and Lee 1996] Kong, J. and Lee, G. Relaxing the Inclusion Property in Cache Only Memory Architecture. In *Proc. of the Second Int'l Euro-Par Conf.*, volume II, pages 435–444, August 1996.
- [Kudoh *et al.* 1995] Kudoh, T., Amano, H., Matsumoto, T., Hiraki, K., Yang, Y., Nishimura, K., Yoshimura, K., and Fukushima, Y. Hierarchical Bit-map Directory Schemes on the RDT Interconnection Network for a Massively Parallel Processor JUMP-1. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 186–193, August 1995.
- [Kumar *et al.* 1995] Kumar, M. J., Venkatesh, S., Kieronska, D., and Patnaik, L. M. Hierarchical Directory-based Shared Memory Architecture. *Computer Journal*, 38(3):207–216, 1995.
- [Kuo *et al.* 1998] Kuo, C.-C., Carter, J., Kuramkote, R., and Swanson, M. ASCOMA: An Adaptive Hybrid Shared Memory Architecture. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 207–216, August 1998.
- [Kuskin *et al.* 1994] Kuskin, J., Ofelt, D., Heinrich, M., Heinlein, J., Simoni, R., Gharachorloo, K., Chapin, J., Nakahira, D., Baxter, J., Horowitz, M., Gupta, A., Rosenblum, M., and Hennessy, J. L. The Stanford FLASH Multiprocessor. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 302–313, April 1994.
- [Landin and Dahlgren 1996] Landin, A. and Dahlgren, F. Bus-based COMA: Reducing Traffic in Shared-bus Multiprocessors. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Laudon and Lenoski 1997] Laudon, J. and Lenoski, D. The SGI Origin: A ccNUMA Highly Scalable Server. In *Proc. of the 24th Annual Int'l Symp. on Computer Architecture (ISCA'97)*, pages 241–251, June 1997.
- [Lawlor *et al.* 1994] Lawlor, F. D., Mott, J. M., Nguyen, T. M., and Johnson, D. W. The Shared Memory Cluster. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 129–140, April 1994.
- [Lee *et al.* 1994] Lee, G., Quattlebaum, B., and Kinney, L. Protocol Mapping for a Bus-Based COMA Multiprocessor. Technical Report DICE #4, Dept. of Electrical Engineering, University of Minnesota, March 1994.
- [Lee 1995] Lee, G. An Assessment of COMA Multiprocessors. In *Proc. of the 9th Int'l Parallel Processing Symp. (IPPS'95)*, pages 388–392, April 1995.
- [Lee and Kong 1995] Lee, G. and Kong, J. Prospects of Distributed Shared Memory for Reducing Global Traffic in Shared-Bus Microprocessors. In *Proc. of the Seventh IASTED/ISMM Int'l Conf. on Parallel and Distributed Computing and Systems*, pages 63–67, October 1995.
- [Lee and Jamil 1995] Lee, G. and Jamil, S. Memory Block Relocation in Cache-Only Memory Multiprocessors. In *Proc. of the Seventh IASTED/ISMM Int'l Conf. on Parallel and Distributed Computing and Systems*, October 1995.
- [Lee *et al.* 1996] Lee, G., Quattlebaum, B., Cho, S., and Kinney, L. Global Bus Design of a Bus-based COMA Multiprocessor DICE. In *Proc. of the Int'l Conf. on Computer Design: VLSI in Computers and Processors*, pages 231–240, October 1996.
- [Lee and Kong 1998] Lee, G. and Kong, J. Prospects of Distributed Shared Memory for Reducing Global Traffic in Shared-Bus Microprocessors. *Journal of Systems Architecture*, 44(11):867–872, 1998.

- [Lenoski *et al.* 1989] Lenoski, D. E., Laudon, J., Gharachorloo, K., Gupta, A., Hennessy, J. L., Horowitz, M., and Lam, M. Design of the Stanford DASH multiprocessor. Technical Report CSL-TR-89-403, Computer Systems Laboratory, Stanford University, December 1989.
- [Lenoski 1991] Lenoski, D. E. The Design and Analysis of DASH: A Scalable Directory-Based Multiprocessor. PhD thesis, Stanford University, 1991.
- [Lenoski *et al.* 1992a] Lenoski, D. E., Ludon, J., Weber, K. G. W.-D., Gupta, A., Hennessy, J. L., Horowitz, M., and Lam, M. S. The Stanford DASH Multiprocessor. *IEEE Computer*, 25(3):63–79, March 1992.
- [Lenoski *et al.* 1992b] Lenoski, D. E., Laudon, J., Joe, T., Nakahira, D., Stevens, L., Gupta, A., and Hennessy, J. L. The DASH Prototype: Implementation and Performance. In *Proc. of the 19th Annual Int'l Symp. on Computer Architecture (ISCA'92)*, pages 92–102, May 1992.
- [Lenoski *et al.* 1993] Lenoski, D. E., Laudon, J., Joe, T., Nakahira, D., Stevens, L., Gupta, A., and Hennessy, J. L. The DASH Prototype: Logic Overhead and Performance. *IEEE Trans. on Parallel and Distributed Systems*, 4(1):41–61, January 1993.
- [Lovett and Clapp 1996] Lovett, T. and Clapp, R. STiNG: A CC-NUMA Computer System for the Commercial Marketplace. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 308–317, May 1996.
- [Maples 1990] Maples, C. Utilizing Virtual Shared Memory in a Topology Independent, Multicomputer Environment. In *Proc. of the 2nd Annual ACM Symp. on Parallel Algorithms and Architectures*, pages 188–198, November 1990.
- [Maples and Wittie 1990] Maples, C. and Wittie, L. MERLIN: A Superglue for Multicomputer Systems. In *Proc. of the 35th IEEE Int'l Computer Conf. (COMPCON Spring'92)*, pages 73–81, February 1990.
- [Matsumoto *et al.* 1996] Matsumoto, T., Kudoh, T., Nishimura, E., Hiraki, K., Amano, H., and Tanaka, H. Distributed Shared Memory Architecture for JUMP-1 a General-Purpose MPP Prototype. In *Proc. of the 2nd Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'96)*, pages 131–137, June 1996.
- [Maxham 1994] Maxham, K. M. ASPEN: High-Performance Hardware Support for Distributed Shared-Memory. Master's thesis, Department of Computer Science, Rice University, April 1994.
- [Mori *et al.* 1993] Mori, S., Saito, H., Goshima, M., Yanagihara, M., Tanaka, T., Fraser, D., Joe, K., Nitta, H., and Tomita, S. A Distributed Shared Memory Multiprocessor: ASURA—Memory and Cache Architectures. In *Proc. of Supercomputing'93*, pages 740–749, November 1993.
- [Muller *et al.* 1996] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. Implementing the Data Diffusion Machine using Crossbar Routers. In *Proc. of the 10th Int'l Parallel Processing Symp. (IPPS'96)*, pages 152–158, April 1996.
- [Nishi *et al.* 1995] Nishi, H., Nishimura, K., Anjo, K., Kudoh, T., and Amano, H. The JUMP-1 Router Chip: A Versatile Router for Supporting a Distributed Shared Memory. In *Proc. of the 15th Annual Int'l Phoenix Conf. on Computers and Communications*, pages 158–164, March 1995.
- [Nowatzky *et al.* 1994] Nowatzky, A., Aybay, G., Browne, M., Kelly, E., Lee, D., and Parkin, M. The S3.mp Scalable Shared Memory Multiprocessor. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 144–153, January 1994.
- [Nowatzky *et al.* 1995a] Nowatzky, A., Aybay, G., Browne, M., Radke, B., and Vishin, S. Scylla: A Memory Controller with Integrated Protocol Engines for Distributed Shared Memory Support. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Nowatzky *et al.* 1995b] Nowatzky, A., Aybay, G., Browne, M., Kelly, E., Parkin, M., Radke, B., and Vishin, S. The S3.mp Scalable Shared Memory Multiprocessor. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 1–10, August 1995.

- [Nowatzky *et al.* 1995c] Nowatzky, A., Aybay, G., Browne, M., Kelly, E., Parkin, M., Radke, B., and Vishin, S. Exploiting Parallelism in Cache Coherency Protocol Engines. In *Proc. of the First Int'l Euro-Par Conf.*, pages 271–286, August 1995.
- [Ono *et al.* 1998] Ono, W., Nakajo, H., Ichikawa, A., Anjo, K., Kudoh, T., and Amano, H. Home Proxy Cache for High Performance DSM on a Workstation Cluster. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA '98)*, volume II, pages 891–898, July 1998.
- [Pfister *et al.* 1985] Pfister, G. F., Brantley, W. C., George, D. A., Harvey, S. L., Kleinfelder, W. L., McAuliffe, K. P., Melton, E. A., Norton, V. A., and Weiss, J. The IBM Research Parallel Prototype (RP3): Introduction and Architecture. In *Proc. of the 1985 Int'l Conf. on Parallel Processing (ICPP'85)*, pages 764–771, August 1985.
- [Philipson *et al.* 1983] Philipson, L., Nilsson, B., and Breidegard, B. A Communication Structure for a Multiprocessor Computer with Distributed Global Memory. In *Proc. of the 10th Annual Int'l Symp. on Computer Architecture (ISCA'83)*, pages 334–340, June 1983.
- [Pose 1989] Pose, R. D. Capability Based, Tightly Coupled Multiprocessor Hardware to Support a Persistent Global Virtual Memory. In *Proc. of the 22th Hawaii Int'l Conf. on System Sciences (HICSS-22)*, January 1989.
- [Prete *et al.* 1995] Prete, C. A., Ricciardi, L., and Prina, G. Reducing Coherence-Related Overhead in Multiprocessor Systems. In *Proc. of the 3rd EUROMICRO Workshop on Parallel and Distributed Processing (PDP'95)*, pages 444–451, January 1995.
- [Reinhardt *et al.* 1995] Reinhardt, S. K., Pfile, R. A., and Wood, D. A. T-Zero: Hardware Support for Distributed Shared Memory on a Cluster of Workstations. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Reinhardt *et al.* 1996] Reinhardt, S. K., Pfile, R. A., and Wood, D. A. Decoupled Hardware Support for Distributed Shared Memory. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 34–43, May 1996.
- [Reinhardt *et al.* 1998] Reinhardt, S. K., Pfile, R. A., and Wood, D. A. Hardware Support for Flexible Distributed Shared Memory. *IEEE Transactions on Computers*, 47(10):1056–1072, October 1998.
- [Rosenberg and Abramson 1985] Rosenberg, J. and Abramson, D. A. MONADS-PC: A Capability Based Workstation to Support Software Engineering. In *Proc. of the 18th Hawaii Int'l Conf. on System Sciences (HICSS-18)*, pages 222–230, January 1985.
- [Saulsbury *et al.* 1994a] Saulsbury, A., Carter, J. B., and Landin, A. SiCO: A Simple COMA Implementation. Technical report, Swedish Institute of Computer Science, 1994.
- [Saulsbury *et al.* 1994b] Saulsbury, A., Landin, A., and Hagersten, E. COMAs Can be Easily Built. In *Proc. of the 1994 ISCA Shared Memory Workshop*, 1994.
- [Saulsbury and Nowatzky 1995] Saulsbury, A. and Nowatzky, A. Implementing Simple COMA on S3-MP. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Talbot and Kelly 1998] Talbot, S. and Kelly, P. Using Proxies to Bound the Worst Case Performance of Simple Page Placement in CC-NUMA Systems. In *Proc. of the 12th Annual Symp. on High Performance Computing Systems and Applications (HPCS'98)*, May 1998.
- [Tam and Farber 1990] Tam, M.-C. and Farber, D. J. CapNet—An Approach to Ultra High Speed Network. In *Proc. of the IEEE Int'l Conf. on Communications (ICC'90)*, pages 955–961, April 1990.
- [Tam and Farber 1993] Tam, M.-C. and Farber, D. J. CapNet-using Gigabit Network as a High Speed Backplane. In *Proc. of the 4th IEEE Workshop on Future Trends of Distributed Computing Systems (FT-DCS'93)*, pages 380–386, September 1993.

- [Tam and Farber 1995] Tam, M.-C. and Farber, D. J. CapNet-Shared Memory Distributed Computing over Wide Area High-Speed Networks. In *Proc. of the Int'l Conf. on Computer Communications (ICCC'95)*, pages 714–719, August 1995.
- [Tanaka *et al.* 1999] Tanaka, K., Matsumoto, T., and Hiraki, K. Lightweight Hardware Distributed Shared Memory Supported by Generalized Combining. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Torrellas and Padua 1996] Torrellas, J. and Padua, D. The Illinois Aggressive Coma Multiprocessor Project (I-Acoma). In *Proc. of the 6th Symp. on the Frontiers of Massively Parallel Computing (Frontiers'96)*, October 1996.
- [Warren and Haridi 1988] Warren, D. H. D. and Haridi, S. Data Diffusion Machine—A Scalable Shared Virtual Memory Multiprocessor. In *Proc. of the Int'l Conf. on Fifth Generation Computer Systems (ICOT'88)*, pages 943–952, 1988.
- [Wilson Jr. *et al.* 1991] Wilson Jr., A. W., Probert, T. H., Lane, T., and Fleischer, B. Galactica Net: An Architecture for Distributed Shared Memory. In *Proc. of the 1991 GOMAC*, pages 513–516, November 1991.
- [Wilson Jr. *et al.* 1992] Wilson Jr., A., Teller, M. J., Probert, T. H., Le, D., and LaRowe Jr., R. P. Lynx/Galactica Net: A Distributed, Cache Coherent Multiprocessing System. In *Proc. of the 25th Hawaii Int'l Conf. on System Sciences (HICSS25)*, pages 416–426, January 1992.
- [Wilson Jr. and LaRowe Jr. 1992] Wilson Jr., A. W. and LaRowe Jr., R. P. Hiding Shared Memory Latency on the Galactica Net Distributed Shared Memory Architecture. *Journal of Parallel and Distributed Computing*, 15(4):351–367, August 1992.
- [Wilson Jr. *et al.* 1993] Wilson Jr., A. W., LaRowe Jr., R. P., and Teller, M. J. Hardware Assist for Distributed Shared Memory. In *Proc. of the 13th Int'l Conf. on Distributed Computing Systems (ICDCS-13)*, pages 246–255, May 1993.
- [Wilson Jr. *et al.* 1994] Wilson Jr., A. W., LaRowe Jr., R. P., Ionta, R. J., Valentino, R. P., Hu, B., Breton, P. R., and Lau, P. Update Propagation in the Galactica Net Distributed Shared Memory Architecture. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 18–31, April 1994.
- [Wittie *et al.* 1992] Wittie, L. D., Hermannsson, G., and Li, A. Eager Sharing for Efficient Massive Parallelism. In *Proc. of the 1992 Int'l Conf. on Parallel Processing (ICPP'92)*, pages 251–255, August 1992.
- [Zhang and Torrellas 1998] Zhang, Y. and Torrellas, J. Hardware for Speculative Run-time Parallelization in Distributed Shared-Memory Multiprocessors. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, pages 162–173, February 1998.

Software Implementations

- [Abrossimov *et al.* 1992] Abrossimov, V., Armand, F., and Ortega, M. I. A Distributed Consistency Server for the CHORUS System. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-III)*, pages 129–148, March 1992.
- [Agarwala and Das 1995] Agarwala, A. and Das, C. R. Experimenting with a Shared Virtual Memory Environment for Hypercubes. *Journal of Parallel and Distributed Computing*, 29(2):228–235, September 1995.
- [Ahamad *et al.* 1991] Ahamad, M., Hutto, P. W., and John, R. Implementing and Programming Casual Distributed Shared Memory. In *Proc. of the 11th Int'l Conf. on Distributed Computing Systems (ICDCS-11)*, pages 274–281, May 1991.

- [Almes *et al.* 1985] Almes, G. T., Black, A. P., Lazowska, E. D., and Noe, J. D. The Eden System: A Technical Review. *IEEE Trans. on Software Engineering*, 11(1):43–59, January 1985.
- [Amza *et al.* 1996] Amza, C., Cox, A. L., Dwarkadas, S., Keleher, P., Lu, H., Rajamony, R., Yu, W., and Zwaenepoel, W. TreadMarks: Shared Memory Computing on Networks of Workstations. *IEEE Computer*, 29(2):18–28, February 1996.
- [Ananthanarayanan *et al.* 1990] Ananthanarayanan, R., Menon, S., Mohindra, A., and Ramachandran, U. Integrating Distributed Shared Memory with Virtual Memory Management. Technical Report GIT-CC-90/40, College of Computing, Georgia Institute of Technology, 1990.
- [Ananthanarayanan *et al.* 1992] Ananthanarayanan, R., Menon, S., Mohindra, A., and Ramachandran, U. Experiences in Integrating Distributed Shared Memory with Virtual Memory Management. *ACM Operating Systems Review*, 26(3):4–26, July 1992.
- [Banerji *et al.* 1993] Banerji, A., Kulkarni, D., Tracey, J., Greenawalt, P., and Cohn, D. L. High-Performance Distributed Shared Memory Substrate for Workstation Clusters. In *Proc. of the Second IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-2)*, 1993.
- [Baratloo *et al.* 1995] Baratloo, A., Dasgupta, P., and Kedem, Z. M. CALYPSO: A Novel Software System for Fault-Tolerant Parallel Processing on Distributed Platforms. In *Proc. of the Fourth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-4)*, pages 122–129, August 1995.
- [Baratloo *et al.* 1996] Baratloo, A., Karaul, M., Kedem, Z. M., and Wyckoff, P. Charlotte: Metacomputing on the Web. In *Proc. of the 9th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-96)*, 1996.
- [Bennett *et al.* 1989] Bennett, J. K., Carter, J. B., and Zwaenepoel, W. Munin: Shared Memory for Distributed Memory Multiprocessors. Technical Report COMP TR89-91, Dept. of Computer Science, Rice University, April 1989.
- [Bennett *et al.* 1990] Bennett, J. K., Carter, J. B., and Zwaenepoel, W. Munin: Distributed Shared Memory Based on Type-Specific Memory Coherence. In *Proc. of the Second ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'90)*, pages 168–177, March 1990.
- [Bennett *et al.* 1991] Bennett, J. K., Carter, J. B., and Zwaenepoel, W. Munin: Distributed Shared Memory Using Multi-Protocol Release Consistency. In Karshmer, A. I. and Nehmer, J., editors, *Operating Systems of the 90s and Beyond*, number 563 in Lecture Notes in Computer Science, pages 56–60. Springer-Verlag, July 1991.
- [Blount and Butrico 1993] Blount, M. L. and Butrico, M. DSVM6K: Distributed Shared Virtual Memory on the Risc System/6000. In *Proc. of the 38th IEEE Int'l Computer Conf. (COMPCON Spring'93)*, pages 491–500, February 1993.
- [Boyer 1991] Boyer, F. A Casual Distributed Shared Memory Based on External Pagers. In *2nd USENIX Mach Symp. Proceedings*, pages 41–57, November 1991.
- [Broessler *et al.* 1988] Broessler, P., Henskens, F. A., Keedy, J. L., and Rosenberg, J. Addressing Objects in a Very Large Distributed Virtual Memory. In *Proc. of the IFIP WG10.3 Working Conf. on Distributed Processing*, pages 105–116, 1988.
- [Bryant *et al.* 1993] Bryant, B., Sears, S., Black, D., and Langerman, A. An Introduction to Mach 3.0's XMM Subsystem. Technical report, OSF Research Institute, June 1993.
- [Bryant *et al.* 1991] Bryant, R., Carini, P., Chang, Y., and Rosenberg, B. Supporting Structured Shared Virtual Memory Under Mach. In *2nd USENIX Mach Symp. Proceedings*, pages 59–76, November 1991.
- [Brzezinski *et al.* 1998] Brzezinski, J., Sobaniec, C., Szychowiak, M., and Wawrzyniak, D. JASH: A Platform for Sharing Data in Distributed Java Applications. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume III, pages 1430–1437, July 1998.

- [Cabillic *et al.* 1994] Cabillic, G., Priol, T., and Puaut, I. MYOAN: An Implementation of the KOAN Shared Virtual Memory on the Intel Paragon. Technical Report PI-812, IRISA, France, April 1994.
- [Cabillic and Puaut 1996] Cabillic, G. and Puaut, I. Stardust: An Environment for Parallel Programming on Networks of Heterogeneous Workstations. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 114–119, August 1996.
- [Cabillic and Puaut 1997] Cabillic, G. and Puaut, I. Stardust: An Environment for Parallel Programming on Networks of Heterogeneous Workstations. *Journal of Parallel and Distributed Computing*, 40(1):65–80, January 1997.
- [Cabrera-Dantart *et al.* 1994] Cabrera-Dantart, R., Demeure, I., and Meunier, P. Phosphorus: Adding Shared Memory to PVM. Presented at the First European PVM Users' Group Meeting, Rome, Italy, October 1994.
- [Cabrera-Dantart *et al.* 1995] Cabrera-Dantart, R., Demeure, I., Meunier, P., and Bartro, V. Phosphorus: a Tool for Shared Memory Management in a Distributed Environment. Technical Report 95D003, Dept. of Computer Science, ENST Paris, 1995.
- [Carriera *et al.* 1997] Carriera, J., Silva, J. G., and Langendoen, K. Efficient and Portable Parallel Programming: An Open Distributed Shared Memory Implementation. In *Proc. of the 10th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-97)*, pages 269–272, October 1997.
- [Carter *et al.* 1991] Carter, J. B., Bennett, J. K., and Zwaenepoel, W. Implementation and Performance of Munin. In *Proc. of the 13th ACM Symp. on Operating Systems Principles (SOSP-13)*, pages 152–164, October 1991.
- [Carter *et al.* 1995] Carter, J. B., Khandekar, D., and Kamb, L. Distributed Shared Memory: Where We Are and Where We Should Be Headed? In *Fifth Workshop on Hot Topics in Operating Systems (HotOS-V)*, pages 119–122, May 1995.
- [Carter 1995] Carter, J. B. Design of the Munin Distributed Shared Memory System. *Journal of Parallel and Distributed Computing*, 29(2):219–227, September 1995.
- [Castro *et al.* 1996] Castro, M., Guedes, P., Sequeira, M., and Costa, M. Efficient and Flexible Object Sharing. In *Proc. of the 1996 Int'l Conf. on Parallel Processing (ICPP'96)*, volume 1, pages 128–137, August 1996.
- [Chen and Dasgupta 1991] Chen, R. C. and Dasgupta, P. Implementing Consistency Control Mechanisms in the Clouds Distributed Operating System. In *Proc. of the 11th Int'l Conf. on Distributed Computing Systems (ICDCS-11)*, May 1991.
- [Chen and Dasgupta 1992] Chen, R. C. and Dasgupta, P. Integrating Consistency Control and Distributed Shared Memory: The Travails of an implementation. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-III)*, pages 279–295, March 1992.
- [Chen and Allan 1998] Chen, X. and Allan, V. H. MultiJav: A Distributed Shared Memory System Based on Multiple Java Virtual Machines. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 91–98, July 1998.
- [Coady *et al.* 1999] Coady, Y., Ong, J.-S., and Feeley, M. J. Using Embedded Network Processors to Implement Global Memory Management in a Workstation Cluster. In *Proc. of the Eighth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-8)*, August 1999.
- [Colohan 1997] Colohan, C. B. Distributed Shared Memory on the x86 Based Linux PC. Undergraduate Thesis Project, April 1997.
- [Cordsen 1994] Cordsen, J. Basing Virtually Shared Memory on a Family of Consistency Models. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 58–72, April 1994.

- [Cordsen *et al.* 1997] Cordsen, J., Garnatz, T., Sander, M., Gerischer, A., Gubitoso, M. D., Haack, U., and Schroder-Preikschat, W. Vote for Peace: Implementation and Performance of a Parallel Operating System. *IEEE Concurrency*, 5(2):16–27, April–June 1997.
- [Cox and Fowler 1989] Cox, A. L. and Fowler, R. J. The Implementation of a Coherent Memory Abstraction on a NUMA Multiprocessor: Experiences with PLATINUM. In *Proc. of the 12th ACM Symp. on Operating Systems Principles (SOSP-12)*, pages 32–44, December 1989.
- [Demeure *et al.* 1995] Demeure, I., Cabrera-Dantart, R., and Meunier, P. Phosphorus: A Distributed Shared Memory System on Top of PVM. In *Proc. of EUROMICRO'95*, pages 269–273, September 1995.
- [Denton and Johnson 1996] Denton, R. and Johnson, T. Distributed Shared Memory Using Reflective Memory: The LAM System. Technical Report TR96-021, Dept. of Computer and Information Science, University of Florida, July 1996.
- [Dwarkadas *et al.* 1996] Dwarkadas, S., Cox, A. L., and Zwaenepoel, W. An Integrated Compile-Time/Run-Time Software Distributed Shared Memory System. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVII)*, pages 186–197, October 1996.
- [Dwarkadas *et al.* 1999] Dwarkadas, S., Hardavellas, N., Kontothanassis, L., Nikhil, R., and Stets, R. Cashmere-VLM: Remote Memory Paging for Software Distributed Shared Memory. In *Proc. of the Second Merged Symp. IPPS/SPDP 1999*, April 1999.
- [Eskicioglu and Marsland 1998] Eskicioglu, M. R. and Marsland, T. A. Shared Memory Computing on SP2: JIAJIA Approach. In *Proc. of the IBM Centre for Advanced Studies Conf. (CASCON'98)*, pages 235–245, November 1998.
- [Eskicioglu *et al.* 1999] Eskicioglu, M. R., Marsland, T. A., Hu, W., and Shi, W. Evaluation of JIAJIA Software DSM System on High Performance Computer Architectures. In *Proc. of the 32st Hawaii Int'l Conf. on System Sciences (HICSS-32) CD-ROM*, pages 287, file: stdcr05.ps, January 1999.
- [Feeley *et al.* 1995] Feeley, M. J., Morgan, W. E., Pighin, F. H., Karlin, A. R., and Levy, H. M. Implementing Global Memory Management in a Workstation Cluster. In *Proc. of the 15th ACM Symp. on Operating Systems Principles (SOSP-15)*, pages 201–212, December 1995.
- [Fleisch 1987] Fleisch, B. D. Distributed Shared Memory in a Loosely Coupled Distributed System. In *Proc. of the ACM SIGCOMM'87 Workshop on Frontiers in Computer Communications Technology*, pages 317–327, August 1987.
- [Fleisch 1989] Fleisch, B. D. Distributed Shared Memory in a Loosely Coupled Environment. PhD thesis, Department of Computer Science, University of California in Los Angeles, 1989.
- [Fleisch and Popek 1989] Fleisch, B. D. and Popek, G. J. Mirage: A Coherent Distributed Shared Memory Design. In *Proc. of the 12th ACM Symp. on Operating Systems Principles (SOSP-12)*, pages 211–223, December 1989.
- [Fleisch *et al.* 1993] Fleisch, B. D., Hyde, R. L., and Juul, N. C. Moving Distributed Shared Memory to the Personal Computer: The MIRAGE+ Experience. Technical Report UCR-CS-93-6, Dept. of Computer Science, University of California at Riverside, June 1993.
- [Fleisch 1993] Fleisch, B. D. The Role of Distributed Shared Memory in Future Experimental Distributed Systems—*Position Statement*. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-IV)*, pages 273–278, September 1993.
- [Fleisch *et al.* 1994] Fleisch, B. D., Hyde, R. L., and Juul, N. C. MIRAGE+: A Kernel Implementation of Distributed Shared Memory on a Network of Personal Computers. *Software—Practice and Experience*, 24(10):887–909, October 1994.

- [Forin *et al.* 1989a] Forin, A., Barrera, J., Young, M., and Rashid, R. Design, Implementation, and Performance Evaluation of a Distributed Shared Memory Server for Mach. In *Proc. of the Winter 1989 USENIX Conference*, January 1989.
- [Forin *et al.* 1989b] Forin, A., Barrera, J., and Sanzi, R. The Shared Memory Server. In *Proc. of the Winter 1989 USENIX Conference*, pages 229–243, January 1989.
- [Friedman *et al.* 1996] Friedman, R., Maxim, G., Itzkovitz, A., and Schuster, A. MILLIPEDE: Easy Parallel Programming in Available Distributed Environments. In *Proc. of the Second Int'l Euro-Par Conf.*, pages 84–87, August 1996.
- [Friedman *et al.* 1997] Friedman, R., Maxim, G., Itzkovitz, A., and Schuster, A. MILLIPEDE: Easy Parallel Programming in Available Distributed Environments. *Software—Practice and Experience*, 27(8):925–965, August 1997.
- [Garbinato *et al.* 1994] Garbinato, B., Guerraoui, R., and Mazouni, K. R. Distributed Programming in GARF. In *Proc. of the Conf. on Object-Based Distributed Programming*, number 791 in Lecture Notes in Computer Science, pages 225–234. Springer-Verlag, 1994.
- [Giloi *et al.* 1991] Giloi, W. K., Hastedt, C., Schoen, F., and Schroeder-Preikschat, W. A Distributed Implementation of Shared Virtual Memory with Strong and Weak Coherence. In *Proc. of the 2nd European Distributed Memory Computing Conf. (EDMCC2)*, pages 23–31, April 1991.
- [Guedes and Castro 1993] Guedes, P. and Castro, M. Distributed Shared Object Memory. In *Proc. of the 4th Workshop on Workstation Operating Systems (WWOS-IV)*, pages 142–149, October 1993.
- [Guerraoui *et al.* 1994] Guerraoui, R., Garbinato, B., and Mazouni, K. R. The GARF Library of DSM Consistency Models. In *Proc. of the 6th ACM SIGOPS European Workshop*, September 1994.
- [Hariram and Gonsalves 1995] Hariram, R. K. and Gonsalves, T. A. A DSM-based Portable Distributed Programming Testbed. In *Proc. of the IFIP TC6 Conference 1994*, pages 215–231, January 1995.
- [Heddaya and Sinha 1993a] Heddaya, A. and Sinha, H. An Overview of Mermera: A System and Formalism for Non-coherent Distributed Parallel Memory. In *Proc. of the 26th Hawaii Int'l Conf. on System Sciences (HICSS26)*, pages 164–173, January 1993.
- [Heddaya and Sinha 1993b] Heddaya, A. and Sinha, H. An Implementation of Mermera: A Shared Memory System that Mixes Coherence with Non-coherence. Technical Report BU-CS-93-006, Computer Science Department, Boston University, June 1993.
- [Henskens 1991] Henskens, F. A. A Capability-Based Persistent Distributed Shared Memory. PhD thesis, University of Newcastle, NSW, Australia, 1991.
- [Henskens *et al.* 1993] Henskens, F. A., Brossler, P., Keedy, J. L., and Rosenberg, J. Coarse and Fine Grain Objects in a Distributed Persistent Store. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOOS'93)*, pages 116–123, December 1993.
- [Henskens and Rosenberg 1993] Henskens, F. A. and Rosenberg, J. Distributed Persistent Stores. *Journal of Microprocessors and Microsystems*, 17(3):147–159, 1993.
- [Hu *et al.* 1999a] Hu, W., Shi, W., and Tang, Z. JIAJIA: An SVM System Based on A New Cache Coherence Protocol. In *Proc. of the High-Performance Computing and Networking Europe 1999 (HPCN'99)*, pages 463–472, April 1999.
- [Hu *et al.* 1999b] Hu, W., Shi, W., and Tang, Z. Reducing System Overheads in Home-based Software DSMs. In *Proc. of the Second Merged Symp. IPPS/SPDP 1999*, pages 167–173, April 1999.
- [Hu *et al.* 1999c] Hu, W., Shi, W., and Tang, Z. Home Migration in Home-based Software DSMs. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.

- [Hu 1999] Hu, W. Reducing Message Overhead in Home-Based Software DSMs. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Hu *et al.* 1999] Hu, W., Shi, W., and Tang, Z. Adaptive Write Detection in Home-Based Software DSMs (Poster). In *Proc. of the Eighth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-8)*, August 1999.
- [Huang and Kedem 1996] Huang, S. C. and Kedem, Z. M. Supporting a Flexible Parallel Programming Model on a Network of Workstations. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 75–82, May 1996.
- [Istavrinou 1992] Istavrinos, P. Experiences with an Object-Oriented Mapper for Coherent Distributed Shared Memory. In *Proc. of the Second Int'l Workshop on Object Orientation in Operating Systems (IWOOS'92)*, pages 257–261, September 1992.
- [Itzkovitz and Schuster 1999a] Itzkovitz, A. and Schuster, A. MultiView and Millipage: Fine-Grain Sharing in Page-Based DSMs. In *Proc. of the 3rd Symp. on Operating Systems Design and Implementation (OSDI'99)*, pages 215–228, February 1999.
- [Itzkovitz and Schuster 1999b] Itzkovitz, A. and Schuster, A. Distributed Shared Memory: Bridging the Granularity Gap. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Jinzaki 1993] Jinzaki, A. A Fast Distributed Shared Virtual Memory System: NET-VMS. *Fujitsu Scientific and Technical Journal*, 29(3):286–295, Autumn 1993.
- [Johnson *et al.* 1995a] Johnson, K. L., Kaashoek, M. F., and Wallach, D. A. CRL: High-Performance All-Software Distributed Shared Memory. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Johnson *et al.* 1995b] Johnson, K. L., Kaashoek, M. F., and Wallach, D. A. CRL: High-Performance All-Software Distributed Shared Memory. In *Proc. of the 15th ACM Symp. on Operating Systems Principles (SOSP-15)*, pages 213–228, December 1995.
- [Johnson 1996] Johnson, K. L. High-Performance All-Software Distributed Shared Memory. PhD thesis, Department of Electrical Engineering and Computer Science, MIT, February 1996.
- [Johnston and Campbell 1989] Johnston, G. M. and Campbell, R. H. An Object-Oriented Implementation of Distributed Virtual Memory. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems*, pages 39–57, October 1989.
- [Keleher *et al.* 1994] Keleher, P., Dwarkadas, S., Cox, A. L., and Zwaenepoel, W. TreadMarks: Distributed Shared Memory on Standard Workstations and Operating Systems. In *Proc. of the Winter 1994 USENIX Conference*, pages 115–131, January 1994.
- [Kermarrec and Pautet 1993] Kermarrec, Y. and Pautet, L. A Distributed Shared Virtual Memory for Ada 83 and Ada 9X Applications. In *Proc. of the TRI-Ada'93 Conf.*, pages 242–251, September 1993.
- [Kermarrec and Pautet 1994] Kermarrec, Y. and Pautet, L. Integrating Page Replacement in a Distributed Shared Virtual Memory. In *Proc. of the 14th Int'l Conf. on Distributed Computing Systems (ICDCS-14)*, pages 355–362, June 1994.
- [Khandekar 1996] Khandekar, D. R. QUARKS: Distributed shared Memory as a Building Block for Complex Parallel and Distributed Systems. Master's thesis, Department of Computer Science, The University of Utah, March 1996.
- [Koch *et al.* 1999] Koch, P. T., Hansen, J. S., Cecchet, E., and Ronsset de Pina, X. SciOS: An SCI-based Software Distributed Shared Memory. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.

- [Koussih *et al.* 1999] Koussih, S., Acharya, A., and Setia, S. Dodo: A User-Level System for Exploiting Idle Memory in Workstation Clusters. In *Proc. of the Eighth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-8)*, August 1999.
- [Lahjomri and Priol 1992] Lahjomri, Z. and Priol, T. KOAN: A Shared Virtual Memory for iPSC/2 Hypercube. In *Proc. of the 2nd Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'92)*, pages 441–452, September 1992.
- [Lazowska *et al.* 1981] Lazowska, E. D., Levy, H. M., Almes, G. T., Fisher, M. J., Fowler, R. J., and Vestal, S. C. The Architecture of the Eden System. In *Proc. of the 8th ACM Symp. on Operating Systems Principles (SOSP-8)*, pages 148–159, December 1981.
- [Lefevre 1997] Lefevre, L. Parallel Programming on top of DSM System. An Experimental Study. *Parallel Computing*, 23(1–2):235–249, April 1997.
- [Leach *et al.* 1983] Leach, P. J., Levine, P. H., Douros, B. P., Hamilton, J., Nelson, D. L., and Stumpf, B. L. The Architecture of an Integrated Local Network. *IEEE Journal on Selected Areas in Communications*, SAC-1(5):842–856, November 1983.
- [Li 1988] Li, K. IVY: A Shared Virtual Memory System for Parallel Computing. In *Proc. of the 1988 Int'l Conf. on Parallel Processing (ICPP'88)*, volume II, pages 94–101, August 1988.
- [Li and Schaefer 1989a] Li, K. and Schaefer, R. Shiva: An Operating System Transforming A Hypercube into a Shared-Memory Machine. Technical Report CS-TR-217-89, Dept. of Computer Science, Princeton University, April 1989.
- [Li and Schaefer 1989b] Li, K. and Schaefer, R. A Hypercube Shared Virtual Memory System. In *Proc. of the 1989 Int'l Conf. on Parallel Processing (ICPP'89)*, volume I, pages 125–132, August 1989.
- [Liang 1994] Liang, W.-Y. Adsmith: A Structure-Based Heterogeneous Distributed Shared Memory on PVM. Master's thesis, Institute of Computer Science, National Tsing Hua University, June 1994.
- [Liang *et al.* 1996] Liang, W.-Y., King, C.-T., and Lai, F. Adsmith: An Efficient Object-Based Distributed Shared Memory on PVM. In *Proc. of the 2nd Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'96)*, pages 173–179, June 1996.
- [Liang *et al.* 1997] Liang, W.-Y., King, C.-T., and Lai, F. Adsmith: An Object-Based Distributed Shared Memory System for Networks of Workstations. *IEICE Trans. on Information and Systems*, E80-D(9):899–908, September 1997.
- [Lim *et al.* 1997] Lim, B.-H., Chang, C.-C., Czajkowski, G., and von Eicken, T. Performance Implications of Communication Mechanisms in All-Software Global Address Space Systems. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 230–239, June 1997.
- [Lin *et al.* 1994] Lin, S.-H., Huang, Y.-M., and Chen, C.-H. The Design of a Distributed File Server Based on Distributed Shared Memory. In *Proc. of the 1994 Int'l Computer Symp.*, pages 1190–1196, December 1994.
- [Lin *et al.* 1995a] Lin, Y.-W., Liang, D., Yuan, S.-M., and Lai, F. Design and Implementation of Moony: A Fault Tolerant Distributed Shared Memory System. In *Proc. of the 18th Australasian Computer Science Conf. (ACSC'95)*, pages 319–328, February 1995.
- [Lin *et al.* 1995b] Lin, Y.-W., Yuan, S.-M., and Liang, D. Design and Implementation of Moony: A Fault Tolerant Distributed Shared Memory System. *Int'l Journal of Computer Systems Science and Engineering*, 10(2):111–119, April 1995.
- [Mairandres and Zeisset 1996] Mairandres, M. and Zeisset, S. Shared Virtual Memory with Integrated Monitoring Support. In *Proc. of the 1st Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'96)*, April 1996.

- [Manis *et al.* 1995] Manis, G., Voliotis, K., Lekatsas, C., Tsanakas, P., and Papakonstantinou, G. Orchid: The Design of a Parallel and Portable Software Platform for Local Area Networks. In *Proc. of the IEEE Int'l Conf. on Algorithms and Architectures for Parallel Processing*, April 1995.
- [Manoj and Govindarajan 1999] Manoj, N. P. and Govindarajan, R. CAS-DSM: A Compiler-assisted DSM. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Marino and de Campos 1999] Marino, M. D. and de Campos, G. L. A Preliminary DSM Speedup Comparison: JIAJIA x NAUTILUS. In *Proc. of the 13th Annual Int'l Symp. on High Performance Computing Systems and Applications (HPCS'99)*, June 1999.
- [Mentre *et al.* 1998] Mentre, D., , and Priol, T. NOA: A Shared Virtual Memory over a SCI Cluster. In *Proc. of the European Multimedia, Multiprocessor Systems and Electronic Commerce Conference (SCI Europe'98)*, September 1998.
- [Michel 1990] Michel, B. GOTHIC Memory Management: A Multiprocessor Shared Single Level Store. Technical Report PI-523, IRISA, France, March 1990.
- [Minnich and Farber 1989] Minnich, R. G. and Farber, D. J. The Mether System: Distributed Shared Memory for SunOS 4.0. In *Proc. of the 1989 Summer USENIX Conference*, pages 51–60, June 1989.
- [Minnich and Farber 1990] Minnich, R. G. and Farber, D. J. Reducing Host Load, Network Load, and Latency in a Distributed Shared Memory. In *Proc. of the 10th Int'l Conf. on Distributed Computing Systems (ICDCS-10)*, pages 468–475, May 1990.
- [Minnich 1991] Minnich, R. G. Mether: A Memory System for Network Multiprocessors. PhD thesis, Department of Computer and Information Sciences, University of Pennsylvania, 1991.
- [Minnich and Pryor 1993] Minnich, R. G. and Pryor, D. V. Mether: Supporting the Shared Memory Model on Computing Clusters. In *Proc. of the 38th IEEE Int'l Computer Conf. (COMPCON Spring'93)*, pages 558–567, February 1993.
- [Minnich 1993] Minnich, R. G. Mether-NFS: A Modified NFS Which Supports Virtual Shared Memory. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDM-IV)*, pages 89–107, September 1993.
- [Murray *et al.* 1993] Murray, K., Osmon, P. E., Valsamidis, A., Whitcroft, A., and Wilkinson, T. Experiences with Distributed Shared Memory. Technical Report TCU/SARC/1993/3, Systems Architecture Research Centre, City University, U.K., 1993.
- [Myr 1995] Myrias Computer Technologies, Inc. Parallel Application Management System (PAMS) V2, 1995.
- [Nanri *et al.* 1998] Nanri, T., Sato, H., and Shimasaki, M. Implementation of PVM-based Distributed Shared Memory System. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 867–874, July 1998.
- [Nikhil 1994] Nikhil, R. S. Cid: A Parallel, “Shared Memory” C for Distributed-Memory Machines. In *Proc. of the 7th Int'l Workshop on Languages and Compilers for Parallel Computing*, August 1994.
- [Niwa *et al.* 1997] Niwa, J., Inagaki, T., Matsumoto, T., and Hiraki, K. Efficient Implementation of Software Release Consistency on Asymmetric Distributed Shared Memory. In *Proc. of the 3rd Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'97)*, pages 198–201, December 1997.
- [Osmon *et al.* 1992] Osmon, P., Murray, K., Whitcroft, A., Wilkinson, T., and Williams, N. Network Shared Memory. Technical Report TCU/SARC/1992/3, Systems Architecture Research Centre, City University, U.K., 1992.

- [Parastatidis and Watson 1999] Parastatidis, S. and Watson, P. An Object-Based Software DSM for the NIP Parallel System. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Pfenning 1994] Pfenning, J.-T. Experiences with the Mether-NFS Virtual Shared Memory. In *Proc. of the Int'l Conf. and Exhibition on High-Performance Computing and Networking*, pages 316–323, April 1994.
- [Pfenning *et al.* 1995] Pfenning, J.-T., Bachem, A., and Minnich, R. Virtual Shared Memory Programming on Workstation Clusters. *Future Generation Computer Systems*, 11(4–5):387–399, August 1995.
- [Priol and Lahjomri 1992] Priol, T. and Lahjomri, Z. Experiments with Shared Virtual Memory and Message Passing on iPSC/2 hypercube. In *Proc. of the 1992 Int'l Conf. on Parallel Processing (ICPP'92)*, volume II, pages 145–148, August 1992.
- [Raghavachari and Rogers 1997] Raghavachari, M. and Rogers, A. Ace: Linguistic Mechanisms for Customizable Protocols. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 80–89, June 1997.
- [Raina 1990] Raina, S. Software Controlled Shared Virtual Memory Management on a Transputer Based Multiprocessor. In *Proc. of the 4th North American Transputer Users Group Conference (NATUG-4)*, October 1990.
- [Raina *et al.* 1990] Raina, S., Warren, D. H. D., and Cownie, J. Shared Virtual Memory on Transputers via the Data Diffusion Machine. In *Proc. of the Thirteenth Occam Users Group Conference (OUG-13)*, pages 322–330, October 1990.
- [Ramachandran *et al.* 1988] Ramachandran, U., Ahamad, M., and Khalidi, M. Y. A. Unifying Synchronization and Data Transfer in Maintaining Coherence of Distributed Shared Memory. Technical Report GIT-ICS-88/23, College of Computing, Georgia Institute of Technology, June 1988.
- [Ramachandran *et al.* 1989] Ramachandran, U., Ahamad, M., and Khalidi, M. Y. A. Coherence of Distributed Shared Memory: Unifying Synchronization and Data Transfer. In *Proc. of the 1989 Int'l Conf. on Parallel Processing (ICPP'89)*, volume II, pages 160–169, August 1989.
- [Ramachandran and Khalidi 1989a] Ramachandran, U. and Khalidi, M. Y. A. Programming with Distributed Shared Memory. In *Proc. of the 13th Annual Int'l Computer Software and Applications Conf. (COMPSAC'89)*, pages 176–183, September 1989.
- [Ramachandran and Khalidi 1989b] Ramachandran, U. and Khalidi, M. Y. A. An Implementation of Distributed Shared Memory. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems*, pages 21–38, October 1989.
- [Ramachandran and Khalidi 1991] Ramachandran, U. and Khalidi, M. Y. A. An Implementation of Distributed Shared Memory. *Software—Practice and Experience*, 21(5):443–464, May 1991.
- [Ramesh *et al.* 1997] Ramesh, S., Lakshmi, R., and Govindarajan, R. Distributed Shared Memory on IBM SP2. In *Proc. of the 1997 Int'l Conf. on Parallel and Distributed Systems (ICPDS'97)*, pages 338–345, December 1997.
- [Rochat 1990] Rochat, B. Implementation of a Multi-Cache System on a Loosely Coupled Multiprocessor. In *Proc. of the 5th Distributed Memory Computing Conference*, pages 676–681, April 1990.
- [Sato *et al.* 1997] Sato, H., Nanri, T., and Shimasaki, M. A Portable Distributed Shared Memory System on a Cluster Environment: Design and Implementation Fully in Software—*Poster Paper*. In *Proc. of the Int'l Symp. on High Performance Computing (ISHPC'97)*, pages 343–350, November 1997.
- [Saulsbury and Stiernerling 1992] Saulsbury, A. and Stiernerling, T. A DVSM Server for Meshix. Technical Report TCU/CS/1992/7, Dept. of Computer Science, City University, U.K., February 1992.

- [Scales *et al.* 1996] Scales, D. J., Gharachorloo, K., and Thekkath, C. A. Shasta: A Low Overhead, Software-Only Approach for Supporting Fine-Grain Shared Memory. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVII)*, pages 174–185, October 1996.
- [Scales and Gharachorloo 1997] Scales, D. J. and Gharachorloo, K. Shasta: a System for Supporting Fine-Grain Shared Memory across Clusters. In *Proc. of the Eighth SIAM Conference on Parallel Processing for Scientific Computing*, March 1997.
- [Scales *et al.* 1998] Scales, D. J., Gharachorloo, K., and Aggarwal, A. Fine-Grain Software Distributed Shared Memory on SMP Clusters. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, February 1998.
- [Shi *et al.* 1999] Shi, W., Hu, W., and Tang, Z. Where Does the Time Go in SVM System?—Experiences with JIAJIA. *Journal of Computer Science and Technology*, 14(3):193–205, May 1999.
- [Shi and Tang 1999] Shi, W. and Tang, Z. Dynamic Computation Scheduling for Load Balancing in Home-based Software DSMs. In *Proc. of the 4th Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'99)*, June 1999.
- [Shi *et al.* 1999a] Shi, W., Tang, Z., and Hu, W. A More Practical Loop Scheduling for Home-based Software DSMs. In *Proc. of the ACM-SIGARCH Workshop on Scheduling Algorithms for Parallel and Distributed Computing—From Theory to Practice*, June 1999.
- [Shi *et al.* 1999b] Shi, W., Hu, W., Tang, Z., and Eskicioglu, M. R. Dynamic Task Migration in Home-based Software DSM Systems. Technical Report TR990004, Institute of Computing Technology, Center of High Performance Computing, February 1999.
- [Shi *et al.* 1999c] Shi, W., Hu, W., Tang, Z., and Eskicioglu, M. R. Dynamic Task Migration in Home-based Software DSM Systems (Poster). In *Proc. of the Eighth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-8)*, August 1999.
- [Shieh *et al.* 1995] Shieh, C.-K., Lai, A.-C., Ueng, J.-C., Liang, T.-Y., Chang, T.-C., and Mac, S.-C. Cohesion: An Efficient Distributed Shared Memory System Supporting Multiple Memory Consistency Models. In *Proc. of the First Aizu Int'l Symp. on Parallel Algorithms/Architecture Synthesis*, pages 146–152, February 1995.
- [Silcock and Goscinski 1997] Silcock, J. and Goscinski, A. Update Based Distributed Shared Memory Integrated into RHODOS' Memory Management. In *Proc. of IEEE 3rd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'97)*, pages 239–252, December 1997.
- [Silcock and Goscinski 1998] Silcock, J. and Goscinski, A. The RHODOS DSM System. *Journal of Microprocessors and Microsystems*, 22(3–4):183–196, 1998.
- [Sinha *et al.* 1991a] Sinha, P. K., Ashihara, H., Shimizu, K., and Maekawa, M. Flexible Address Space Sharing Mechanisms in the GALAXY Distributed Operating System. In *Proc. of the 10th Annual IEEE Int'l Phoenix Conf. on Computers and Communications*, March 1991.
- [Sinha *et al.* 1991b] Sinha, P. K., Ashihara, H., Shimizu, K., and Maekawa, M. Flexible User-Definable Memory Coherence Scheme in Distributed Shared Memory of GALAXY. In *Proc. of the 2nd European Distributed Memory Computing Conf. (EDMCC2)*, pages 52–61, April 1991.
- [Speight 1997] Speight, W. E. Efficient Runtime Support for Cluster-Based Distributed Shared Memory Multiprocessors. PhD thesis, Department of Electrical and Computer Engineering, Rice University, July 1997.
- [Speight and Bennett 1997] Speight, W. E. and Bennett, J. K. Brazos: A Third Generation DSM System. In *Proc. of the USENIX Windows NT Workshop*, August 1997.

- [Souto and Stark 1997] Souto, R. D. and Stark, E. W. A Distributed Shared Memory Facility for FreeBSD. In *Proc. of the USENIX 1997 Annual Technical Conference*, pages 149–162, January 1997.
- [Stets *et al.* 1997] Stets, R., Dwarkadas, S., Hardavellas, N., Hunt, G., Kontothanassis, L., Parthasarathy, S., and Scott, M. Cashmere-2L: Software Coherent Shared Memory on a Clustered Remote-Write Network. In *Proc. of the 16th ACM Symp. on Operating Systems Principles (SOSP-16)*, October 1997.
- [Stiemerling *et al.* 1992] Stiemerling, T., Wilkinson, T., and Saulsbury, A. Implementing DVSM on the TOPSY Multicomputer. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-III)*, pages 263–278, March 1992.
- [Swanson *et al.* 1998] Swanson, M., Stroller, L., and Carter, J. B. Making Distributed Shared Memory Simple, Yet Efficient. In *Proc. of the 3rd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'98)*, pages 2–13, March 1998.
- [Ueng *et al.* 1999] Ueng, J. C., Shieh, C. K., and Lin, Q. C. Design and Implementation of Proteus. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Voliotis *et al.* 1997] Voliotis, K., Manis, G., Lekatsas, C., Tsanakas, P., and Papakonstantinou, G. Orchid: A Portable Platform for Parallel Programming. *Journal of Systems Architecture*, 43(6–7):459–478, April 1997.
- [Watson and Rawsthorne 1995] Watson, I. and Rawsthorne, A. Decoupled Pre-fetching for Distributed Shared Memory. In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, volume I, pages 252–261, January 1995.
- [Watson *et al.* 1995] Watson, I., Rawsthorne, A., Cumpstey, M., and Charpin, F. An Evaluation of DELTA, a Decoupled Pre-fetching Virtual Shared Memory System. In *Proc. of the Seventh IEEE Symp. on Parallel and Distributed Processing*, pages 482–487, October 1995.
- [Zhu and Wong 1999] Zhu, W. and Wong, A. K. L. Jasmine: A Pure Java Based DSM. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, July 1999.

Language Support for DSM

- [Ahuja *et al.* 1986] Ahuja, S., Carriero, N., and Gelernter, D. Linda and Friends. *IEEE Computer*, 19(8):26–34, August 1986.
- [Bal and Tanenbaum 1988] Bal, H. E. and Tanenbaum, A. S. Distributed Programming with Shared Data. In *Proc. of the 1988 Int'l Conf. on Computer Languages*, pages 82–91, 1988.
- [Bal 1989] Bal, H. E. The Shared Data-Object Model as a Paradigm for Programming Distributed Systems. PhD thesis, Free University, The Netherlands, 1989.
- [Bal *et al.* 1989] Bal, H. E., Kaashoek, M. F., and Tanenbaum, A. S. A Distributed Implementation of the Shared Data-Object model. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems*, pages 1–19, October 1989.
- [Bal 1990] Bal, H. E. Programming Distributed Systems. Silicon Press, 1990.
- [Bal *et al.* 1990a] Bal, H. E., Kaashoek, M. F., and Tanenbaum, A. S. Experience with Distributed Programming in Orca. In *Proc. of the 1990 Int'l Conf. on Computer Languages*, pages 79–89, March 1990.
- [Bal *et al.* 1990b] Bal, H. E., Tanenbaum, A. S., and Kaashoek, M. F. Orca: A Language for Distributed Programming. *ACM SIGPLAN Notices*, 25(5):17–24, May 1990.
- [Bal and Tanenbaum 1991] Bal, H. E. and Tanenbaum, A. S. Distributed Programming with Shared Data. *Computer Languages*, 16(2):129–146, 1991.

- [Bal 1991] Bal, H. E. Programming Distributed Systems. Prentice-Hall International, 1991.
- [Bal *et al.* 1992] Bal, H. E., Kaashoek, M. F., and Tanenbaum, A. S. Orca: A Language For Parallel Programming of Distributed Systems. *IEEE Trans. on Software Engineering*, 18(3):190–205, March 1992.
- [Bal and Kaashoek 1993] Bal, H. E. and Kaashoek, M. F. Object Distribution in Orca using Compile-Time and Run-Time Techniques. In *Proc. of the Eighth Annual Conf. on Object-Oriented Programming Systems, Languages, and Applications (OOPSLA'93)*, pages 162–177, September 1993.
- [Bal *et al.* 1998] Bal, H. E., Bhoedjang, R., Hofman, R., Jacobs, C., Langendoen, K., and Ruhl, T. Performance Evaluation of the Orca Shared-Object System. *ACM Trans. on Computer Systems*, 16(1):1–40, February 1998.
- [Ben-Asher 1996] Ben-Asher, Y. A Mixed Shared Memory and Message Passing Programming Style for Heterogeneous Local Area Network. In *Heterogenous Computing*. Artech House, 1996.
- [Ben-Asher *et al.* 1996] Ben-Asher, Y., Feitelson, D. G., and Rudolph, L. Parc—An Extension of C for Shared Memory Parallel Processing. *Software—Practice and Experience*, 26(10):581–612, May 1996.
- [Berrendorf and Gerndt 1995] Berrendorf, R. and Gerndt, M. SVM-Fortran Reference Manual Version 1.4. Technical Report KFA-ZAM-IB-9510, Central Institute for Applied Mathematics, Research Centre Juelich, Germany, April 1995.
- [Berrendorf *et al.* 1995a] Berrendorf, R., Gerndt, M., Mairandres, M., and Zeisset, S. A Programming Environment for Shared Virtual Memory on the Intel Paragon. In *Proc. of the Intel User Group Meeting*, June 1995.
- [Berrendorf *et al.* 1995b] Berrendorf, R., Gerndt, M., and Mairandres, M. Programming Shared Virtual Memory on the Intel Paragon(TM) Supercomputer. In *Proc. of the Fifth Workshop on Compilers for Parallel Computers (CPC'95)*, pages 257–270, July 1995.
- [Berrendorf and Gerndt 1995a] Berrendorf, R. and Gerndt, M. Compiling Data Parallel Languages for Shared Virtual Memory Systems. Technical Report KFA-ZAM-IB-9517, Central Institute for Applied Mathematics, Research Centre Juelich, Germany, September 1995.
- [Berrendorf and Gerndt 1995b] Berrendorf, R. and Gerndt, M. Compiling SVM-Fortran for the Intel Paragon XP/S. In *Proc. of the 1995 2nd Int'l Conf. on Programming Models for Massively Parallel Computers*, pages 52–59, October 1995.
- [Berrendorf *et al.* 1996] Berrendorf, R., Gerndt, M., and Krumme, A. A Programming Environment for Parallel Computers with a Global Address Space. In *Proc. of the 1st Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'96)*, pages 10–16, April 1996.
- [Bisiani *et al.* 1987] Bisiani, R., Alleva, F., Correrini, F., Forin, A., Lecouat, F., and Lerner, R. Heterogeneous Parallel Processing: The Agora Shared Memory. Technical Report CMU-CS-87-112, School of Computer Science, Carnegie-Mellon University, March 1987.
- [Bisiani and Forin 1988] Bisiani, R. and Forin, A. Multilanguage Parallel Programming of Heterogeneous Machines. *IEEE Transactions on Computers*, 37(8):930–945, August 1988.
- [Blumofe *et al.* 1996a] Blumofe, R. D., Frigo, M., Joerg, C. F., Leiserson, C. E., and Randall, K. H. Dag-Consistent Distributed Shared Memory. In *Proc. of the 10th Int'l Parallel Processing Symp. (IPPS'96)*, pages 132–141, April 1996.
- [Blumofe *et al.* 1996b] Blumofe, R. D., Frigo, M., Joerg, C. F., Leiserson, C. E., and Randall, K. H. An Analysis of Dag-Consistent Distributed Shared-Memory Algorithms. In *Proc. of the 8th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'96)*, pages 297–308, June 1996.
- [Bodin *et al.* 1993] Bodin, F., Kervella, L., and Priol, T. Fortran-S: A Fortran Interface for Shared Virtual Memory Architectures. In *Proc. of Supercomputing'93*, pages 274–283, November 1993.

- [Bodin *et al.* 1994] Bodin, F., Granston, E. D., and Montaut, T. Evaluating Two Loop Transformations for Reducing Multiple-Writer False Sharing. In *Proc. of the 7th Int'l Workshop on Languages and Compilers for Parallel Computing*, August 1994.
- [Bodin and O'Boyle 1995] Bodin, F. and O'Boyle, M. F. P. A Compiler Strategy for SVM. In *Proc. of the 3rd Workshop on Languages, Compilers and Runtime Systems for Scalable Computing*, May 1995.
- [Carriero and Gelernter 1986] Carriero, N. and Gelernter, D. The S/Net's Linda Kernel. *ACM Trans. on Computer Systems*, 4(2):110–129, May 1986.
- [Carriero *et al.* 1994] Carriero, N. J., Gelernter, D., Mattson, T. G., and Sherman, A. H. The Linda Alternative to Message-passing Systems. *Parallel Computing*, 20(4):635–655, April 1994.
- [Chandra *et al.* 1996] Chandra, S., Richards, B. E., and Larus, J. R. Teapot: Language Support for Writing Memory Coherence Protocols. In *Proc. of the SIGPLAN Conf. on Programming Language Design and Implementation (PLDI'96)*, pages 237–248, May 1996.
- [Chase *et al.* 1989] Chase, J. S., Amador, F. G., Lazowska, E. D., Levy, H. M., and Littlefield, R. J. The Amber System: Parallel Programming on a Network of Multiprocessors. In *Proc. of the 12th ACM Symp. on Operating Systems Principles (SOSP-12)*, pages 147–158, December 1989.
- [Chiba *et al.* 1992] Chiba, S., Kato, K., and Masuda, T. Exploiting a Weak Coherency to Implement Distributed Tuple Space. In *Proc. of the 12th Int'l Conf. on Distributed Computing Systems (ICDCS-12)*, pages 416–423, June 1992.
- [Chiueh and Verma 1995] Chiueh, T.-C. and Verma, M. A Compiler-Directed Distributed Shared Memory System. In *Proc. of the 9th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 77–86, July 1995.
- [Feeley and Levy 1992] Feeley, M. J. and Levy, H. M. Distributed Shared Memory with Versioned Objects. In *Proc. of the Seventh Annual Conf. on Object-Oriented Programming Systems, Languages, and Applications (OOPSLA'92)*, pages 247–262, October 1992.
- [Fenwick Jr. and Pollock 1997] Fenwick Jr., J. B. and Pollock, L. L. Issues and Experiences in Implementing a Distributed Tuplespace. *Software—Practice and Experience*, 27(10):1199–1232, October 1997.
- [Forin *et al.* 1987] Forin, A., Bisiani, R., and Correrini, F. Parallel Processing with Agora. Technical Report CMU-CS-87-183, School of Computer Science, Carnegie-Mellon University, December 1987.
- [Freeh *et al.* 1994] Freeh, V. W., Lowenthal, D. K., and Andrews, G. R. Distributed Filaments: Efficient Fine-Grain Parallelism on a Cluster of Workstations. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 201–213, November 1994.
- [Gerndt and Berrendorf 1995] Gerndt, M. and Berrendorf, R. Parallelizing Applications with SVM-Fortran. In *Proc. of the High-Performance Computing and Networking Europe 1995 (HPCN'95)*, number 919 in Lecture Notes in Computer Science, pages 793–798. Springer-Verlag, May 1995.
- [Gokhale and Minnich 1993] Gokhale, M. B. and Minnich, R. G. An Implementation of the Shared Data Formats Standard for Distributed Shared Memories. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-IV)*, pages 109–121, September 1993.
- [Granston 1994] Granston, E. D. Towards a Compile-time Methodology for Reducing False Sharing and Communication Traffic in Shared Virtual Memory Systems. In *Proc. of the 6th Int'l Workshop on Languages and Compilers for Parallel Computing*, pages 273–289, June 1994.
- [Haber and Ben-Asher 1995] Haber, G. and Ben-Asher, Y. On the Usage of Simulators to Detect Inefficiency of Parallel Programs Caused by “bad” Scheduling: the SIMPARC Approach. In *Proc. of the 2nd Int'l Conf. on High Performance Computing (HiPC'95)*, pages 255–272, December 1995.
- [Haines and Bohm 1992] Haines, M. and Bohm, W. The Design of VISA: A Virtual Shared Addressing System. Technical Report CS-92-120, Dept. of Computer Science, Colorado State University, May 1992.

- [Haines and Bohm 1993a] Haines, M. and Bohm, W. The VISA User's Guide. Technical Report CS-93-102, Dept. of Computer Science, Colorado State University, March 1993.
- [Haines and Bohm 1993b] Haines, M. and Bohm, W. Task Management, Virtual Shared Memory, and Multithreading in a Distributed Memory Implementation of Sisal. In *Proc. of Parallel Architectures and Languages Europe (PARLE'93)*, pages 12–23, June 1993.
- [Haines and Bohm 1993c] Haines, M. and Bohm, W. On the Design of Distributed Memory Sisal. *Journal of Programming Languages*, 1(3):209–240, September 1993.
- [Inagaki *et al.* 1998] Inagaki, T., Niwa, J., Matsumoto, T., and Hiraki, K. Supporting Software Distributed Shared Memory with a Optimizing Compiler. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 225–235, August 1998.
- [John and Ahamad 1993] John, R. and Ahamad, M. Casual Memory: Implementation, Programming Support and Experiences. Technical Report GIT-CC-93-10, College of Computing, Georgia Institute of Technology, 1993.
- [Jul *et al.* 1988] Jul, E., Levy, H., Hutchinson, N., and Black, A. Fine-Grained Mobility in the Emerald System. *ACM Trans. on Computer Systems*, 6(1):109–133, February 1988.
- [Kaashoek *et al.* 1989] Kaashoek, M. F., Bal, H. E., and Tanenbaum, A. S. Experience with the Distributed Data Structure Paradigm in Linda. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems*, pages 175–191, October 1989.
- [Karpoff and Lake 1993] Karpoff, W. and Lake, B. PARDO—A Deterministic, Scalable Programming Paradigm for Distributed Memory Parallel Computer Systems and Workstation Clusters. In *Proc. of Supercomputing Symp—High Performance Computing: New Horizons*, pages 145–152, 1993.
- [Koelbel *et al.* 1990] Koelbel, C., Mehrotra, P., and van Rosendale, J. Supporting Shared Data Structures on Distributed Memory Architectures. In *Proc. of the Second ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'90)*, pages 177–186, March 1990.
- [Langendoen *et al.* 1995] Langendoen, K., Bhoedjang, R., and Bal, H. E. Automatic Distribution of Shared Data Objects. In *Proc. of the Conference on Languages, Compilers and Run-Time Systems for Scalable Computers*, pages 287–290, May 1995.
- [Larus *et al.* 1994] Larus, J. R., Richards, B. E., and Viswanathan, G. LCM: Memory System Support for Parallel Language Implementation. In *Proc. of the 6th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVI)*, pages 208–218, October 1994.
- [Lee 1993] Lee, J. W. Concord: Re-Thinking the Division of Labor in a Distributed Shared Memory System. Technical Report 93-12-05, Dept. of Computer Science and Engineering, University of Washington, December 1993.
- [Lee 1994] Lee, J. W. Concord: Re-Thinking the Division of Labor in a Distributed Shared Memory System. In *Proc. of the Scalable High-Performance Computing Conf. (SHPCC'94)*, pages 585–592, May 1994.
- [Lilja 1995] Lilja, D. Compiler Assistance for Directory-Based Cache Coherence Enforcement. In *Proc. of the 1995 ICPP Workshop on Challenges for Parallel Processing*, August 1995.
- [Lim and Feldman 1995] Lim, C.-C. and Feldman, J. A. Distributed Memory Implementation of a Shared-address Parallel Object-oriented Language. In *Proc. of the Conf. on Languages, Compilers and Run-Time Systems for Scalable Computers*, pages 303–306, May 1995.
- [Lu 1997a] Lu, P. Aurora: Scoped Behaviour for Per-Context Optimized Distributed Data Sharing. In *Proc. of the 11th Int'l Parallel Processing Symp. (IPPS'97)*, April 1997.

- [Lu 1997b] Lu, P. Implementing Optimized Distributed Data Sharing Using Scoped Behaviour and a Class Library. In *Proc. of the 3rd USENIX Conference on Object-Oriented Technologies and Systems (COOTS)*, June 1997.
- [Lupke 1994] Lupke, S. Accelerated Access to Shared Distributed Arrays on Distributed Memory Systems by Access Objects. In *Proc. of the 3rd Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'94)*, pages 449–460, June 1994.
- [Lupke *et al.* 1995] Lupke, S., Quittek, J. W., and Wiese, T. The Public Shared Objects Run-time System. In *Proc. of the Workshop on Parallel Programming and Computation (ZEUS'95)*, pages 203–211, 1995.
- [Matthews and Le Sergent 1995] Matthews, D. C. J. and Le Sergent, T. LEMMA: A Distributed Shared Memory with Global and Local Garbage Collection. In *Proc. of the Int'l Workshop on Memory Management (IWMM'95)*, pages 297–311, September 1995.
- [Matsumoto *et al.* 1998] Matsumoto, T., Niwa, J., and Hiraki, K. Compiler-Assisted Distributed Shared Memory Schemes Using Memory-Based Communication Facilities. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 875–882, July 1998.
- [Mirchandaley and Hiranandani 1994] Mirchandaley, R. and Hiranandani, S. Improving the Performance of DSM Systems via Compiler Involvement. In *Proc. of Supercomputing'94*, pages 763–772, November 1994.
- [Mounes–Toussi *et al.* 1994] Mounes–Toussi, F., Lilja, D. J., and Li, Z. An Evaluation of a Compiler Optimization for Improving the Performance of a Coherence Directory Based Cache Coherence Mechanism. In *Proc. of the 8th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 75–84, July 1994.
- [O'Boyle and Bodin 1995] O'Boyle, M. F. P. and Bodin, F. Compiler Reduction of Synchronization in Shared Virtual Memory Systems. In *Proc. of the 9th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 318–327, July 1995.
- [O'Boyle and Bull 1995] O'Boyle, M. F. P. and Bull, J. M. Loop versus Data Scheduling: Models, Language and Application for SVM. In *Proc. of the 1995 2nd Int'l Conf. on Programming Models for Massively Parallel Computers*, pages 60–67, 1995.
- [O'Boyle *et al.* 1996a] O'Boyle, M. F. P., Ford, R. W., and Nisbet, A. P. Compiler Reduction of Invalidation Traffic in Virtual Shared Memory Systems. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 432–440, August 1996.
- [O'Boyle *et al.* 1996b] O'Boyle, M. F. P., Nisbet, A. P., and Ford, R. W. A Compiler Algorithm to Reduce Invalidation Latency in Virtual Shared Memory Systems. In *Proc. of the 1996 Conf. on Parallel Architectures and Compilation Techniques*, pages 248–257, October 1996.
- [Richards 1996] Richards, B. E. Memory Systems for Parallel Programming. PhD thesis, Computer Sciences Department, University of Wisconsin-Madison, August 1996.
- [Ruhl and Bal 1994] Ruhl, T. and Bal, H. E. The Nested Object Model. In *Proc. of the 6th ACM SIGOPS European Workshop*, September 1994.
- [Ruhl and Bal 1998] Ruhl, T. and Bal, H. E. Synchronizing Operations on Multiple Objects. In *Proc. of the IPPS/SPDP'98 Workshops*, pages 135–146, March 1998.
- [Scales and Lam 1994a] Scales, D. J. and Lam, M. S. An Efficient Shared Memory Layer for Distributed Memory Machines. Technical Report CSL-TR-94-627, Computer Systems Laboratory, Stanford University, 1994.
- [Scales and Lam 1994b] Scales, D. J. and Lam, M. S. The Design and Evaluation of a Shared Object System for Distributed Memory Machines. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 101–114, November 1994.

- [Schoettner *et al.* 1999] Schoettner, M., Schirpf, O., Wende, M., and Schulthess, P. Implementation of the Java Language in a Persistent DSM Operating System. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, July 1999.
- [Schuerman and Li 1994] Schuerman, K. and Li, L.-L. Tackling False Sharing in a Parallel Logic Programming System. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 88–102, April 1994.
- [Tanenbaum and Bal 1993] Tanenbaum, A. S. and Bal, H. E. Programming a Distributed System Using Shared Objects. In *Proc. of the Second IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-2)*, pages 5–12, July 1993.
- [Tanenbaum *et al.* 1993] Tanenbaum, A. S., Bal, H. E., and Kaashoek, M. F. Programming Multicomputers Using Shared Objects—*Position Paper*. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOS'93)*, pages 199–202, December 1993.
- [Trachos and Maehle 1995] Trachos, K. and Maehle, E. A Class Hierarchy Emulating Virtual Shared Objects. In *Proc. of the 3rd EUROMICRO Workshop on Parallel and Distributed Processing (PDP'95)*, pages 174–181, January 1995.
- [Verma 1996] Verma, M. Compiler-Directed Distributed Shared Virtual Memory. PhD thesis, Dept. of Computer Science, State University of New York at Stony Brook, 1996.
- [Verma and Chiueh 1998] Verma, M. and Chiueh, T.-C. Implementation and Performance Evaluation of Locust. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 96–104, August 1998.
- [Viswanathan and Larus 1996] Viswanathan, G. and Larus, J. R. Compiler-directed Shared-Memory Communication for Iterative Parallel Applications. In *Proc. of Supercomputing'96*, November 1996.

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- [Abandah and Davidson 1998a] Abandah, G. A. and Davidson, E. S. Characterizing Distributed Shared Memory Performance: A Case Study of the Convex SPP1000. *IEEE Trans. on Parallel and Distributed Systems*, 9(2):206–216, February 1998.
- [Abandah and Davidson 1998b] Abandah, G. A. and Davidson, E. S. A Comparative Study of Cache-Coherent Nonuniform Memory Access Systems. In *Proc. of the 12th Annual Symp. on High Performance Computing Systems and Applications (HPCS'98)*, May 1998.
- [Abandah and Davidson 1998c] Abandah, G. A. and Davidson, E. S. Effects of Architectural and Technological Advances on the HP/Convex Exemplar's Memory and Communication Performance. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Agarwal *et al.* 1988] Agarwal, A., Simoni, R., Hennessy, J. L., and Horowitz, M. An Evaluation of Directory scheme for Cache Coherence. In *Proc. of the 15th Annual Int'l Symp. on Computer Architecture (ISCA'88)*, pages 280–289, May 1988.
- [Bai and Makinouchi 1995] Bai, G. and Makinouchi, A. Implementation and Performance Evaluation of a Distributed Paged-object Storage Server. *IEICE Trans. on Information and Systems*, E78-D(11):1439–1448, November 1995.
- [Bennett and Fletcher 1996] Bennett, J. K. and Fletcher, K. E. The Performance Value of Shared Network Caches in Clustered Multiprocessor Workstations. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 64–74, May 1996.

- [Bodin and Priol 1994] Bodin, F. and Priol, T. Overview of the KOAN Programming Environment for the iPSC/2 and Performance Evaluation of the BECAUSE Test Program 2.51. *Future Generation Computer Systems*, 10(4):391–401, November 1994.
- [Bogineni and Dowd 1992a] Bogineni, K. and Dowd, P. W. Performance Analysis of Two Address Space Allocation Schemes for an Optically Interconnected Distributed Shared Memory System. In *Proc. of the 6th Int'l Parallel Processing Symp. (IPPS'92)*, pages 562–566, March 1992.
- [Bogineni and Dowd 1992b] Bogineni, K. and Dowd, P. W. Performance Analysis of Two Address Space Allocation Schemes for an Optically Interconnected Distributed Shared Memory System. *Int'l Journal of High Speed Computing*, 4(3):179–212, September 1992.
- [Brorsson 1991] Brorsson, M. Local vs. Global Memory in the IBM RP3—Experiments and Performance Modeling. In *Proc. of the Third IEEE Symp. on Parallel and Distributed Processing*, pages 496–503, December 1991.
- [Brunie *et al.* 1996] Brunie, L., Lefevre, L., and Reymann, O. Monitoring and Performance Analysis of Distributed Shared Memory Applications. In *Proc. of the Second Int'l Conf. on Massively Parallel Computing Systems*, pages 382–389, May 1996.
- [Buck and Keleher 1998] Buck, B. and Keleher, P. Locality and Performance of Page- and Object-Based DSMs. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 687–693, March 1998.
- [Cabillic *et al.* 1995] Cabillic, G., Muller, G., and Puaut, I. The Performance of Consistent Checkpointing in Distributed Shared Memory Systems. In *Proc. of the 14th Symp. on Reliable Distributed Systems (SRDS'95)*, pages 96–105, September 1995.
- [Chaiken and Agarwal 1994] Chaiken, D. and Agarwal, A. Software-Extended Coherent Shared Memory: Performance and Cost. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 314–324, April 1994.
- [Chandra *et al.* 1994] Chandra, R., Gharachorloo, K., Soundararajan, V., and Gupta, A. Performance Evaluation of Hybrid Hardware and Software Distributed Shared Memory Protocols. In *Proc. of the 8th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 274–288, July 1994.
- [Chapin *et al.* 1995] Chapin, J., Herrod, S. A., Rosenblum, M., and Gupta, A. Memory System Performance of UNIX on CC-NUMA Multiprocessors. In *Proc. of the 1995 ACM SIGMETRICS Joint Int'l Conf. on Measurement and Modeling of Computer Systems (SIGMETRICS'95/PERFORMANCE'95)*, pages 1–13, May 1995.
- [Chong *et al.* 1996] Chong, F. T., Lim, B.-H., Bianchini, R., Kubiawicz, J., and Agarwal, A. Application Performance on the MIT Alewife Machine. *IEEE Computer*, 29(12):57–64, December 1996.
- [Chong and Hwang 1995] Chong, Y.-K. and Hwang, K. Performance Analysis of Four Memory Consistency Models for Multithreaded Multiprocessors. *IEEE Trans. on Parallel and Distributed Systems*, 6(10):1085–1099, October 1995.
- [Cordsen and Gubitoso 1996] Cordsen, J. and Gubitoso, M. D. Performance Considerations in a Virtually Shared Memory System. Technical Report TR #1034, GMD-FIRST, November 1996.
- [Cox *et al.* 1997] Cox, A. L., Dwarkadas, S., Lu, H., and Zwaenepoel, W. Evaluating the Performance of Software Distributed Shared Memory as a Target for Parallelizing Compilers. In *Proc. of the 11th Int'l Parallel Processing Symp. (IPPS'97)*, pages 474–482, April 1997.
- [Cox *et al.* 1999] Cox, A., de Lara, E., and Y. C. Hu, W. Z. A Performance Comparison of Homeless and Home-Based Lazy Release Consistency Protocols for Software Shared Memory. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.

- [Dwarkadas *et al.* 1993] Dwarkadas, S., Keleher, P., Cox, A. L., and Zwaenepoel, W. Evaluation of Release Consistent Software Distributed Shared Memory on Emerging Network Technology. In *Proc. of the 20th Annual Int'l Symp. on Computer Architecture (ISCA'93)*, pages 144–155, May 1993.
- [Dwarkadas *et al.* 1999] Dwarkadas, S., Gharachorloo, K., Kontothanassis, L., Scales, D. J., Scott, M. L., and Stets, R. Comparative Evaluation of Fine- and Coarse-Grain Approaches for Software Distributed Shared Memory. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Foong 1995] Foong, W.-K. Comparative Study of Two Parallel-Programming Models and Their Performance. In *Proc. of the 7th Conf. on North American Transputer Users Group (NATUG-7)*, pages 317–329, October 1995.
- [Gharachorloo *et al.* 1991] Gharachorloo, K., Gupta, A., and Hennessy, J. L. Performance Evaluation of Memory Consistency Models for Shared Memory Multiprocessors. In *Proc. of the 4th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOS-IV)*, pages 245–257, April 1991.
- [Holt *et al.* 1995] Holt, C., Heinrich, M., Singh, J. P., Rothberg, E., and Hennessy, J. L. The Performance Effects of Latency, Occupancy and Bandwidth in Cache-Coherent DSM Multiprocessors. In *Proc. of the Fifth Workshop on Scalable Shared Memory Multiprocessors*, June 1995.
- [Hu 1995] Hu, W. Improving the Performance of Sequential Consistency in Cache Coherence Systems. In *Proc. of the 2nd Int'l Conf. on High Performance Computing (HiPC'95)*, pages 81–86, December 1995.
- [Hwang 1996] Hwang, T.-S. Performance Evaluation of a WDMA OIDSMS Multiprocessors. In *Proc. of the 9th Int'l Conf. on Parallel and Distributed Systems*, pages 162–168, June 1996.
- [Iftode *et al.* 1996] Iftode, L., Singh, J. P., and Li, K. Understanding Application Performance on Shared Virtual Memory Systems. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 122–133, May 1996.
- [Jou *et al.* 1995] Jou, C.-J., Alkhatib, H. S., and Li, Q. Two-tier Paging and its Performance Analysis for Network-based Distributed Shared Memory Systems. *IEICE Trans. on Information and Systems*, E75-D(8):1021–1031, August 1995.
- [Karlsson and Stenstrom 1996] Karlsson, M. and Stenstrom, P. Performance Evaluation of Cluster-Based Multicomputer Built from ATM Switches and Bus-Based Multiprocessor Servers. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, pages 4–12, February 1996.
- [Karlsson and Brorsson 1998] Karlsson, S. and Brorsson, M. A Comparative Characterization of Communication Patterns in Applications using MPI and Shared Memory on an IBM SP2. In *Proc. of the Second Int'l Workshop on Communication and Architectural Support for Network-Based Parallel Computing (CANPC'98)*, pages 189–201, January 1998.
- [Keleher *et al.* 1995] Keleher, P., Cox, A. L., Dwarkadas, S., and Zwaenepoel, W. An Evaluation of Software-Based Release Consistent Protocols. *Journal of Parallel and Distributed Computing*, 29(2):126–141, September 1995.
- [Koch 1993] Koch, P. T. Performance Evaluation of Two Software Distributed Shared Memory Models—*Position Paper*. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOS'93)*, pages 162–166, December 1993.
- [Kontothanassis *et al.* 1997] Kontothanassis, L. I., Hunt, G., Stets, R., Hardavellas, N., Cierniak, M., Parthasarathy, S., Meira, Jr., W., Dwarkadas, S., and Scott, M. L. VM-Based Shared Memory on Low-Latency, Remote-Memory-Access Networks. In *Proc. of the 24th Annual Int'l Symp. on Computer Architecture (ISCA'97)*, pages 157–169, June 1997.

- [Lau *et al.* 1996] Lau, A. C. K., Yung, N. H. C., and Cheung, Y. S. Performance Analysis of the Doubly-linked List Protocol Family for Distributed Shared Memory Systems. In *Proc. of IEEE 2nd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'96)*, pages 365–372, June 1996.
- [Levelt *et al.* 1992] Levelt, W. G., Kaashoek, M. F., Bal, H. E., and Tanenbaum, A. S. A Comparison of Two Paradigms for Distributed Shared Memory. *Software—Practice and Experience*, 22(11):985–1010, November 1992.
- [Li *et al.* 1994] Li, A., Hermannsson, G., and Wittie, L. Program Analysis and Transformation for Fast Data Sharing. In *Proc. of the Third IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-3)*, pages 131–138, April 1994.
- [Li *et al.* 1995] Li, A., Hermannsson, G., and Wittie, L. An Analytical Model for Performance Evaluation of Eager-Sharing Distributed Memory Systems. In *Proc. of the Seventh SIAM Conference on Parallel Processing for Scientific Computing*, pages 774–775, February 1995.
- [Liao *et al.* 1998a] Liao, C., Jiang, D., Iftode, L., Martonosi, M., and Clark, D. W. Monitoring Shared Virtual Memory Performance on a Myrinet-based PC Cluster. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Liao *et al.* 1998b] Liao, C., Jiang, D., Iftode, L., Martonosi, M., and Clark, D. W. Monitoring Shared Virtual Memory Performance on a Myrinet-based PC Cluster. In *Proc. of the 12th ACM Int'l Conf. on Supercomputing*, July 1998.
- [Lindemann and Schoen 1993a] Lindemann, C. and Schoen, F. Evaluating Sequential Consistency in a Virtually Shared Memory System by Deterministic and Stochastic Petri Nets. In *Proc. of the 1st Int'l Workshop on Modeling, Analysis, and Simulation of Computers and Telecommunication Systems (MAS-COTS'93)*, pages 63–68, January 1993.
- [Lindemann and Schoen 1993b] Lindemann, C. and Schoen, F. Performance Evaluation of Consistency Models for Multicomputers with Virtually Shared Memory. In *Proc. of the 26th Hawaii Int'l Conf. on System Sciences (HICSS26)*, pages 154–163, January 1993.
- [Lu *et al.* 1997] Lu, H., Cox, A. L., Dwarkadas, S., Rajamony, R., and Zwaenepoel, W. Compiler and Software Distributed Shared Memory Support for Irregular Applications. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 48–56, June 1997.
- [Marquardt and Alkhatib 1992] Marquardt, D. E. and Alkhatib, H. S. A Cache-coherent Distributed Memory Multiprocessor System and Its Performance Analysis. *IEICE Trans. on Information and Systems*, E75-D(3):274–290, May 1992.
- [Meira Jr. *et al.* 1997] Meira Jr., W., LeBlanc, T. J., Hardavellas, N., and Amorim, C. Understanding the Performance of DSM Applications. In *Proc. of the First Int'l Workshop on Communication and Architectural Support for Network-Based Parallel Computing (CANPC'97)*, pages 198–211, February 1997.
- [Midorikawa 1997] Midorikawa, H. The Evaluation of User-level Software Based Distributed Shared Memory Systems. In *Proc. of the 1997 IEEE Pacific Rim Conf. on Fault Tolerant Systems (PRFTS'97)*, volume 2, pages 920–923, August 1997.
- [Mizrahi *et al.* 1989] Mizrahi, H. E., Baer, J.-L., Lazowska, E. D., and Zahorjan, J. Extending the Memory Hierarchy into Multiprocessor Interconnection Network: A Performance Analysis. In *Proc. of the 1989 Int'l Conf. on Parallel Processing (ICPP'89)*, volume I, pages 41–50, 1989.
- [Moga *et al.* 1997] Moga, A., Gefflaut, A., and Dubois, M. Hardware vs. Software Implementation of COMA: A Performance Comparison. In *Proc. of the 1997 Int'l Conf. on Parallel Processing (ICPP'97)*, August 1997.

- [Mowry *et al.* 1998] Mowry, T. C., Chan, C., and Lo, A. Comparative Evaluation of Latency Tolerance Techniques for Software Distributed Shared Memory. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, pages 300–311, February 1998.
- [Muller *et al.* 1994] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. An Evaluation Study of a Link-Based Data Diffusion Machine. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 115–128, April 1994.
- [Muller *et al.* 1996] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. The Role of Associative Memory in VSM Architectures: A Price-Performance Comparison. In *Proc. of the 4th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'96)*, pages 41–49, January 1996.
- [Nakano *et al.* 1998] Nakano, M., Imai, H., and Kitsuregawa, M. Performance Analysis of Parallel Hash Join Algorithms on a Distributed Shared Memory Machine Implementation and Evaluation on HP Exemplar SPP 1600. In *Proc. of 14th Int'l Conf. on Data Engineering*, pages 76–85, February 1998.
- [Oi 1995] Oi, H. Performance Analysis of a Data Diffusion Machine with High Fanout and Split Directories. *Journal of the Information Processing Society of Japan*, 36(7):1662–1668, July 1995.
- [Owicki and Agarwal 1989] Owicki, S. and Agarwal, A. Evaluating the Performance of Software Cache Coherence. In *Proc. of the Third Int'l Conf. on Architectural Support for Programming Languages and Operating Systems (ASPLOS-III)*, pages 230–242, May 1989.
- [Parsons *et al.* 1997] Parsons, E. W., Brorsson, M., and Sevcik, K. C. Predicting the Performance of Distributed Virtual Shared-Memory Applications. *IBM Systems Journal*, 36(4):527–549, 1997.
- [Petersen and Li 1993] Petersen, K. and Li, K. A Comparative Evaluation of Cache Coherence Schemes Based on Virtual Memory Support. Technical Report TR-373-92, Dept. of Computer Science, Princeton University, 1993.
- [Petersen and Li 1994] Petersen, K. and Li, K. An Evaluation of Multiprocessor Cache Coherence Based on Virtual Memory Support. In *Proc. of the 8th Int'l Parallel Processing Symp. (IPPS'94)*, pages 158–164, April 1994.
- [Rajamony and Cox 1997] Rajamony, R. and Cox, A. L. Performance Debugging Shared Memory Parallel Programs Using Run-Time Dependency Analysis. In *Proc. of the 1997 ACM SIGMETRICS Conference on Performance Measurement, Modeling, and Evaluation*, June 1997.
- [Rajamony 1998] Rajamony, R. Prescriptive Performance Tuning: The Rx Approach. PhD thesis, Department of Computer Science, Rice University, January 1998.
- [Ranganathan *et al.* 1997] Ranganathan, P., Pai, V. S., and Adve, S. V. Using Speculative Retirement and Larger Instruction Windows to Narrow the Performance Gap between Memory Consistency Models. In *Proc. of the 9th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'97)*, pages 199–210, 1997.
- [Scales and Gharachorloo 1997] Scales, D. J. and Gharachorloo, K. Performance of the Shasta Distributed Shared Memory Protocol. Technical Report WRL-TR-97/2, Digital Western Research Laboratory, February 1997.
- [Srblijic and Budin 1993] Srblijic, S. and Budin, L. Analytic Performance Evaluation of Data Replication Based Shared Memory Model. In *Proc. of the Second IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-2)*, pages 326–335, July 1993.
- [Srblijic *et al.* 1994] Srblijic, S., Vranesic, Z. G., and Budin, L. Performance Prediction for Different Consistency Schemes in Distributed Shared Memory Systems. In *Proc. of the Third IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-3)*, pages 295–302, April 1994.

- [Stenstrom *et al.* 1992] Stenstrom, P., Joe, T., and Gupta, A. Comparative Performance Evaluation of Cache-Coherent NUMA and COMA Architectures. In *Proc. of the 19th Annual Int'l Symp. on Computer Architecture (ISCA'92)*, pages 80–91, May 1992.
- [Sun and Zhu 1995] Sun, X.-H. and Zhu, J. Performance Considerations of Shared Virtual Memory Machines. *IEEE Trans. on Parallel and Distributed Systems*, 6(11):1185–1194, November 1995.
- [Sun and Zhu 1996] Sun, X.-H. and Zhu, J. Performance Prediction: A Case Study Using a Scalable Shared-Virtual-Memory-Machine. *IEEE Parallel and Distributed Technology*, 4(4):36–49, winter 1996.
- [Thekkath *et al.* 1997] Thekkath, R., Singh, A. P., Singh, J. P., S, J., and Hennessy, J. An Evaluation of a Commercial CC-NUMA Architecture—The CONVEX Exemplar SPP1200. In *Proc. of the 11th Int'l Parallel Processing Symp. (IPPS'97)*, April 1997.
- [Torrellas and Hennessy 1990] Torrellas, J. and Hennessy, J. L. Estimating the Performance Advantages of Relaxing Consistency in A Shared Memory Multiprocessor. In *Proc. of the 1990 Int'l Conf. on Parallel Processing (ICPP'90)*, volume I, pages 26–33, August 1990.
- [Trancoso *et al.* 1997] Trancoso, P., Larriba-Pey, J.-L., Zhang, Z., and Torrellas, J. The Memory Performance of DSS Commercial Workloads in Shared-Memory Multiprocessors. In *Proc. of the 3rd IEEE Symp. on High-Performance Computer Architecture (HPCA-3)*, February 1997.
- [Wada *et al.* 1993] Wada, K., Obata, M., Nakamura, M., and Yamazaki, T. A Performance Evaluation of Tree-based Coherent Distributed Shared Memory. In *Proc. of the IEEE Pacific Rim Conf. on Communications, Computers and Signal Processing (PRFTS'93)*, May 1993.
- [Yang and Torrellas 1997] Yang, L. and Torrellas, J. Speeding up the Memory Hierarchy in Flat COMA Multiprocessors. In *Proc. of the 3rd IEEE Symp. on High-Performance Computer Architecture (HPCA-3)*, February 1997.
- [Zhang and Yan 1995] Zhang, X. and Yan, Y. Comparative Modeling and Evaluation of CC-NUMA and COMA on Hierarchical Ring Structures. *IEEE Trans. on Parallel and Distributed Systems*, 6(12):1316–1331, December 1995.
- [Zhang and Torrellas 1997] Zhang, Z. and Torrellas, J. Reducing Remote Conflict Misses: NUMA with Remote Cache versus COMA. In *Proc. of the 3rd IEEE Symp. on High-Performance Computer Architecture (HPCA-3)*, February 1997.
- [Zhou *et al.* 1997] Zhou, Y., Iftode, L., Li, K., Singh, J. P., Toonen, B. R., Schoinas, I., Hill, M. D., and Wood, D. A. Relaxed Consistency and Coherence Granularity in DSM Systems: A Performance Evaluation. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 193–205, June 1997.
- [Zucker and Baer 1992] Zucker, R. N. and Baer, J.-L. A Performance Study of Memory Consistency Models. In *Proc. of the 19th Annual Int'l Symp. on Computer Architecture (ISCA'92)*, pages 2–12, May 1992.
- [Zucker and Baer 1994] Zucker, R. N. and Baer, J.-L. Software versus Hardware Coherence: Performance versus Cost. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 163–172, January 1994.

Related Issues

- [Ahn *et al.* 1995] Ahn, J.-H., Lee, K.-W., and Kim, H.-J. Architectural Issues in Adopting Distributed Shared Memory for Distributed Object Management Systems. In *Proc. of the 5th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'95)*, pages 294–300, August 1995.

- [Baldoni *et al.* 1997] Baldoni, R., Helary, J.-M., Mostefaoui, A., and Raynal, M. Consistent State Restoration in Shared Memory Systems. In *Proc. of the 1997 Advances in Parallel and Distributed Computing (APDC'97)*, 1997.
- [Banatre *et al.* 1990] Banatre, M., Muller, G., Rochat, B., and Sanchez, P. A Reliable Distributed Virtual Memory on Top of the MACH Kernel. In *OSF Micro Kernel Applications Workshop*, November 1990.
- [Banatre *et al.* 1996] Banatre, M., Gefflaut, A., Joubert, P., Morin, C., and Lee, P. A. An Architecture for Tolerating Processor Failures in Shared Memory Multiprocessors. *IEEE Transactions on Computers*, 45(10):1101–1115, October 1996.
- [Barker *et al.* 1995] Barker, K., Peters, R., and Graham, P. Distributed Shared Memory for Interoperability of Heterogeneous Information Systems—*Extended Abstract*. In *OOPSLA Workshop on Interoperable Objects—Experiences and Issues*, October 1995.
- [Bennett *et al.* 1990] Bennett, J. K., Carter, J. B., and Zwaenepoel, W. Adaptive Software Cache Management for Distributed Shared Memory Architectures. In *Proc. of the 17th Annual Int'l Symp. on Computer Architecture (ISCA'90)*, pages 125–135, May 1990.
- [Bodorik *et al.* 1992] Bodorik, P., Smith, F. I., and Lewis, D. J. Transactions in Distributed Shared Memory Systems. In *Proc. of the 8th Int'l Conf. on Data Engineering*, pages 480–487, February 1992.
- [Brown 1993] Brown, L. Fault-Tolerant Distributed Shared Memories. PhD thesis, Department of Computer Science and Engineering, Florida Atlantic University, 1993.
- [Brown and Wu 1994] Brown, L. and Wu, J. Dynamic Snooping in a Fault-Tolerant Distributed Shared Memory. In *Proc. of the 14th Int'l Conf. on Distributed Computing Systems (ICDCS-14)*, pages 218–226, June 1994.
- [Brown and Wu 1995] Brown, L. and Wu, J. Snooping Fault-Tolerant Distributed Shared Memories. *The Journal of Systems and Software*, 29(2):149–165, May 1995.
- [Brzezinski *et al.* 1997] Brzezinski, J., Szychowiak, M., and Wawrzyniak, D. Page-based Distributed Shared Memory for OSF/DCE. In *Proc. of the 2nd Int'l Conf. on Parallel Processing and Applied Mathematics*, volume 1, pages 30–40, September 1997.
- [Carter *et al.* 1992] Carter, J. B., Cox, A., Johnson, D., and Zwaenepoel, W. Distributed Operating Systems Based on a Protected Global Virtual Address Space. In *Proc. of the 3rd Workshop on Workstation Operating Systems (WWOS-III)*, April 1992.
- [Carter *et al.* 1993] Carter, J. B., Cox, A. L., Dwarkadas, S., Elnozahy, E. N., Johnson, D. B., Keleher, P., Rodrigues, S., Yu, W., and Zwaenepoel, W. Network Multicomputing Using Recoverable Distributed Shared Memory. In *Proc. of the 38th IEEE Int'l Computer Conf. (COMPCON Spring'93)*, pages 519–527, February 1993.
- [Chaudhuri *et al.* 1996] Chaudhuri, S., Kanthadai, S., and Welch, J. The Role of Data-Race-Free Programs in Recoverable DSM. In *Proc. of the 15th Annual ACM Symp. on Principles of Distributed Computing (PODC'96)*, page 245, May 1996.
- [Choy *et al.* 1995] Choy, M., Leong, H. V., and Wong, M. H. On Distributed Object Checkpointing and Recovery. In *Proc. of the 14th Annual ACM Symp. on Principles of Distributed Computing (PODC'95)*, pages 64–73, August 1995.
- [Dieter and Lumpp Jr. 1997] Dieter, W. R. and Lumpp Jr., J. E. Fault Recovery for Distributed Shared Memory Systems. In *Proc. of the 1997 IEEE-Aerospace Conf.*, volume 2, pages 525–540, February 1997.
- [Dini *et al.* 1999] Dini, G., Lettieri, G., and Lopriore, L. Implementing Distributed Single Address Space in the Presence of Failures. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, July 1999.

- [Feeley *et al.* 1994] Feeley, M. J., Chase, J. S., Narasayya, V. R., and Levy, H. M. Integrating Coherency and Recoverability in Distributed Systems. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 215–227, November 1994.
- [Ferreira and Shapiro 1994a] Ferreira, P. and Shapiro, M. Garbage Collection of Persistent Objects in Distributed Shared Memory. In *Proc. of the Sixth Int'l Workshop on Persistent Object Systems*, September 1994.
- [Ferreira and Shapiro 1994b] Ferreira, P. and Shapiro, M. Garbage Collection and DSM Consistency. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 229–241, November 1994.
- [Ferreira and Shapiro 1995] Ferreira, P. and Shapiro, M. Garbage Collection in the Larchant Persistent Distributed Shared Store. In *Proc. of the 5th IEEE Workshop on Future Trends of Distributed Computing Systems (FTDCS'95)*, pages 461–467, August 1995.
- [Ferreira and Shapiro 1996] Ferreira, P. and Shapiro, M. Larchant: Persistence by Reachability in Distributed Shared Memory Through Garbage Collection. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 394–401, May 1996.
- [Fleisch 1990] Fleisch, B. D. Reliable Distributed Shared Memory—Extended Abstract. In *Proc. of the 2nd IEEE Workshop on Experimental Distributed Systems*, pages 102–105, October 1990.
- [Fuchi and Tokoro 1994] Fuchi, T. and Tokoro, M. A Mechanism for Recoverable Shared Virtual Memory. Manuscript, 1994.
- [Gefflaut *et al.* 1995] Gefflaut, A., Morin, C., and Banatre, N. Tolerating Node Failures in Cache Only Memory Architectures. In *Proc. of Supercomputing'94*, pages 370–379, November 1995.
- [Gunaseelan and LeBlanc, Jr. 1993] Gunaseelan, L. and LeBlanc, Jr., R. J. Event Ordering in a Shared Memory Distributed System. In *Proc. of the 13th Int'l Conf. on Distributed Computing Systems (ICDCS-13)*, pages 256–263, May 1993.
- [Hardavellas *et al.* 1997] Hardavellas, N., Hunt, G. C., Ioannidis, S., Stets, R., Dwarkadas, S., Konthanassis, L., and Scott, M. L. Efficient Use of Memory-Mapped Network Interfaces for Shared Memory Computing. *Newsletter of the IEEE CS Technical Committee on Computer Architecture*, pages 28–33, March 1997.
- [Hastings 1992] Hastings, A. B. Transactional Distributed Memory. PhD thesis, School of Computer Science, Carnegie Mellon University, July 1992.
- [Hermannsson and Wittie 1994a] Hermannsson, G. and Wittie, L. Fast Locks in Distributed Shared Memory Systems. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 574–583, January 1994.
- [Hermannsson and Wittie 1994b] Hermannsson, G. and Wittie, L. Optimistic Synchronization in Distributed Shared Memory. In *Proc. of the 14th Int'l Conf. on Distributed Computing Systems (ICDCS-14)*, pages 345–354, June 1994.
- [Hsu and Tam 1988] Hsu, M. and Tam, V.-O. Managing Databases in Distributed Virtual Memory. Technical Report TR-07-88, Aiken Computation Laboratory, Harvard University, March 1988.
- [Hsu and Tam 1989] Hsu, M. and Tam, V.-O. Transaction Synchronization in Distributed Shared Virtual Memory Systems. In *Proc. of the 13th Annual Int'l Computer Software and Applications Conf. (COMP-SAC'89)*, pages 166–175, September 1989.
- [Hummel 1994] Hummel, S. F. Adding Fault-Tolerance to Algorithms for Weak Consistency. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 564–573, January 1994.

- [Islam and Devarakonda 1996] Islam, N. and Devarakonda, M. An Essential Design Pattern for Fault-Tolerant Distributed State Sharing. *Communications of the ACM*, 39(10):65–74, October 1996.
- [Jalili *et al.* 1994] Jalili, R., Henskens, F. A., Rosenberg, J., and Koch, D. M. Operating System Support for Object Dependencies in Stable Distributed Persistent Stores. In *Proc. of the Int'l Workshop on Object-Oriented Real-Time Dependable Systems*, pages 21–29, 1994.
- [Jalili and Henskens 1995a] Jalili, R. and Henskens, F. A. Using Directed Graphs to Describe Entity Dependency in Stable Distributed Stores. In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, pages 665–674, January 1995.
- [Jalili and Henskens 1995b] Jalili, R. and Henskens, F. A. Reducing the Extent of Cascadable Operations in Stable Distributed Stores. In *Proc. of the 18th Australasian Computer Science Conf. (ACSC'95)*, pages 227–236, February 1995.
- [James and Singh 1997] James, J. and Singh, A. K. Fault Tolerance Bounds of Memory Consistency. In *Proc. of the 11th Int'l Workshop on Distributed Algorithms (WDAG'97)*, pages 200–214, September 1997.
- [Janakiraman and Tamir 1994] Janakiraman, G. and Tamir, Y. Coordinated Checkpointing-Rollback Error Recovery for Distributed Shared Memory Multicomputers. In *Proc. of the 13th Symp. on Reliable Distributed Systems (SRDS'94)*, pages 42–51, October 1994.
- [Janssens and Fuchs 1993] Janssens, B. and Fuchs, W. K. Relaxing Consistency in Recoverable Distributed Shared Memory. In *Proc. of the 23rd Annual Int'l Symp. on Fault-Tolerant Computing (FTCS-23)*, pages 155–163, 1993.
- [Janssens and Fuchs 1994] Janssens, B. and Fuchs, W. K. Reducing Interprocessor Dependence in Recoverable Distributed Shared Memory. In *Proc. of the 13th Symp. on Reliable Distributed Systems (SRDS'94)*, pages 34–41, October 1994.
- [Janssens and Fuchs 1995] Janssens, B. and Fuchs, W. K. Ensuring Correct Rollback Recovery in Distributed Shared Memory Systems. *Journal of Parallel and Distributed Computing*, 29(2):211–218, September 1995.
- [Judge and Cahill 1993] Judge, A. and Cahill, V. Sharing Objects in a Distributed System—*Position Paper*. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOS'93)*, pages 136–140, December 1993.
- [Juil and Fleisch 1995] Juil, N. C. and Fleisch, B. D. A Memory Approach to Consistent, Reliable DSM. In *Fifth Workshop on Hot Topics in Operating Systems (HotOS-V)*, pages 108–112, May 1995.
- [Kanthadai and Welch 1996] Kanthadai, S. and Welch, J. L. Implementation of Recoverable Distributed Shared Memory by Logging Writes. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 116–123, May 1996.
- [Karlsson and Stenstrom 1997a] Karlsson, M. and Stenstrom, P. Lock Prefetching in Distributed Virtual Shared Memory Systems—Initial Results. *Newsletter of the IEEE CS Technical Committee on Computer Architecture*, pages 41–48, March 1997.
- [Karlsson and Stenstrom 1997b] Karlsson, M. and Stenstrom, P. Effectiveness of Dynamic Prefetching in Multiple-Writer Distributed Virtual Shared Memory Systems. *Journal of Parallel and Distributed Computing*, 43(2):79–93, June 1997.
- [Keedy and Vosseberg 1992] Keedy, J. L. and Vosseberg, K. Security in a Persistent Distributed Operating System. In *Proc. of the 12th GI/ITG Conf. on Architektur von Rechensystemen (Architecture for Computer Systems)*, 1992.

- [Kermarrec *et al.* 1995] Kermarrec, A.-M., Cabillic, G., Gefflaut, A., Morin, C., and Puaut, I. A Recoverable Distributed Shared Memory Integrating Coherence and Recoverability. In *Proc. of the 25th Annual Int'l Symp. on Fault-Tolerant Computing (FTCS-25)*, pages 289–298, June 1995.
- [Kim and Farber 1992] Kim, H. and Farber, D. J. GOBNET (Gigabit Object Network). In *MILCOM'92*, volume 3, pages 986–990, October 1992.
- [Kim and Vaidya 1995a] Kim, J.-H. and Vaidya, N. H. Distributed Shared Memory: Recoverable and Non-recoverable Limited Update Protocols. Technical Report 95–025, Dept. of Computer Science, Texas A&M University, May 1995.
- [Kim and Vaidya 1995b] Kim, J.-H. and Vaidya, N. H. Recoverable Distributed Shared Memory Using the Competitive Update Protocol. In *Proc. of the 1995 IEEE Pacific Rim Conf. on Fault Tolerant Systems (PRFTS'95)*, December 1995.
- [Kim and Vaidya 1996a] Kim, J.-H. and Vaidya, N. H. A Cost-Comparison Approach for Adaptive Distributed Shared Memory. In *Proc. of the 10th ACM Int'l Conf. on Supercomputing*, pages 44–51, May 1996.
- [Kim and Vaidya 1996b] Kim, J.-H. and Vaidya, N. H. Adaptive Migratory Scheme for Distributed Shared Memory. Technical Report 96–023, Dept. of Computer Science, Texas A&M University, 1996.
- [Kim and Vaidya 1997a] Kim, J.-H. and Vaidya, N. H. Research on Adaptive and Recoverable Distributed Shared Memory. *Newsletter of the IEEE CS Technical Committee on Computer Architecture*, pages 49–52, March 1997.
- [Kim and Vaidya 1997b] Kim, J.-H. and Vaidya, N. H. Adaptive Migratory Scheme for Distributed Shared Memory. In *Proc. of the 11th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'97)*, pages 325–332, July 1997.
- [Kim and Vaidya 1997c] Kim, J.-H. and Vaidya, N. H. A Cost Model for Distributed Shared Memory Using Competitive Update. In *Proc. of the 4th Int'l Conf. on High Performance Computing (HiPC'97)*, pages 112–117, December 1997.
- [Kim and Vaidya 1997d] Kim, J.-H. and Vaidya, N. H. Single Fault-Tolerant Distributed Shared Memory Using Competitive Update. *Journal of Microprocessors and Microsystems*, 21(3):183–196, December 1997.
- [Knox and Mead 1995] Knox, D. L. and Mead, R. The Effects of Connectivity on Memory Utilization in Distributed Virtual Memory Systems. In *Proc. of the Seventh IASTED/ISMM Int'l Conf. on Parallel and Distributed Computing and Systems*, pages 78–81, October 1995.
- [Kogan and Schuster 1997] Kogan, D. and Schuster, A. Collecting Garbage Pages in a Distributed Shared Memory with Reduced Memory and Communication Overhead. In *Proc. of the 1997 European Symp. on Algorithms (ESA'97)*, pages 308–325, September 1997.
- [Kordale *et al.* 1993] Kordale, R., Ahamad, M., and Shilling, J. Distributed/Concurrent Garbage Collection in Distributed Shared Memory Systems. In *Proc. of the Third Int'l Workshop on Object Orientation in Operating Systems (IWOOS'93)*, pages 51–60, December 1993.
- [Le Sergent and Berthomieu 1992] Le Sergent, T. and Berthomieu, B. Incremental Multi-threaded Garbage Collection on Virtually Shared Memory Architectures. In *Proc. of the Int'l Workshop on Memory Management (IWMM'92)*, pages 179–199, September 1992.
- [Lenoski 1996] Lenoski, D. E. Design Issues for Distributed Shared Memory. In *Proc. of the Int'l Conf. on Computer Design—VLSI in Computers and Processors*, page 62, October 1996.
- [Li *et al.* 1989] Li, K., Stumm, M., and Wortman, D. Shared Virtual Memory Accommodating Heterogeneity. Technical Report CS-TR-210-89, Dept. of Computer Science, Princeton University, February 1989.

- [Li 1992] Li, K. Scalability Issues of Shared Virtual Memory for Multicomputers. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 263–279. Kluwer Academic Publishers, 1992.
- [Michael and Scott 1995] Michael, M. M. and Scott, M. L. Implementation of Atomic Primitives on Distributed Shared Memory Multiprocessors. In *Proc. of the 1st IEEE Symp. on High-Performance Computer Architecture (HPCA-1)*, pages 222–231, January 1995.
- [Milutinovic *et al.* 1997] Milutinovic, V., Milutinovic, D., Ciric, V., Starcevic, D., Radenkovic, B., and Ivkovic, M. Some Solutions for Critical Problems in the Theory and Practice of Distributed Shared Memory: Ideas and Implications. In *Proc. of the 30th Hawaii Int'l Conf. on System Sciences (HICSS-30)*, January 1997.
- [Moga and Dubois 1998] Moga, A. and Dubois, M. The Effectiveness of SRAM Network caches in Clustered DSMs. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, pages 103–112, February 1998.
- [Mohindra and Ramachandran 1989] Mohindra, A. and Ramachandran, U. Implementation of Fault-Tolerant Transactions Using Distributed Shared Memory. Technical Report GIT-ICS-89/41, College of Computing, Georgia Institute of Technology, 1989.
- [Mohindra 1993] Mohindra, A. Issues in the Design of Distributed Shared Memory Systems. PhD thesis, College of Computing, Georgia Institute of Technology, May 1993.
- [Monnerat and Bianchini 1998] Monnerat, L. R. and Bianchini, R. Efficiently Adapting to Sharing Patterns in Software DSMs. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, February 1998.
- [Morin and Puaut 1995] Morin, C. and Puaut, I. A Survey of Recoverable Distributed Shared Memory Systems. Technical Report PI-975, IRISA, France, December 1995.
- [Morin *et al.* 1996] Morin, C., Gefflaut, A., Banatre, M., and Kermarrec, A.-M. COMA: an Opportunity for Building Fault-Tolerant Scalable Shared Memory Multiprocessors. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 56–65, May 1996.
- [Morin and Puaut 1997] Morin, C. and Puaut, I. A Survey of Recoverable Distributed Shared Memory Systems. *IEEE Trans. on Parallel and Distributed Systems*, 8(9):959–969, September 1997.
- [Morin and Lottiaux 1999] Morin, C. and Lottiaux, R. Global Resource Management for High Availability and Performance in a DSM-based Cluster. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Murer and Farber 1992] Murer, S. and Farber, P. A Scalable Distributed Shared Memory. In *Proc. of the 2nd Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'92)*, pages 453–466, September 1992.
- [Nash *et al.* 1996] Nash, J. M., Dew, P. M., Davy, J. R., and Dyer, M. E. Implementation Issues Relating to the WPRAM Model for Scalable Computing. In *Proc. of the Second Int'l Euro-Par Conf.*, volume II, pages 319–326, August 1996.
- [O'Boyle *et al.* 1995] O'Boyle, M. F. P., Kervella, L., and Bodin, F. Synchronization in a SPMD Execution Model. *Journal of Parallel and Distributed Computing*, 29(2):196–210, September 1995.
- [Ouyang and Heiser 1995] Ouyang, J. and Heiser, G. Checkpointing and Recovery for Distributed Shared Memory Applications. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (IWOOS'95)*, pages 191–199, August 1995.
- [Park and Saavedra 1996] Park, D. and Saavedra, R. H. Adaptive Granularity: Transparent Integration of Fine- and Coarse-grain Communication. In *Proc. of the 1996 Conf. on Parallel Architectures and Compilation Techniques*, pages 260–268, October 1996.

- [Park *et al.* 1997] Park, D., Saavedra, R. H., and Moon, S. Adaptive Granularity: Transparent Integration of Fine- and Coarse-grain Communication. *Int'l Journal of Parallel Programming*, 25(5):419–446, October 1997.
- [Park *et al.* 1996] Park, T., Chou, S. B., and Yeom, H. Y. An Improved Logging and Checkpointing Scheme for Recoverable Distributed Shared Memory. In *Proc. of the 2nd Asian Computing Science Conference (ASIAN'96)*, pages 74–83, December 1996.
- [Park *et al.* 1997] Park, T., Chou, S. B., and Yeom, H. Y. An Efficient Logging Scheme for Recoverable Distributed Shared Memory. In *Proc. of the 17th Int'l Conf. on Distributed Computing Systems (ICDCS-17)*, May 1997.
- [Park and Yeom 1997] Park, T. and Yeom, H. Y. Recoverable Distributed Shared Memory System with Reduced Stable Logging. In *Proc. of the 10th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-97)*, October 1997.
- [Park and Yeom 1998] Park, T. and Yeom, H. Y. An Efficient Logging Scheme for Lazy Release Consistent Distributed Shared Memory System. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 670–674, March 1998.
- [Ramachandran and Singhal 1994] Ramachandran, M. and Singhal, M. On the Synchronization Mechanisms in Distributed Shared Memory Systems. Technical Report OSU-CISRC-10/94-TR54, Computer and Information Sciences Research Center, The Ohio State University, October 1994.
- [Ramachandran and Singhal 1995] Ramachandran, M. and Singhal, M. Decentralized Semaphore Support in a Virtual Shared Memory System. *The Journal of Supercomputing*, 9(1–2):51–70, 1995.
- [Richard III and Singhal 1993] Richard III, G. G. and Singhal, M. Using Logging and Asynchronous Checkpointing to Implement Recoverable Distributed Shared Memory. In *Proc. of the 12th Symp. on Reliable Distributed Systems (SRDS'93)*, pages 58–67, October 1993.
- [Richard III 1994] Richard III, G. G. Techniques for Process Recovery in Message-Passing and Distributed Shared Memory Systems. PhD thesis, Department of Computer and Information Sciences, The Ohio State University, December 1994.
- [Schoinas *et al.* 1994] Schoinas, I., Falsafi, B., Lebeck, A. R., Reinhardt, S. K., Larus, J. R., and Wood, D. A. Fine-grain Access Control for Distributed Shared Memory. In *Proc. of the 6th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVI)*, pages 297–307, October 1994.
- [Shah 1997] Shah, G. Fault Tolerance and Scalability in DSM Coherence Protocols—A Simulation Approach. Master's thesis, Dept. of Computer Science, University of California at Riverside, June 1997.
- [Silva *et al.* 1996] Silva, L. M., Silva, J. G., and Chapple, S. Portable Transparent Checkpointing for Distributed Shared Memory. In *Proc. of the Fifth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-5)*, pages 422–431, August 1996.
- [Silva and Silva 1996] Silva, L. M. and Silva, J. G. A Checkpointing Facility for an Heterogeneous DSM System. In *Proc. of the 9th Int'l Conf. on Parallel and Distributed Computing Systems (PDCS-96)*, pages 554–559, September 1996.
- [Silva and Silva 1997] Silva, L. M. and Silva, J. G. Checkpointing Distributed Shared Memory. *The Journal of Supercomputing*, 11(2):137–158, October 1997.
- [Silva *et al.* 1997] Silva, L. M., Chapple, S., and Silva, J. G. Implementation and Performance of DSMPI. *Scientific Programming*, 6(2):210–214, Summer 1997.
- [Speight and Bennett 1998a] Speight, W. E. and Bennett, J. K. Using Multicast and Multithreading to Reduce Communication in Software DSM Systems. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, pages 312–322, February 1998.

- [Speight and Bennett 1998b] Speight, W. E. and Bennett, J. K. Reducing Coherence-Related Communication in Software Distributed Shared Memory Systems. Technical Report ECE TR-98-03, Electrical and Computer Engineering Department, Rice University, 1998.
- [Speight *et al.* 1998] Speight, W. E., Abdel-Shafi, H., and Bennett, J. K. An Integrated Shared-Memory/Message Passing API for Cluster-Based Computing. Technical Report ECE TR-98-06, Electrical and Computer Engineering Department, Rice University, 1998.
- [Speight *et al.* 1999] Speight, W. E., Abdel-Shafi, H., and Bennett, J. K. Realizing the Performance Potential of the Virtual Interface Architecture. In *Proc. of the 13th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'99)*, June 1999.
- [Stumm and Zhou 1990] Stumm, M. and Zhou, S. Fault Tolerant Distributed Shared Memory. In *Proc. of the Second IEEE Symp. on Parallel and Distributed Processing*, pages 719–724, December 1990.
- [Suri *et al.* 1995] Suri, G., Janssens, B., and Fuchs, W. K. Reduced Overhead Logging for Rollback Recovery in Distributed Shared Memory. In *Proc. of the 25th Annual Int'l Symp. on Fault-Tolerant Computing (FTCS-25)*, pages 279–288, June 1995.
- [Tam and Hsu 1990a] Tam, V.-O. and Hsu, M. Token Transactions: Managing Fine-Grained Migration of Data. In *Proc. of the 9th ACM Symp. on Principles of Database Systems*, April 1990.
- [Tam and Hsu 1990b] Tam, V.-O. and Hsu, M. Fast Recovery in Distributed Shared Virtual Memory Systems. In *Proc. of the 10th Int'l Conf. on Distributed Computing Systems (ICDCS-10)*, pages 38–45, May 1990.
- [Tam 1991] Tam, V.-O. Transaction Management in Data Migration Systems. PhD thesis, Department of Computer Science, Harvard University, January 1991.
- [Tzeng and Wallach 1996] Tzeng, N.-F. and Wallach, S. J. Issues on the Architecture and the Design of Distributed Shared Memory Systems. In *Proc. of the Int'l Conf. on Computer Design—VLSI in Computers and Processors*, pages 60–61, October 1996.
- [Tzeng and Kongmunvattana 1997] Tzeng, N.-F. and Kongmunvattana, A. Distributed Shared Memory Systems with Improved Barrier Synchronization and Data Transfer. In *Proc. of the 11th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'97)*, pages 148–155, July 1997.
- [Van Asche 1995] Van Asche, B. DSM Under Mach: A Quantitative Approach. In *Proc. of Parallel Computing: State-of-the-Art and Perspectives Conf.*, pages 463–470, September 1995.
- [Vochteloo *et al.* 1993] Vochteloo, J., Russell, S., and Heiser, G. Capability-Based Protection in a Persistent Global Virtual Memory System. Technical Report SCS&E Report 9303, School of Computer Science and Engineering, The University of New South Wales, 1993.
- [Wilkinson 1993] Wilkinson, T. J. Implementing Fault Tolerance in a 64-bit Distributed Operating System. PhD thesis, Department of Computer Science, City University, U.K., July 1993.
- [Wortman *et al.* 1994] Wortman, D. B., Zhou, S., and Fink, S. Automating Data Conversion for Heterogeneous Distributed Shared Memory. *Software—Practice and Experience*, 24(1):111–125, January 1994.
- [Wu and Fuchs 1989] Wu, K.-L. and Fuchs, W. K. Recoverable Distributed Shared Virtual Memory: Memory Coherence and Storage Structures. In *Proc. of the 19th Annual Int'l Symp. on Fault-Tolerant Computing (FTCS-19)*, pages 520–527, June 1989.
- [Wu and Fuchs 1990] Wu, K.-L. and Fuchs, W. K. Recoverable Distributed Shared Memory. *IEEE Transactions on Computers*, 39(4):460–469, April 1990.
- [Yu and Cox 1996] Yu, W. M. and Cox, A. L. Conservative Garbage Collection on Distributed Shared Memory Systems. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 402–410, May 1996.

- [Zhou *et al.* 1990] Zhou, S., Stumm, M., and McInerney, T. Extending Distributed Shared Memory to Heterogeneous Environments. In *Proc. of the 10th Int'l Conf. on Distributed Computing Systems (ICDCS-10)*, May 1990.
- [Zhou *et al.* 1992] Zhou, S., Stumm, M., Li, K., and Wortman, D. Heterogeneous Distributed Shared Memory. *IEEE Trans. on Parallel and Distributed Systems*, 3(5):540–554, September 1992.

Miscellaneous

- [Adve *et al.* 1991] Adve, S. V., Hill, M. D., and Vernon, M. Comparison of Hardware and Software Cache Coherence Schemes. In *Proc. of the 18th Annual Int'l Symp. on Computer Architecture (ISCA'91)*, pages 298–308, May 1991.
- [Adve *et al.* 1996] Adve, S. V., Cox, A. L., Dwarkadas, S., Rajamony, R., and Zwaenepoel, W. A Comparison of Entry Consistency and Lazy Release Consistency Implementations. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, pages 26–37, February 1996.
- [Agrawal *et al.* 1993] Agrawal, D., Choy, M., Leong, H.-V., and Singh, A. K. Evaluating Weak Memories with Maya. Technical Report TRCS93-23, Department of Computer Science, University of California at Santa Barbara, 1993.
- [Agrawal *et al.* 1994a] Agrawal, D., Choy, M., Leong, H.-V., and Singh, A. K. Investigating Weak Memories using Maya. In *Proc. of the Third IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-3)*, pages 123–130, April 1994.
- [Agrawal *et al.* 1994b] Agrawal, D., Choy, M., Leong, H.-V., and Singh, A. K. Maya: A Simulation Platform for Distributed Shared Memories. In *Proc. of the 8th Workshop on Parallel and Distributed Simulation (PADS'94)*, pages 151–155, July 1994.
- [Alkhatib *et al.* 1994] Alkhatib, H. S., Li, Q., Jou, C.-J., Chen, T., and Arefeh, H. DICE—A Distributed Integrated Computing Environment for Multi-threaded Parallel Processing. In *Proc. of the Third Int'l Conf. on Systems Integration*, volume 1, pages 612–621, August 1994.
- [Amza *et al.* 1997] Amza, C., Cox, A. L., Ramajamni, K., and Zwaenepoel, W. Tradeoffs between False Sharing and Aggregation in Software Distributed Shared Memory. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 90–99, June 1997.
- [Ananthanarayanan *et al.* 1993] Ananthanarayanan, R., Ahamad, M., and LeBlanc, Jr., R. J. Coherence, Synchronization and Statesharing in Distributed Shared Memory Applications. In *Proc. of the 1993 Int'l Conf. on Parallel Processing (ICPP'93)*, August 1993.
- [Andre and Priol 1992] Andre, F. and Priol, T. Programming Distributed Memory Parallel Computers without Explicit Message Passing. In *Proc. of the Scalable High-Performance Computing Conf. (SHPCC'92)*, pages 90–97, April 1992.
- [Arantes and Sato 1998] Arantes, L. B. and Sato, L. M. CPAR-DSM: A Support for Parallel Programming on Top of DSM. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 859–866, July 1998.
- [Arantes *et al.* 1999a] Arantes, L. B., Folliot, B., and Sens, P. An approach for a Multi-LAN DSM Based on Lazy Release Consistency. In *Proc. of the 3rd European Research Seminar on Advances in Distributed Systems (ERSADS'99)*, April 1999.
- [Arantes *et al.* 1999b] Arantes, L., Folliot, B., and Sens, P. A Customized Logical Clock for Timestamp-based Relaxed Consistency DSM Systems. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.

- [Arnold and Mohay 1992] Arnold, D. and Mohay, G. A Typed Linda Kernel for Helios. In *Proc. of the 5th Australian Transputer and OCCAM User Group Conference*, pages 7–12, November 1992.
- [Assenmacher *et al.* 1995] Assenmacher, H., Breitbart, T., Buhler, P., Hubsch, V., Peine, H., and Schwarz, R. Parallel Programming in PANDA. *The Journal of Supercomputing*, 9(1–2):71–90, 1995.
- [Attiya *et al.* 1993] Attiya, H., Chaudhuri, S., Friedman, R., and Welch, J. Shared Memory Consistency Conditions for Non-Sequential Execution: Definitions and Programming Strategies. In *Proc. of the 5th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'93)*, pages 241–250, June 1993.
- [Attiya and Friedman 1996] Attiya, H. and Friedman, R. Limitations of Fast Consistency Conditions for Distributed Shared Memories. *Information Processing Letters*, 57(5):243–248, March 1996.
- [Attiya *et al.* 1998] Attiya, H., Chaudhuri, S., Friedman, R., and Welch, J. Shared Memory Consistency Conditions for Non-Sequential Execution: Definitions and Programming Strategies. *SIAM Journal of Scientific Computing*, 27(1):65–89, February 1998.
- [Badouel *et al.* 1996] Badouel, D., Priol, T., and Renambot, L. SVMVIEW: A Performance Tuning Tool for DSM-Based Parallel Computers. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 98–105, August 1996.
- [Bai *et al.* 1995] Bai, G., Amano, H., and Makinouchi, A. WAKASHI/C: A Storage System for Multimedia Database. *Systems and Computers in Japan*, 26(7):13–23, July 1995.
- [Balaniuk 1997] Balaniuk, A. Multiple Memory Consistency Models on a SVM Parallel Programming Environment. In *Proc. of the Int'l Conf. on Principles of Distributed Systems (OPODIS'97)*, December 1997.
- [Bang and Cho 1995] Bang, D. W. and Cho, Y. K. Distributed Shared Memory for Function-Grained Graph Reduction Machine. In *Proc. of the 3rd EUROMICRO Workshop on Parallel and Distributed Processing (PDP'95)*, pages 148–155, January 1995.
- [Bar-Lev *et al.* 1998] Bar-Lev, A., Itzkovitz, A., and Schuster, A. R. A. Parallel Vertex-to-vertex Radiosity on a Distributed Shared Memory. In *Proc. of the Fifth Int'l Symposium on Solving Irregularly Structured Problems in Parallel (IRREGULAR '98)*, August 1998.
- [Barriga *et al.* 1996] Barriga, L., Brorsson, M., and Ayani, R. A Model for Parallel Simulation of Distributed Shared Memory. In *Proc. of the 4th Int'l Workshop on Modeling, Analysis, and Simulation of Computers and Telecommunication Systems (MASCOTS'96)*, pages 179–184, February 1996.
- [Baylor *et al.* 1997] Baylor, S., Ekanadham, K., Jann, J., Lim, B.-H., and Pattnaik, P. Lazy Home Migration for Distributed Shared Memory Systems. In *Proc. of the 4th Int'l Conf. on High Performance Computing (HiPC'97)*, December 1997.
- [Bennett *et al.* 1992] Bennett, J. K., Carter, J. B., and Zwaenepoel, W. Toward Large-Scale Shared Memory Multiprocessors. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*, pages 281–300. Kluwer Academic Publishers, 1992.
- [Bernard and Hamma 1994] Bernard, G. and Hamma, S. Remote Memory Paging in Networks of Workstations. In *Proc. of the SUUG Int'l Conf. on Open Systems: Solutions for Open World*, April 1994.
- [Bernaschi *et al.* 1992] Bernaschi, M., Blount, M., Squazzero, P., and Vitaletti, M. Distributed Shared Virtual Memory on RISC System/6000 Clusters and Large Scale Computations: Two Case Studies. *Future Generation Computer Systems*, 8(1–3):235–242, July 1992.
- [Berrendorf 1992] Berrendorf, R. Memory Access in Shared Virtual Memory—*Poster Paper*. In *Proc. of Parallel Architectures and Languages Europe (PARLE'92)*, number 634 in Lecture Notes in Computer Science, pages 785–786. Springer-Verlag, September 1992.

- [Berrendorf *et al.* 1994] Berrendorf, R., Gerndt, M., Lahjomri, Z., and Priol, T. A Comparison of Shared Virtual Memory and Message Passing Programming Techniques Based on a Finite Element Application. In *Proc. of the 3rd Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'94)*, pages 461–472, June 1994.
- [Bershad 1993] Bershad, B. N. Practical Considerations for Non-Blocking Concurrent Objects. In *Proc. of the 13th Int'l Conf. on Distributed Computing Systems (ICDCS-13)*, pages 264–273, May 1993.
- [Bhatti 1997a] Bhatti, A. M. Evaluation of All-Software Conventional Distributed Shared Memory on NOWs based on High Speed Networks. In *Proc. of the Cluster Computing Conference*, March 1997.
- [Bhatti 1997b] Bhatti, A. M. Data Block Size, Software Access Control and Release Consistent Distributed Shared Memory on Reliable Networks. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'97)*, June 1997.
- [Bhatti 1997c] Bhatti, A. M. SNOW: A Stateless Low-Overhead All Software Distributed Shared Memory System. In *Proc. of the 11th Annual Symp. on High Performance Computing Systems and Applications (HPCS'97)*, pages 611–622, July 1997.
- [Bhatti 1998] Bhatti, A. M. Design and Evaluation of Shared Memory for Workstation Networks. PhD thesis, Department of Computer Science, Boston University, January 1998.
- [Bianchini *et al.* 1996] Bianchini, R., Kontothanassis, L. I., Pinto, R., De Maria, M., Abud, M., and Amorim, C. L. Hiding Communication Latency and Coherence Overhead in Software DSMs. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOS VII)*, pages 198–209, October 1996.
- [Bianchini *et al.* 1997] Bianchini, R., Carrera E., E. V., and Kontothanassis, L. I. The Interaction of Parallel Programming Constructs and Coherence Protocols. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 69–79, June 1997.
- [Bianchini *et al.* 1998a] Bianchini, R., Carrera E., E. V., and Kontothanassis, L. Evaluating the Effect of Coherence Protocols on the Performance of Parallel Programming Constructs. *Int'l Journal of Parallel Programming*, 26(2):143–182, April 1998.
- [Bianchini *et al.* 1998b] Bianchini, R., Pinto, R., and Amorim, C. L. Data Prefetching for Software DSMs. In *Proc. of the Int'l Conf. on Supercomputing'98*, July 1998.
- [Bilas *et al.* 1996a] Bilas, A., Iftode, L., and Singh, J. P. Shared Virtual Memory across SMP Nodes Using Automatic Update: Protocols and Performance. In *Proc. of the Sixth Workshop on Scalable Shared Memory Multiprocessors*, October 1996.
- [Bilas *et al.* 1996b] Bilas, A., Iftode, L., and Singh, J. P. Supporting A Coherent Shared Address Space Across SMP Nodes: An Application-Driven Investigation. In *Proc. of IMA Workshop on Parallel Algorithms and Parallel Systems*, November 1996.
- [Bilas and Singh 1997] Bilas, A. and Singh, J. P. The Effects of Communication Parameters on End Performance of Shared Virtual Memory Clusters. In *Proc. of Supercomputing'97*, November 1997.
- [Bilas *et al.* 1998a] Bilas, A., Iftode, L., Samanta, R., and Singh, J. P. Supporting a Coherent Shared Address Space Across SMP Nodes: An Application-driven Investigation, volume 105 of *IMA Volumes in Mathematics and its Applications*, pages 15–59. Springer-Verlag, 1998.
- [Bilas *et al.* 1998b] Bilas, A., Iftode, L., and Singh, J. P. Evaluation of Hardware Support for Next Generation Shared Virtual Memory clusters. In *Proc. of the 12th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'98)*, July 1998.
- [Bilas 1998] Bilas, A. Improving the Performance of Shared Virtual Memory on System Area Networks. PhD thesis, Dept. of Computer Science, Princeton University, November 1998.

- [Bilas *et al.* 1999] Bilas, A., Liao, C., and Singh, J. P. Accelerating Shared Virtual Memory using Commodity NI Support to Avoid Asynchronous Message Handling. In *Proc. of the 26th Annual Int'l Symp. on Computer Architecture (ISCA'99)*, May 1999.
- [Bode and Hackenberg 1994] Bode, A. and Hackenberg, R. An Environment for Investigating Shared Virtual Memory Behavior. In *Proc. of the 8th Int'l Parallel Processing Symp. (IPPS'94)*, April 1994.
- [Bodorik and Jutla 1997] Bodorik, P. and Jutla, D. N. Multi-view Memory to Support OS Locking for Transaction Systems. In *Proc. of the Int'l Database Engineering and Applications Symp. (IDEAS'97)*, pages 309–318, August 1997.
- [Bodorik *et al.* 1999] Bodorik, P., Jutla, D. N., Slonim, J., and Agarwal, A. Locking with Different Granularities for Reads and Writes in an MVM System. In *Proc. of the Int'l Database Engineering and Applications Symp. (IDEAS'99)*, August 1999.
- [Bolosky 1993] Bolosky, W. J. Software Coherence in Multiprocessor Memory Systems. PhD thesis, Department of Computer Science, University of Rochester, 1993.
- [Brorsson 1989] Brorsson, M. A Decentralized Virtual Memory Scheme Implemented on an Emulated MIMD Multiprocessor. In *Proc. of the 22th Hawaii Int'l Conf. on System Sciences (HICSS-22)*, pages 286–295, January 1989.
- [Brorsson and Stenstrom 1996] Brorsson, M. and Stenstrom, P. Characterizing and Modelling Shared Memory Accesses in Multiprocessor Programs. *Parallel Computing*, 22(6):869–893, October 1996.
- [Brorsson and Kral 1998] Brorsson, M. and Kral, M. Visualization for Performance Tuning of DVSM System Applications on Networks of Workstations. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, pages 532–541, January 1998.
- [Brunie *et al.* 1996] Brunie, L., Lefevre, L., and Reymann, O. Execution Analysis of DSM Applications: A Distributed and Scalable Approach. In *Proc. of SIGMETRICS Symp. on Parallel and Distributed Tools*, pages 51–60, May 1996.
- [Brunie and Lefevre 1996] Brunie, L. and Lefevre, L. New Propositions to Improve the Efficiency and Scalability of DSM Systems. In *Proc. of IEEE 2nd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'96)*, pages 356–364, June 1996.
- [Brunie and Reymann 1996a] Brunie, L. and Reymann, O. Integration of Performance Evaluation Facilities into Distributed Shared Memory Based Programming Environments. In *Proc. of Telecommunication Distribution Parallelism (TDP'96)*, June 1996.
- [Brunie and Reymann 1996b] Brunie, L. and Reymann, O. Concepts and Functionalities of the DOSMOS-Trace Monitoring Tool. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 74–77, August 1996.
- [Brunie and Lefevre 1996] Brunie, L. and Lefevre, L. A DSM-Based Structural Programming Environment for Distributed and Parallel Processing. In *Proc. of the 3rd Int'l Conf. on High Performance Computing (HIPC'96)*, pages 469–473, December 1996.
- [Campos 1993] Campos, A. E. Distributed, Garbage-Collected, Persistent, Virtual Address Spaces. PhD thesis, Dept. of Computer Science, Princeton University, June 1993.
- [Cappello *et al.* 1999] Cappello, F., Richard, O., and Etiemble, D. Performance Evaluation of Two Programming Models for a Cluster of PC Biprocessors. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, June 1999.
- [Carter and Zwaenepoel 1989] Carter, J. B. and Zwaenepoel, W. Optimistic Implementation of Bulk Data Transfer Protocols. In *Proc. of the 1989 ACM SIGMETRICS Conference on Performance Measurement, Modeling, and Evaluation*, pages 61–69, May 1989.

- [Carter *et al.* 1998a] Carter, J. B., Hsieh, W., Swanson, M., Davis, A., Parker, M., Schaelicke, L., Stoller, L., Tateyama, T., and Zhang, L. Memory System Support for Irregular Applications. In *Proc. of the Fourth Workshop on Languages, Compilers, and Run-time Systems for Scalable Computers (LCR'98)*, May 1998.
- [Carter *et al.* 1998b] Carter, N. P., Dally, W. J., Chang, A., Keckler, S. W., and Lee, W. S. Integrated Processor Mechanisms for Software Shared Memory. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Chandra *et al.* 1996] Chandra, S., , and Larus, J. R. HPF on Fine-Grain Distributed Shared Memory: Early Experience. In *Proc. of the 9th Int'l Workshop on Languages and Compilers for Parallel Computing (LCPC'96)*, pages 450–465, August 1996.
- [Chandra and Larus 1997] Chandra, S. and Larus, J. R. Optimizing Communication in HPF Programs for Fine-Grain Distributed Shared Memory. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 100–111, June 1997.
- [Chandra *et al.* 1997] Chandra, S., Larus, J. R., Dahlin, M., Richards, B., Wang, R. Y., and Anderson, T. E. Experience with a Language for Writing Coherence Protocols. In *Proc. of the USENIX Conference on Domain-Specific Languages (DSL)*, October 1997.
- [Chang *et al.* 1997] Chang, J. B., Shieh, C. K., and Ueng, J. C. C-thru: A Transparent DSM System Using User-Level Approach. In *Proc. of the 1997 Workshop on Distributed System Technologies & Applications*, pages 652–659, May 1997.
- [Chang *et al.* 1998] Chang, J.-B., Tsai, Y., Shieh, C., and Chung, P. An Efficient Thread Architecture for a Distributed Shared Memory on Symmetric Multiprocessor Clusters. In *Proc. of the 1998 Int'l Conf. on Parallel and Distributed Systems (ICPADS'98)*, pages 816–823, December 1998.
- [Cheriton and Duda 1995] Cheriton, D. R. and Duda, K. J. Logged Virtual Memory. In *Proc. of the 15th ACM Symp. on Operating Systems Principles (SOSP-15)*, pages 26–39, December 1995.
- [Cheriton and Kutter 1996] Cheriton, D. R. and Kutter, R. A. Optimized Memory-Based Messaging: Leveraging the Memory System for High-Performance Communication. *Computing Systems*, 9(3):179–215, summer 1996.
- [Cholvi-Juan and Bernabeu-Auban 1996] Cholvi-Juan, V. and Bernabeu-Auban, J. M. Correctness Proof for a Distributed Memory System. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 526–531, August 1996.
- [Chong 1993] Chong, Y.-K. Effects of Memory Consistency Models on Multithreaded Multiprocessor Performance. Master's thesis, Department of Electrical Engineering—Systems, University of Southern California, 1993.
- [Chong and Hwang 1994] Chong, Y.-K. and Hwang, K. Evaluation of Relaxed Memory Consistency Models for Multithreaded Multiprocessors. In *Proc. of the 1994 Int'l Conf. on Parallel and Distributed Systems (ICPADS'94)*, pages 474–480, December 1994.
- [Chowell 1997] Choweller, D. Enhancements to Synchronization Mechanisms for Distributed Systems. Master's thesis, Dept. of Computer Science, University of California at Riverside, March 1997.
- [Clancey and Francioni 1990] Clancey, P. M. and Francioni, J. M. Distribution of Pages in a Distributed Virtual Memory. In *Proc. of the 1990 Int'l Conf. on Parallel Processing (ICPP'90)*, volume II, pages 258–265, August 1990.
- [Clematis and Gresso 1995] Clematis, A. and Gresso, E. HeterEL—A Server for Programming Parallel Heterogeneous Systems. In *Proc. of the 3rd EUROMICRO Workshop on Parallel and Distributed Processing (PDP'95)*, pages 472–479, January 1995.

- [Clematis and Corona 1997] Clematis, A. and Corona, A. Performance Analysis of SPMD Algorithms on a Network of Workstations with Virtual Shared Memory. In *Proc. of Parallel Computing: Fundamentals, Applications and New Directions Conf.*, pages 657–664, September 1997.
- [Clemencon *et al.* 1996] Clemencon, C., Mukherjee, B., and Schwan, K. Distributed Shared Abstractions (DSA) on Multiprocessors. *IEEE Trans. on Software Engineering*, 22(2):132–152, February 1996.
- [Cohn *et al.* 1989] Cohn, D. L., Delaney, W., and Tracey, K. Structured Shared Memory Among Heterogeneous Machines in ARCADE. In *Proc. of the First IEEE Symp. on Parallel and Distributed Processing*, pages 378–379, December 1989.
- [Cohn *et al.* 1991] Cohn, D. L., Greenawalt, P. M., Casey, M. R., and Stevenson, M. P. Using Kernel-Level Support for Distributed Shared Data. In *Proc. of the USENIX Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-II)*, pages 247–259, March 1991.
- [Cohn *et al.* 1992] Cohn, D. L., Banerji, A., Greenawalt, P. M., Casey, M. R., and Kulkarni, D. C. Workstation Cooperation Through a Typed Distributed Shared Memory Abstraction. In *Proc. of the 3rd Workshop on Workstation Operating Systems (WWOS-III)*, pages 70–74, April 1992.
- [Colvin and Cormen 1998] Colvin, A. and Cormen, T. H. ViC*: A Compiler for Virtual-Memory C*. In *Proc. of the 3rd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'98)*, pages 23–33, March 1998.
- [Comer and Griffioen 1992] Comer, D. and Griffioen, J. Efficient Order-Dependent Communication in a Distributed Virtual Memory Environment. In *Proc. of the Symp. on Experiences with Distributed and Multiprocessor Systems (SEDMS-III)*, pages 249–262, March 1992.
- [Condon *et al.* 1999] Condon, A., Hill, M., Plakal, M., and Sorin, D. Using Lamport Clocks to Reason About Relaxed Memory Models. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Cordsen and Schroder-Preikschat 1998] Cordsen, J. and Schroder-Preikschat, W. On the Symbiosis of Memory and Communication in the Programming of Parallel Applications. *Future Generation Computer Systems*, 13(4–5):373–383, March 1998.
- [Cordsen *et al.* 1998] Cordsen, J., Nolte, J., and Schroder-Preikschat, W. Experiences Developing a Virtual Shared Memory System Using High-level Object Paradigms. In *Proc. of the 12th European Conf. on Object Oriented Programming (ECOOP'98)*, pages 285–306, July 1998.
- [Cornilleau *et al.* 1995] Cornilleau, T., Gressier, E., and Ortega, M. I. A Multiconsistency Memory Protocol Test Environment on Chorus. In *Proc. of the European Research Seminar on Advances in Distributed Systems (ERSADS'95)*, April 1995.
- [Costa *et al.* 1996] Costa, M., Guedes, P., Sequeira, M., Neves, N., and Castro, M. Lightweight Logging for Lazy Release Consistent Distributed Shared Memory. In *Proc. of the 2nd Symp. on Operating Systems Design and Implementation (OSDI'96)*, pages 59–73, October 1996.
- [Costa *et al.* 1997] Costa, V. S., Bianchini, R., and Dutra, I. C. Evaluating the Impact of Coherence Protocols on Parallel Logic Programming Systems. In *Proc. of the 5th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'97)*, January 1997.
- [Coulouris *et al.* 1994] Coulouris, G., Dollimore, J., and Kindberg, T. *Distributed Systems—Concepts and Design*, 2nd Ed., chapter 17, pages 517–544. Addison-Wesley Publishers Ltd., 1994.
- [Cox *et al.* 1994] Cox, A. L., Dwarkadas, S., Keleher, P., Lu, H., Rajamony, R., and Zwaenepoel, W. Software Versus Hardware Shared-Memory Implementation: A Case Study. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 106–117, April 1994.

- [Chu and Kedem 1996] Chu, C. and Kedem, Z. Techniques for Improving the Performance of Multiple Writer Protocols in Distributed Shared Memory Systems. In *Proc. of the 3rd Int'l Conf. on High Performance Computing (HiPC'96)*, 1996.
- [Dahlgren and Landin 1997] Dahlgren, F. and Landin, A. Reducing the Replacement Overhead in Bus-Based COMA Multiprocessors. In *Proc. of the 3rd IEEE Symp. on High-Performance Computer Architecture (HPCA-3)*, February 1997.
- [Dai and Panda 1996] Dai, D. and Panda, D. K. Reducing Cache Invalidation Overheads in Wormhole Routed DSMs Using Multidestination Message Passing. In *Proc. of the 1996 Int'l Conf. on Parallel Processing (ICPP'96)*, volume 1, pages 138–145, August 1996.
- [Dai and Panda 1997] Dai, D. and Panda, D. K. How Much Does Network Contention Affect Distributed Shared Memory Performance? In *Proc. of the 1997 Int'l Conf. on Parallel Processing (ICPP'97)*, pages 454–461, August 1997.
- [Dearle and Hulse 1995] Dearle, A. and Hulse, D. On Page-based Optimistic Process Checkpointing. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (IWOOS'95)*, pages 24–32, August 1995.
- [Dechamboux *et al.* 1996] Dechamboux, P., Hagimont, D., and Lopez, M. Using a Distributed Shared Memory for Implementing Efficient Information Mediators. In *Proc. of the High-Performance Computing and Networking Europe 1996 (HPCN'96)*, pages 909–912, April 1996.
- [Delaney *et al.* 1989] Delaney, W. P., Tracey, K. M., and Cohn, D. L. Unifying Kernel-level and Language-level Approaches to Distributed Shared Data. Technical Report TR-891101, Dept. of Electrical and Computer Engineering, University of Notre Dame, 1989.
- [DeMatteis 1996] DeMatteis, C. K. A Fault Tolerant Distributed Shared Memory System: Reliable Mirage+. Master's thesis, Dept. of Computer Science, University of California at Riverside, March 1996.
- [Desbiens *et al.* 1994] Desbiens, J., Toulouse, M., Lavoie, M., Pouzyreff, S., Raymond, P., and Tamazouzt, T. An Actor Based Programming System for Heterogeneous Processing. In *Proc. of Parallel Architectures and Languages Europe (PARLE'94)*, pages 753–756, July 1994.
- [Dietz and Mattox 1997] Dietz, H. G. and Mattox, T. I. Managing Polyatomic Coherence and Races with Replicated Shared Memory. *Newsletter of the IEEE CS Technical Committee on Computer Architecture*, pages 53–58, March 1997.
- [Dimitrelos and Halatsis 1995a] Dimitrelos, D. and Halatsis, C. On the Distribution of Directory Information in a Software Controlled Distributed Shared Memory System. In *Proc. of the Workshop on Parallel Programming and Computation (ZEUS'95)*, pages 75–89, 1995.
- [Dimitrelos and Halatsis 1995b] Dimitrelos, D. and Halatsis, C. SVS: Can the Shared Variable Paradigm Exist in Massively Parallel Multiprocessor Architectures? In *Proc. of IEEE 1st Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'95)*, volume 1, pages 328–331, 1995.
- [Dormanns *et al.* 1997a] Dormanns, M., Sprangers, W., Ertl, H., and Bemmerl, T. Performance Potential of an SCI Workstation Cluster for Grid-Based Scientific Codes. In *Proc. of the High Performance Computing 1997 (HPC'97)*, pages 226–231, April 1997.
- [Dormanns *et al.* 1997b] Dormanns, M., Sprangers, W., Ertl, H., and Bemmerl, T. A Programming Interface for NUMA Shared-Memory Clusters. In *Proc. of the High-Performance Computing and Networking Europe 1997 (HPCN'97)*, pages 698–707, April 1997.
- [Doroshenko 1991] Doroshenko, A. E. Methods for Increasing the Efficiency and Reliability of Macropipelined Programs with External Memory. *Programming and Computer Software*, 17(6):361–369, Nov–Dec. 1991.

- [Dowd and Chu 1994] Dowd, P. W. and Chu, J. Photonic Architectures for Distributed Shared Memory Multiprocessors. In *Proc. of the 1st Int'l Workshop on Massively Parallel Processing Using Optical Interconnections*, pages 151–161, April 1994.
- [Dreier and Ungerer 1995] Dreier, B. and Ungerer, T. Implementing Distributed Shared Memory Based on DCE. In *Proc. of the 3rd EUROMICRO Workshop on Parallel and Distributed Processing (PDP'95)*, pages 84–90, January 1995.
- [Dreier *et al.* 1996] Dreier, B., Zahn, M., and Ungerer, T. Rthreads—A Uniform Interface for Parallel and Distributed Programming. In *Proc. of the 2nd Int'l Conference on Massively Parallel Computing Systems (MPCS'96)*, pages 530–534, May 1996.
- [Dreier *et al.* 1998a] Dreier, B., Zahn, M., and Ungerer, T. Parallel and Distributed Programming with Pthreads and Rthreads. In *Proc. of the 3rd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'98)*, pages 34–40, March 1998.
- [Dreier *et al.* 1998b] Dreier, B., Zahn, M., and Ungerer, T. The Rthreads Distributed Shared Memory System. In *Proc. of the 3rd Int'l Conference on Massively Parallel Computing Systems (MPCS'98)*, April 1998.
- [Dubnicki *et al.* 1996] Dubnicki, C., Iftode, L., Felten, E. W., and Li, K. Software Support for Virtual Memory Mapped Communication. In *Proc. of the 10th Int'l Parallel Processing Symp. (IPPS'96)*, April 1996.
- [Dubois *et al.* 1995] Dubois, M., Skeppstedt, J., and Stenstrom, P. Essential Misses and Data Traffic in Coherence Protocols. *Journal of Parallel and Distributed Computing*, 29(2):108–125, September 1995.
- [Dubrovski 1996] Dubrovski, A. Load Balancing in Distributed Shared Memory Systems. Master's thesis, Computer Science Department, Technion–Israel Institute of Technology, June 1996.
- [Dubrovski *et al.* 1998] Dubrovski, A., Friedman, R., and Schuster, A. Load Balancing in Distributed Shared Memory Systems. *International Journal of Applied Software Technology*, 3:167–202, March 1998.
- [Dwarkadas *et al.* 1994] Dwarkadas, S., Schaffer, A., Cottingham, R. W., Cox, A. L., Keleher, P., and Zwaenepoel, W. Parallelization of General Linkage Analysis Problems. *Human Heredity*, 44:127–141, July 1994.
- [Dwarkadas *et al.* 1999] Dwarkadas, S., Lu, H., Cox, A. L., Rajamony, R., and Zwaenepoel, W. Combining Compile-Time and Run-Time Support for Efficient Software Distributed Shared Memory. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):476–486, March 1999.
- [Erlichson *et al.* 1995] Erlichson, A., Nayfeh, B. A., Olukotun, K., and Singh, J. P. The Benefits of Clustering in Shared-address-space Multiprocessors: An Applications-driven Investigation. In *Proc. of Supercomputing'95*, December 1995.
- [Erlichson *et al.* 1996] Erlichson, A., Nuckolls, N., Chesson, G., and Hennessy, J. L. SoftFLASH: Analyzing the Performance of Clustered Distributed Virtual Shared Memory. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOS VII)*, pages 210–220, October 1996.
- [Eskicioglu 1995] Eskicioglu, M. R. A Comprehensive Bibliography of Distributed Shared Memory. Technical Report TR-95-01, Computer Science Department, University of New Orleans, May 1995.
- [Eskicioglu 1996] Eskicioglu, M. R. A Comprehensive Bibliography of Distributed Shared Memory. *ACM Operating Systems Review*, 30(1):71–96, January 1996.
- [Fang and Ju 1994] Fang, Z. and Ju, J. Standardization: The Key to DSM System Development and Application. In *Proc. of the Third Int'l Conf. on Systems Integration*, volume 1, pages 441–446, August 1994.
- [Fekete *et al.* 1995] Fekete, A., Kaashoek, M. F., and Lynch, N. Implementing Sequentially Consistent Shared Objects Using Broadcast and Point-to-Point Communication. In *Proc. of the 15th Int'l Conf. on Distributed Computing Systems (ICDCS-15)*, pages 439–449, May 1995.

- [Fekete *et al.* 1998] Fekete, A., Kaashoek, M. F., and Lynch, N. Implementing Sequentially Consistent Shared Objects Using Broadcast and Point-to-Point Communication. *Journal of the ACM*, 41(1):35–69, January 1998.
- [Felten and Zahorjan 1991] Felten, E. W. and Zahorjan, J. Issues in Implementation of a Remote Memory Paging System. Technical Report 91-03-09, Dept. of Computer Science, University of Washington, 1991.
- [Fleisch and Hyde 1998] Fleisch, B. D. and Hyde, R. L. High Performance Distributed Objects Using Distributed Shared Memory and Remote Method Invocation. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, pages 574–578, January 1998.
- [Ford *et al.* 1995] Ford, R. W., Nisbet, A. P., and Bull, J. M. User-level VSM Optimization and Its Application. In *Proc. of the Conf. on Applied Parallel Computing (PARA'95)*, pages 223–246, August 1995.
- [Fox 1995] Fox, J. Distributed Shared Memory. *Forth Dimensions*, 17(1):16–19, May–June 1995.
- [Freeh and Andrews 1996] Freeh, V. W. and Andrews, G. R. Dynamically Controlling False Sharing in Distributed Shared Memory. In *Proc. of the Fifth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-5)*, pages 403–411, August 1996.
- [Frietman *et al.* 1997] Frietman, E. E. E., Ernst, R. J., Crosbie, R., and Shimoji, M. Features of Optical Interconnects in Distributed-shared Memory Organized MIMD Architectures: the Ultimate Goal. In *Proc. of the 3rd Int'l Symp. on Parallel Architectures, Algorithms, and Networks (I-SPAN'97)*, pages 37–43, December 1997.
- [Fu and Tzeng 1996] Fu, S. S. and Tzeng, N.-F. Lock Improvement Technique for Release Consistency in Distributed Shared Memory Systems. In *Proc. of the 6th Symp. on the Frontiers of Massively Parallel Computing (Frontiers'96)*, pages 255–262, October 1996.
- [Gao and Sarkar 1997] Gao, G. R. and Sarkar, V. On the Importance of an End-to-End View of Memory Consistency in Future Computer Systems. In *Proc. of the Int'l Symp. on High Performance Computing (ISHPC'97)*, pages 30–41, November 1997.
- [Gerndt 1996] Gerndt, M. Programming Shared Virtual Memory Multiprocessors. In *Proc. of the 4th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'96)*, January 1996.
- [Gerndt and Krumme 1997] Gerndt, M. and Krumme, A. A Rule-based Approach for Automatic Bottleneck Detection in Programs on Shared Virtual Memory Systems. In *Proc. of the 2nd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'97)*, April 1997.
- [Ghughal *et al.* 1998] Ghughal, R., Mokkedem, A., Nalumasu, R., and Gopalakrishnan, G. Using “Test Model-Checking” to Verify the Runway-PA8000 Memory Model. In *Proc. of the 10th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'98)*, pages 231–239, June 1998.
- [Gibbons and Korach 1994] Gibbons, P. B. and Korach, E. On Testing Cache-Coherent Shared Memories. In *Proc. of the 6th ACM Symp. on Parallel Algorithms and Architectures (SPAA'94)*, pages 177–188, June 1994.
- [Giloi 1994] Giloi, W. K. Parallel Supercomputer Architectures and Their Programming Models. *Parallel Computing*, 20(10–11):1443–1470, November 1994.
- [Gomm and Kindler 1993] Gomm, D. and Kindler, E. Causality Based Proof of a Distributed Shared Memory System. In *Parallel Computer Architectures: Theory, Hardware, Software, Applications*, number 732 in Lecture Notes in Computer Science, pages 133–149. Springer-Verlag, 1993.
- [Gontmakher and Schuster 1998] Gontmakher, A. and Schuster, A. Characterizations for Java Memory Behavior. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 682–686, March 1998.
- [Granston and Wishoff 1993] Granston, E. D. and Wishoff, H. Managing Pages in Shared Virtual Memory Systems. In *Proc. of the 7th ACM Int'l Conf. on Supercomputing*, July 1993.

- [Groh and Rudolph 1997] Groh, S. and Rudolph, J. Distributed Operating Systems based on Manager Agents. In *Proc. of the 11th Annual Symp. on High Performance Computing Systems and Applications (HPCS'97)*, pages 623–632, July 1997.
- [Groh *et al.* 1997] Groh, S., Pizka, M., and Rudolph, J. Shadow Stacks—A Hardware-supported DSM for Objects of any Granularity. In *Proc. of IEEE 3rd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'97)*, pages 225–238, December 1997.
- [Groh 1998] Groh, S. An Overview of Distributed Virtual Address Space Management in the Shadow Project. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 230–236, July 1998.
- [Grujic *et al.* 1994] Grujic, A., Tomasevic, M., and Milutinovic, V. A Simulation Study of Hardware-Oriented DSM Approaches. In *Proc. of 1994 IEEE Region 10's 9th Annual Int'l Conference*, volume 1, pages 386–390, August 1994.
- [Grujic *et al.* 1996] Grujic, A., Tomasevic, M., and Milutinovic, V. A Simulation Study of Hardware-Oriented DSM Approaches. *IEEE Parallel and Distributed Technology*, 4(1):74–83, Spring 1996.
- [Grygier and Cin 1994] Grygier, A. and Cin, M. D. Stable Object Storage for Multiprocessors with Distributed Shared Memory. In *Proc. of the 1st Workshop on Object-Oriented Real-Time Dependable Systems*, pages 94–99, October 1994.
- [Gull 1991] Gull, A. Using a Wafer-Scale Component to Create an Efficient Distributed Shared Memory. In *Proc. of the Autumn 1991 EurOpen Conference*, pages 47–65, September 1991.
- [Gull 1993] Gull, A. Cherub: A Hardware Distributed Single Shared Address Space Memory Architecture. PhD thesis, Department of Computer Science, City University, U.K., March 1993.
- [Guyennet and Lapayre 1996] Guyennet, H. and Lapayre, J.-C. A Co-operative Application Management Platform Based on Shared Virtual Memory. In *Proc. of the 3rd Int'l Conf. on High Performance Computing (HiPC'96)*, December 1996.
- [Guyennet *et al.* 1997a] Guyennet, H., Lapayre, J.-C., and Trehel, M. Distributed Shared Memory Layer for Cooperative Work Application. In *Proc. of the 22nd Conference on Local Computer Networks (LCN'97)*, pages 72–78, November 1997.
- [Guyennet *et al.* 1997b] Guyennet, H., Lapayre, J.-C., and Trehel, M. The Pilgrim: A New Consistency Protocol for Distributed Shared Memory. In *Proc. of IEEE 3rd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'97)*, pages 253–264, December 1997.
- [Guyennet *et al.* 1997c] Guyennet, H., Lapayre, J.-C., and Trehel, M. A New Consistency Protocol Implemented in the CALiF System. In *Proc. of the 4th Int'l Conf. on High Performance Computing (HiPC'97)*, pages 82–87, December 1997.
- [Hackenberg 1994a] Hackenberg, R. G. A Test Environment for Investigating Shared Virtual Memory Behavior. In *Proc. of the Parallel Systems Fair*, pages 68–75, April 1994.
- [Hackenberg 1994b] Hackenberg, R. G. MaX—Investigating Shared Virtual Memory. In *Proc. of the Int'l Conf. and Exhibition on High-Performance Computing and Networking*, pages 308–315, April 1994.
- [Hahad *et al.* 1994] Hahad, M., Priol, T., and Erhel, J. Irregular Loop Patterns Compilation on Distributed Shared Memory Multiprocessors. Technical Report RR-2361, INRIA, France, September 1994.
- [Han *et al.* 1997] Han, H., Tseng, C.-W., and Keleher, P. Reducing Synchronization Overhead for Compiler-Parallelized Codes on Software DSMs. In *Proc. of the 10th Int'l Workshop on Languages and Compilers for Parallel Computing (LCPC'97)*, August 1997.
- [Han and Tseng 1998] Han, H. and Tseng, C.-W. Compile-Time Synchronization Optimizations for Software DSMs. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 662–669, March 1998.

- [Han *et al.* 1998] Han, H., Tseng, C.-W., and Keleher, P. Eliminating Barrier Synchronization for Compiler-Parallelized Codes on Software DSMs. *Int'l Journal of Parallel Programming*, 26(5):591–612, October 1998.
- [Hardavellas *et al.* 1998] Hardavellas, N., Kontothanassis, L., Nikhil, R., and Stets, R. Software Cache Coherence with Memory Scaling. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Heddaya *et al.* 1994] Heddaya, A., Park, K., and Sinha, H. Using Warp to Control Network Contention in Mermera. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume II, pages 96–105, January 1994.
- [Heinzle *et al.* 1994] Heinzle, H.-P., Bal, H. E., and Langendoen, K. Implementing Object-Based Distributed Shared Memory on Transputers. In *Proc. of the 1994 World Transputer Congress*, pages 390–405, September 1994.
- [Hellwagner 1990] Hellwagner, H. A Survey of Virtually Shared Memory Schemes. Technical Report TUM-19056, Institute for Informatics, Technical University of Munich, Germany, December 1990.
- [Hellwagner 1991] Hellwagner, H. Virtually Shared Memory Architectures for Scalable Universal Parallel Computers. In *Proc. of the Int'l Symp. on Applied Computer Science and Software*, pages 91–112, September 1991.
- [Hellwagner 1992] Hellwagner, H. On the Practical Efficiency of Randomized Shared Memory. In *Proc. of the 2nd Joint Int'l Conf. on Vector and Parallel Processing (CONPAR'92)*, pages 429–440, September 1992.
- [Hellwagner 1993] Hellwagner, H. Randomized Shared Memory-Concept and Efficiency of a Scalable Shared Memory Scheme. In *Parallel Computer Architectures: Theory, Hardware, Software, Applications*, number 732 in Lecture Notes in Computer Science, pages 102–117. Springer-Verlag, 1993.
- [Hellwagner *et al.* 1997] Hellwagner, H., Karl, W., and Leberecht, M. Enabling a PC Cluster for High-Performance Computing. In *Proc. of the 21st Workshop on Vector and Parallel Computing*, pages 18–23, March 1997.
- [Hemmendinger and Fleckenstein 1992] Hemmendinger, D. and Fleckenstein, C. J. Multiprocessing—Architectural Support for Distributed Shared Memory, volume 35 of *Advances in Computers*, pages 270–285. Academic Press, Inc., 1992.
- [Hennessy 1994] Hennessy, J. L. Distributed Shared Memory: Perspectives on Its Development and Future. In *Proc. of the ARPA High Performance Computing and Communications Symp.*, March 1994.
- [Henskens *et al.* 1992] Henskens, F. A., Broessler, P., Keedy, J. L., and Rosenberg, J. Transparent Distribution Using Two Object Granularities. Presented at *The Workshop on Object Granularity* in association with the European Conf. on Object-Oriented Programming (ECOOP'92), 1992.
- [Henskens 1992] Henskens, F. A. Addressing Moved Modules in a Capability-based Distributed Shared Memory. In *Proc. of the 25th Hawaii Int'l Conf. on System Sciences (HICSS25)*, pages 769–778, January 1992.
- [Hermannsson *et al.* 1994] Hermannsson, G., Li, A., and Wittie, L. EC/DSIM: A Frontend and Simulator for Huge Parallel Systems. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 241–250, January 1994.
- [Hill *et al.* 1995] Hill, M. D., Larus, J. R., and Wood, D. A. Tempest: A Substrate for Portable Parallel Programs. In *Proc. of the 40th IEEE Int'l Computer Conf. (COMPCON Spring'95)*, March 1995.
- [Hill 1998] Hill, M. D. Multiprocessors Should Support Simple Memory Consistency Protocols. *IEEE Computer*, 31(8), August 1998.

- [Hirayama and Honda 1999] Hirayama, H. and Honda, H. Distributed Shared Memory with Log Based Consistency Scheme for Scalable Data Mining (Poster). In *Proc. of the High-Performance Computing and Networking Europe 1999 (HPCN'99)*, April 1999.
- [Ho *et al.* 1998] Ho, C., Ziegler, H., and Dubois, M. In-Memory Directories: Eliminating the Directory Overhead in CC-NUMAs. In *Proc. of the 10th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'98)*, pages 222–230, June 1998.
- [Holt *et al.* 1996] Holt, C., Hennessy, J. L., and Singh, J. P. Application and Architectural Bottlenecks in Large Scale Distributed Shared Memory Machines. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 134–145, May 1996.
- [Houzet 1999] Houzet, D. A Shared Memory Model on a Cluster of PCs. In *Proc. of the 32st Hawaii Int'l Conf. on System Sciences (HICSS-32) CD-ROM*, January 1999.
- [Hsieh *et al.* 1994] Hsieh, F.-M., Chen, Y.-N., and Tseng, L.-M. A Kernel-Level DSVM Controller for the Diskless Cluster. In *Proc. of the 1994 Int'l Conf. on Parallel and Distributed Systems (ICPADS'94)*, pages 642–628, December 1994.
- [Hsieh *et al.* 1993] Hsieh, W. C., Wang, P., and Weihl, W. E. Computation Migration: Enhancing Locality for Distributed-Memory Parallel Systems. In *Proc. of the Fourth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'93)*, May 1993.
- [Hsieh 1995] Hsieh, W. C. Dynamic Computation Migration in Distributed Shared Memory Systems. PhD thesis, Department of Electrical Engineering and Computer Science, MIT, September 1995.
- [Hsieh *et al.* 1996] Hsieh, W. C., Kaashoek, M. F., and Weihl, W. E. Dynamic Computation Migration in Distributed Shared Memory Systems. In *Proc. of Supercomputing'96*, November 1996.
- [Hu *et al.* 1999] Hu, Y. C., Lu, H., Cox, A., and Zwaenepoel, W. OpenMP for Networks of SMPs. In *Proc. of the Second Merged Symp. IPPS/SPDP 1999*, April 1999.
- [Hu 1994] Hu, W. A Graph Model for Investigating Memory Consistency. In *Proc. of the 1994 Int'l Conf. on Parallel and Distributed Systems (ICPADS'94)*, pages 516–523, December 1994.
- [Hu and Xia 1996] Hu, W. and Xia, P. Hardware Controlled Prefetching in Directory-based Cache Coherent Systems. In *Proc. of the 6th Symp. on the Frontiers of Massively Parallel Computing (Frontiers'96)*, pages 206–213, October 1996.
- [Hu and Xia 1998] Hu, W. and Xia, P. Out-of-Order Execution in Sequentially Consistency Shared Memory Systems: Theory and Practice. *Journal of Computer Science and Technology*, 13(2):125–140, March 1998.
- [Huang 1995a] Huang, F. Restructuring Virtual Memory to Support Distributed Computing Environments. PhD thesis, Computer Laboratory, University of Cambridge, U.K., July 1995.
- [Huang 1995b] Huang, F. Operating System Support for Flexible Coherence in Distributed Object Systems. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (IWOOOS'95)*, pages 171–174, August 1995.
- [Huang *et al.* 1995] Huang, F., Bacon, J., and Mapp, G. Virtual Memory Support for Distributed Computing Environments Using a Shared Data Object Model. *Distributed Systems Engineering*, 2(4):202–211, December 1995.
- [Huang and Bacon 1996] Huang, F. and Bacon, J. Operating System Support for Flexible Coherence in Distributed Shared Memory. In *Proc. of the 29th Hawaii Int'l Conf. on System Sciences (HICSS29)*, volume I, pages 92–101, January 1996.

- [Huang *et al.* 1997a] Huang, Z., Sun, C., Sattar, A., and Lei, W.-J. Parallel Logic Programming on Distributed Shared Memory System. In *Proc. of the IEEE Int'l Conf. on Intelligence Processing Systems*, October 1997.
- [Huang *et al.* 1997b] Huang, Z., Lei, W.-J., Sun, C., and Sattar, A. Heuristic Diff Acquiring in Lazy Release Consistency Model. In *Proc. of the 3rd Asian Computing Science Conference (ASIAN'97)*, pages 98–109, December 1997.
- [Huang *et al.* 1998a] Huang, Z., Sun, C., Sattar, A., and Lei, W.-J. Region-based Updates Propagation in Distributed Shared Memory. In *Proc. of The Second European Parallel and Distributed Systems Conference (Euro-PDS98)*, July 1998.
- [Huang *et al.* 1998b] Huang, Z., Sun, C., and Sattar, A. Exploring Regional Locality in Distributed Shared Memory. In *Proc. of the 4th Asian Computing Science Conference (ASIAN'98)*, December 1998.
- [Hung *et al.* 1996] Hung, K. P., Yung, N. H. C., and Cheung, Y. S. Reduction of False Sharing by Using Process Affinity in Page-based Distributed Shared Memory Multiprocessor Systems. In *Proc. of IEEE 2nd Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'96)*, pages 383–390, June 1996.
- [Hunt and Scott 1994] Hunt, G. C. and Scott, M. L. Using Peer Support to Reduce Fault-Tolerant Overhead in Distributed Shared Memories. Technical Report TR-626, Dept. of Computer Science, The University of Rochester, 1994.
- [Hussak and Keane 1993] Hussak, W. and Keane, J. A. Representation of Coherency Classes for Parallel Systems. In *Proc. of the Fifth IEEE Symp. on Parallel and Distributed Processing*, pages 391–398, December 1993.
- [Hwang and Chung 1994] Hwang, I.-S. and Chung, C.-P. Delayed Precise Invalidation—A Software Cache Coherence Scheme. In *Proc. of the 1994 Int'l Conf. on Parallel and Distributed Systems (ICPADS'94)*, pages 524–529, December 1994.
- [Hyde and Fleisch 1994] Hyde, R. L. and Fleisch, B. D. Degenerate Sharing. In *Proc. of the 1994 Int'l Conf. on Parallel Processing (ICPP'94)*, pages 267–270, August 1994.
- [Hyde and Fleisch 1996] Hyde, R. L. and Fleisch, B. D. An Analysis of Degenerate Sharing and False Coherence. *Journal of Parallel and Distributed Computing*, 34(2):183–195, May 1996.
- [Hyde and Fleisch 1998] Hyde, R. L. and Fleisch, B. D. A Case for Virtual Distributed Objects. *Int'l Journal on Parallel and Distributed Computing*, 1(3), September 1998.
- [Iftode *et al.* 1993] Iftode, L., Li, K., and Petersen, K. Memory Servers for Multicomputers. In *Proc. of the 38th IEEE Int'l Computer Conf. (COMPCON Spring'93)*, pages 534–547, February 1993.
- [Iftode *et al.* 1996a] Iftode, L., Dubnicki, C., Felten, E. W., and Li, K. Improving Release-Consistent Shared Virtual Memory using Automatic Update. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Iftode *et al.* 1996b] Iftode, L., Singh, J. P., and Li, K. Irregular Applications under Shared Virtual Memory. Technical Report TR-96-514, Dept. of Computer Science, Princeton University, February 1996.
- [Iftode *et al.* 1998] Iftode, L., Blumrich, M., Dubnicki, C., Oppenheimer, D., Singh, J. P., and Li, K. Implementation and Performance of Shared Virtual Memory Protocols on SHRIMP. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Iftode and Singh 1999] Iftode, L. and Singh, J. P. Shared Virtual Memory: Progress and Challenges. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):498–507, March 1999.
- [Iftode *et al.* 1999] Iftode, L., Blumrich, M., Dubnicki, C., Oppenheimer, D. L., Singh, J. P., and Li, K. Shared Virtual Memory with Automatic Update Support. In *Proc. of the 13th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'99)*, June 1999.

- [Iliev *et al.* 1991] Iliev, R., Djendov, D., and Lasarov, V. Distributed Shared Memory as a Global Communication Space in a Parallel Architecture. In *Proc. of the Third Workshop on Parallel and Distributed Processing (WP&DP'91)*, pages 17–37, April 1991.
- [Ioannidis and Dwarkadas 1998] Ioannidis, S. and Dwarkadas, S. Compiler and Run-Time Support for Adaptive Load Balancing in Software Distributed Shared Memory Systems. In *Proc. of the Fourth Workshop on Languages, Compilers, and Run-time Systems for Scalable Computers*, May 1998.
- [Istavrinou *et al.* 1994] Istavrinou, P., Hartlage, H., and Baumgarten, H. G. An Evaluation of a Distributed Shared Memory Implementation for a Large-Scale Parallel System. In *Proc. of the Int'l Workshop on Support for Large Scale Shared Memory Architectures*, pages 46–57, April 1994.
- [Itzkovitz *et al.* 1997] Itzkovitz, A., Schuster, A., and Wolfovich, L. Supporting Multiple Parallel Programming Paradigms on top of the Millipede Virtual Parallel Machine. In *Proc. of the 2nd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'97)*, April 1997.
- [Itzkovitz *et al.* 1998] Itzkovitz, A., Schuster, A., and Shalev, L. Thread Migration and its Applications in Distributed Shared Memory Systems. *The Journal of Systems and Software*, 47(1):71–87, July 1998.
- [Itzkovitz 1998] Itzkovitz, A. Distributed Shared Memory: Bridging the Granularity Gap. PhD thesis, Computer Science Department, Technion–Israel Institute of Technology, December 1998.
- [Itzkovitz *et al.* 1999] Itzkovitz, A., Schuster, A., and Talmor, Y. Harnessing the Power of Fast Low-Latency Networks for Software DSMs. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Jalili and Henskens 1993a] Jalili, R. and Henskens, F. A. Distributed Shared Memories. *Gozaresh-E-Computer, Informatic Society of Iran*, 15(119):16–33, 1993.
- [Jalili and Henskens 1993b] Jalili, R. and Henskens, F. A. Management of Persistent Data. *Gozaresh-E-Computer, Informatic Society of Iran*, 15(121):24–32, 1993.
- [Jegou 1995] Jegou, Y. Task Migration, a Technique for Irregular Code Implementation on Distributed Memory Architectures. Technical Report PI-958, IRISA, France, November 1995.
- [Jiang *et al.* 1997] Jiang, D., Shan, H., and Singh, J. P. Application Restructuring and Performance Portability on Shared Virtual Memory and Hardware-Coherent Multiprocessors. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 217–229, June 1997.
- [Jiang and Singh 1997] Jiang, D. and Singh, J. P. Improving Parallel Shear-Warp Volume Rendering on Shared Address Space Multiprocessors. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 252–263, June 1997.
- [Jiang *et al.* 1999] Jiang, D., Cokelley, B., Yu, X., Bilas, A., and Singh, J. P. Scalability of Home-Based Shared Virtual Memory on Clusters of SMPs. In *Proc. of the 13th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'99)*, June 1999.
- [Joe and Hennessy 1994] Joe, T. and Hennessy, J. L. Evaluating the Memory Overhead Required for COMA Architectures. In *Proc. of the 21th Annual Int'l Symp. on Computer Architecture (ISCA'94)*, pages 82–93, April 1994.
- [John *et al.* 1993] John, R., Ahamad, M., Ramachandran, U., Ananthanarayanan, R., and Mohindra, A. An Evaluation of State Sharing Techniques in Distributed Operating Systems. Technical Report GIT-CC-93-73, College of Computing, Georgia Institute of Technology, 1993.
- [John and Ahamad 1994] John, R. and Ahamad, M. Evaluation of Casual Distributed Shared Memory for Data-race-free Programs. Technical Report GIT-CC-94-34, College of Computing, Georgia Institute of Technology, 1994.

- [John 1995] John, R. Implementing and Programming Weakly Consistent Memories. PhD thesis, College of Computing, Georgia Institute of Technology, March 1995.
- [Jones *et al.* 1992] Jones, J. P., Butler, P. L., Johnston, S. E., and Heywood, T. G. Hetero Helix: Synchronous and Asynchronous Control Systems in Heterogeneous Distributed Networks. *Robotic Automation Systems*, 10(2-3):85-99, 1992.
- [Jovanovic *et al.* 1995] Jovanovic, M., Tomasevic, M., and Milutinovic, V. A Simulation-based Comparison of two Reflective Memory Approaches. In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, volume I, pages 140-149, January 1995.
- [Jutla *et al.* 1993] Jutla, D. N., Bodorik, P., and Riordon, J. S. Integrated Concurrency-coherence Control in Distributed Shared Memory. In *Proc. of the 5th Int'l Conf. on Computing and Information (ICCI'93)*, pages 251-255, May 1993.
- [Jutla *et al.* 1996] Jutla, D. N., Bodorik, P., and Olmstead, E. An Application Framework for Modeling Cache Based Virtual Memory Systems. In *Proc. of the 8th Int'l Conf. on Computing and Information (ICCI'96)*, July 1996.
- [Jutla and Bodorik 1998] Jutla, D. N. and Bodorik, P. Integrated Concurrency-coherence Control in Distributed Shared Memory. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, pages 197-206, January 1998.
- [Karamcheti and Chien 1997] Karamcheti, V. and Chien, A. A. View Caching: Efficient Software Shared Memory for Dynamic Computations. In *Proc. of the 11th Int'l Parallel Processing Symp. (IPPS'97)*, pages 483-489, April 1997.
- [Karl 1998] Karl, H. Bridging the Gap between Distributed Shared Memory and Message Passing. In *Proc. of the ACM 1998 Workshop on Java for High-Performance Network Computing*, March 1998.
- [Karlsson and Brorsson 1999] Karlsson, S. and Brorsson, M. An Infrastructure for Portable and Efficient Software DSM. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Kavi and Cohen 1998] Kavi, K. M. and Cohen, W. E. Memory Latency and Thread Migration Challenges for Distributed Shared Memory Systems. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, pages 772-773, January 1998.
- [Keane and Xu 1993] Keane, J. A. and Xu, M. Q. Virtual Shared Memory-based Support for Novel (Parallel) Programming Paradigms. In *Proc. of the 1993 1st Int'l Conf. on Programming Models for Massively Parallel Computers*, pages 83-90, September 1993.
- [Keane *et al.* 1995] Keane, J. A., Grant, A. J., and Xu, M. Q. Comparing Distributed Memory and Virtual Shared Memory Parallel Programming Models. *Future Generation Computer Systems*, 11(2):233-243, March 1995.
- [Keedy 1995] Keedy, J. L. Is Distribution a Genuine Problem for Persistent Systems or is Addressing the Real Problem? In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, pages 695-704, January 1995.
- [Keleher and Tseng 1996] Keleher, P. and Tseng, C.-W. Improving the Compiler/Software DSM Interface: Preliminary Experiences. In *Proc. of the First SUIF Compiler Workshop*, January 1996.
- [Keleher 1996] Keleher, P. The Relative Importance of Concurrent Writers and Weak Consistency Models. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 91-98, May 1996.
- [Keleher and Tseng 1997] Keleher, P. and Tseng, C.-W. Enhancing Software DSMs for Compiler-Parallelized Applications. In *Proc. of the 11th Int'l Parallel Processing Symp. (IPPS'97)*, April 1997.

- [Keleher 1998] Keleher, P. Update Protocols and Iterative Scientific Applications. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 675–681, March 1998.
- [Keleher 1999a] Keleher, P. Tapeworm: High-Level Abstractions of Shared Accesses. In *Proc. of the 3rd Symp. on Operating Systems Design and Implementation (OSDI'99)*, pages 201–214, February 1999.
- [Keleher 1999b] Keleher, P. Decentralized Replicated-Object Protocols. In *Proc. of the 18th Annual ACM Symp. on Principles of Distributed Computing (PODC'99)*, May 1999.
- [Keleher 1999c] Keleher, P. On the Importance of Being Lazy. In *Proc. of the 13th ACM Int'l Conf. on Supercomputing*, June 1999.
- [Kessler 1999] Kessler, C. W. NestStep: Nested Parallelism and Virtual Shared Memory for the BSP Model. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, July 1999.
- [Kim 1995] Kim, E.-G. A Study on Developing a Distributed Problem Solving System. In *Computer Software and Applications Conference (COMPSAC'95)*, pages 122–127, August 1995.
- [Kindler and Walter 1996] Kindler, E. and Walter, R. Arc-Typed Petri Nets. In Billington, J. and Reisig, W., editors, *Application and Theory of Petri Nets 1996*, volume 1091 of *LNCS*, pages 289–306. Springer-Verlag, June 1996.
- [Kindler 1996] Kindler, E. A Specification and Verification Method for Caching Protocols. In Desel, J. and Reichel, H., editors, *Formal Methods for Concurrency, GI-Colloquium*, pages 11–16. Technische Universität Dresden, July 1996.
- [Klepacki 1996] Klepacki, D. J. CFD Using Virtual Shared Memory. In *Proc. of the Parallel CFD'96 Conf.*, pages 400–407, May 1996.
- [Koch and Fowler 1995] Koch, P. T. and Fowler, R. J. Carlsberg: A Distributed Execution Environment Providing Coherent Shared Memory and Integrated Message Passing. In *Proc. of the Nordic Workshop on Programming Environment Research (NWPER'94)*, pages 279–293, June 1995.
- [Koch *et al.* 1998] Koch, P. T., Cecchet, E., and Ronsset de Pina, X. Global Management of Coherent Shared Memory on an SCI Cluster. In *Proc. of the European Multimedia, Multiprocessor Systems and Electronic Commerce Conference (SCI Europe'98)*, September 1998.
- [Kohli *et al.* 1998a] Kohli, P., Ahamad, M., and Schwan, K. Indigo: User-level Support for Building Distributed Shared Abstractions. In *Proc. of the 4th IEEE Symp. on High-Performance Computer Architecture (HPCA-4)*, pages 130–137, August 1998.
- [Kohli *et al.* 1998b] Kohli, P., Ahamad, M., and Schwan, K. Indigo: User-level Support for Building Distributed Shared Abstractions. *Concurrency: Practice and Experience*, 10(1):1–29, 1998.
- [Kolarik 1993] Kolarik, T. Cooperative Computing in Loosely-coupled Distributed Systems. In *Proc. of SHARE Europe Spring Meeting—Distributed Applications*, pages 359–362, April 1993.
- [Kong and Lee 1998] Kong, J. and Lee, G. Binding Time in Distributed Shared Memory Architectures. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 198–206, August 1998.
- [Kongmunvattana and Tzeng 1999] Kongmunvattana, A. and Tzeng, N.-F. Lazy Logging and Prefetch-Based Crash Recovery in Software Distributed Shared Memory Systems. In *Proc. of the Second Merged Symp. IPPS/SPDP 1999*, April 1999.
- [Kontothanassis and Scott 1996] Kontothanassis, L. I. and Scott, M. L. Distributed Shared Memory for New Generation Networks. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, pages 166–177, February 1996.

- [Kostkova and Wilkinson 1998] Kostkova, P. and Wilkinson, T. MAGNET: A Virtual Shared Tuplespace Resource Manager. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, January 1998.
- [Kranz *et al.* 1993] Kranz, D., Johnson, K., Agarwal, A., Kubiatowicz, J., and Lim, B.-H. Integrating Message-Passing and Shared-Memory: Early Experience. In *Proc. of the Fourth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'93)*, pages 54–63, July 1993.
- [Kuhn and Nozicka 1996] Kuhn, E. and Nozicka, G. Post-client/server Coordination Tools. In *Proc. of the Coordination Technology for Collaborative Applications: Organization, Processes, and Agents Conf.*, pages 231–253, December 1996.
- [Kuo *et al.* 1999] Kuo, C.-C., Carter, J., and Kuramkote, R. MP-LOCKS: Replacing Hardware Synchronization Primitives with Message Passing. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Lai and Lei 1996] Lai, A. I.-C. and Lei, C.-L. Data Prefetching for Distributed Shared Memory Systems. In *Proc. of the 29th Hawaii Int'l Conf. on System Sciences (HICSS29)*, volume I, pages 102–110, January 1996.
- [Lai *et al.* 1997] Lai, A. I.-C., Shieh, C.-K., and Kok, Y.-T. Load Balancing in Distributed Shared Memory Systems. In *Proc. of the 1997 IEEE Int'l Performance, Computing, and Communications Conf.*, pages 152–158, February 1997.
- [Lai and Falsafi 1999] Lai, A.-C. and Falsafi, B. Memory Sharing Predictor: The Key to a Speculative Coherent DSM. In *Proc. of the 26th Annual Int'l Symp. on Computer Architecture (ISCA'99)*, May 1999.
- [Lau *et al.* 1995] Lau, A. C. K., Yung, N. H. C., and Cheung, Y. S. On the Doubly-linked List Protocol for Distributed Shared Memory Multiprocessor Systems. In *Proc. of IEEE 1st Int'l Conf. on Algorithms & Architectures for Parallel Processing (ICA3PP'95)*, pages 293–302, April 1995.
- [Le Sergent and Matthews 1994] Le Sergent, T. and Matthews, D. C. J. Adaptive Selection of Protocols for Strict Coherency in Distributed Shared Memory. Technical Report ECS-LFCS-94-306, Dept. of Computer Science, LFCS, University of Edinburgh, 1994.
- [Leff *et al.* 1991] Leff, A., Yu, P. S., and Wolf, J. L. Policies for Efficient Memory Utilization in a Remote Caching Architecture. In *Proc. of the First Int'l Conf. on Parallel and Distributed Information Systems*, pages 198–207, December 1991.
- [Lenoski and Weber 1995] Lenoski, D. E. and Weber, W.-D. Shared Memory Multiprocessing. Morgan Kaufmann Publishers, 1995.
- [Li and Sevcik 1994] Li, H. and Sevcik, K. C. Exploiting Cache Affinity in Software Cache Coherence. In *Proc. of the 8th ACM-SIGARCH Int'l Conf. on Supercomputing*, pages 264–273, July 1994.
- [Li *et al.* 1997] Li, Q., Jing, H., and Xie, L. BFXM: A Parallel File System Model Based on the Mechanism of Distributed Shared Memory. *ACM Operating Systems Review*, 31(4):30–40, October 1997.
- [Liang *et al.* 1997] Liang, T.-Y., Shieh, C.-K., and Zhu, W. Task Mapping on Distributed Shared Memory Systems Using Hopfield Neural Network. In *Proc. of the Communication Networks and Distributed Systems Modeling and Simulation Conf.*, pages 37–43, January 1997.
- [Liang *et al.* 1998] Liang, T.-Y., Shieh, C.-K., Liu, D.-C., and Zhu, W. Dynamic Task Scheduling on Multithreaded Distributed Shared Memory Systems. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 1058–1065, July 1998.
- [Liang *et al.* 1999] Liang, T.-Y., Chuang, D.-Y., and Shieh, C.-K. Thread Selection in Software DSM Systems. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.

- [Lilja 1993] Lilja, D. J. Cache Coherence in Large-Scale Shared-Memory Multiprocessors: Issues and Comparisons. *ACM Computing Surveys*, 3(25):303–338, September 1993.
- [Lilja and Yew 1993] Lilja, D. J. and Yew, P.-C. Improving Memory Utilization in Cache Coherence Directories. *IEEE Trans. on Parallel and Distributed Systems*, 4(10):1130–1146, October 1993.
- [Lin and Kuo 1996] Lin, J.-W. and Kuo, S.-Y. A Highly Available Partition-Processing Protocol for Distributed Shared Memory Systems. In *Proc. of the Second Int'l Euro-Par Conf.*, volume I, pages 514–521, August 1996.
- [Lindstrom 1995] Lindstrom, A. Multiversioning and Logging in the Grasshopper Kernel Persistent Store. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (IWOOS'95)*, pages 14–23, August 1995.
- [Lipkind and Karamcheti 1999] Lipkind, I. and Karamcheti, V. Object Views: Bridging the Performance Gap Between Shared Memory and Message Passing. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'99)*, July 1999.
- [Listl 1994] Listl, A. Using Subpages for Coherency Control in Parallel Database Systems. In *Proc. of Parallel Architectures and Languages Europe (PARLE'94)*, pages 765–768, July 1994.
- [Liu *et al.* 1998] Liu, Y.-Z., Mac, S.-C., and Shieh, C.-K. Supporting Small Accesses for the Parallel File Subsystem on Distributed Shared Memory Systems. In *Proc. of the 1998 Int'l Conf. on Parallel and Distributed Systems (ICPADS'98)*, pages 808–815, December 1998.
- [Lo 1994] Lo, V. Operating Systems Enhancements for Distributed Shared Memory, volume 39 of *Advances in Computers*, pages 191–237. Academic Press, Inc., 1994.
- [Lowenthal and Andrews 1996] Lowenthal, D. K. and Andrews, G. R. Adaptive Data Placement for Distributed-Memory Machines. In *Proc. of the 10th Int'l Parallel Processing Symp. (IPPS'96)*, April 1996.
- [Lowenthal *et al.* 1996] Lowenthal, D. K., Freeh, V. W., and Andrews, G. R. Using Fine-Grain Threads and Run-Time Decision Making in Parallel Computing. *Journal of Parallel and Distributed Computing*, 37(1):41–54, August 1996.
- [Lowenthal *et al.* 1998] Lowenthal, D. K., Freeh, V. W., and Andrews, G. R. Efficient Fine-Grain Parallelism on Shared-Memory Machines. *Concurrency: Practice and Experience*, 10(3):157–173, 1998.
- [Lowenthal 1998] Lowenthal, D. K. Local and Global Data Distribution in the Filaments Package. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 33–41, July 1998.
- [Lu 1995] Lu, H. Message-Passing vs. Distributed Shared Memory on Networks of Workstations. Master's thesis, Department of Computer Science, Rice University, May 1995.
- [Lu *et al.* 1995] Lu, H., Dwarkadas, S., Cox, A. L., and Zwaenepoel, W. Message-Passing vs. Distributed Shared Memory on Networks of Workstations. In *Proc. of Supercomputing'95*, December 1995.
- [Lu *et al.* 1997] Lu, H., Dwarkadas, S., Cox, A. L., and Zwaenepoel, W. Quantifying the Performance Differences between PVM and TreadMarks. *Journal of Parallel and Distributed Computing*, 43(2):65–78, June 1997.
- [Lu *et al.* 1998] Lu, H., Hu, Y. C., and Zwaenepoel, W. OpenMP on Network of Workstations. In *Proc. of Supercomputing'98*, October 1998.
- [Lumpp Jr. *et al.* 1998a] Lumpp Jr., J. E., Sivakumar, K., Diaz, C., and Griffioen, N. J. Xunify—A Performance Debugger for a Distributed Shared Memory System. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, pages 587–596, January 1998.

- [Lumpp Jr. *et al.* 1998b] Lumpp Jr., J. E., Sivakumar, K., Diaz, C., and Griffioen, N. J. Xunify—A Performance Debugger for a Distributed Shared Memory System. *Int'l Journal of Parallel and Distributed Systems and Networks*, June 1998.
- [Mac *et al.* 1996] Mac, S.-C., Shieh, C.-K., Ueng, J.-C., and Tseng, L.-M. Design and Implementation of a Parallel File Subsystem on TreadMarks. In *Proc. of the Int'l Computer Symp.*, pages 256–263, December 1996.
- [Mac *et al.* 1999] Mac, S.-C., Shieh, C.-K., and Chang, J.-B. Design and Analysis of A Parallel File System for Distributed Shared Memory Systems. *Journal of Systems Architecture*, 45(8):603–617, 1999.
- [Maddi and Raynal 1991] Maddi, A. and Raynal, M. Implementing Semaphores on a Distributed Memory Parallel Machine. In *Proc. of the Int'l Conf. on Parallel Computing (ParCo91)*, pages 407–412, September 1991.
- [Maggs *et al.* 1997] Maggs, B. M., Meyer auf der Heide, F., Vocking, B., and Westermann, M. Exploiting Locality for Data Management in Systems of Limited Bandwidth. In *Proc. of the 38th Annual Symp. on Foundations of Computer Science*, pages 284–293, October 1997.
- [Maier 1995] Maier, J. Fault-Tolerant Parallel Programming with Atomic Actions. In *Proc. of the 1995 Fault-Tolerant Parallel and Distributed Systems*, pages 210–219, 1995.
- [Malkawi 1998] Malkawi, M. Distributed Virtual Memory Multicomputer Systems. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, July 1998.
- [Manis *et al.* 1996] Manis, G., Voliotis, K., Tsanakas, P., and Papakonstantinou, G. Enhancing PVM with Threads in Distributed Programming. In *Proc. of the High-Performance Computing and Networking Europe 1996 (HPCN'96)*, pages 1013–1014, April 1996.
- [Maples 1990] Maples, C. A High-performance, Memory-based Interconnection System for Multicomputer Environments. In *Proc. of Supercomputing'90*, pages 295–304, November 1990.
- [Marinov *et al.* 1998] Marinov, D., Magdic, D., Milenkovic, A., Protic, J., Tartalja, I., and Milutinovic, V. An Approach to Characterization of Parallel Applications for DSM Systems. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, pages 782–783, January 1998.
- [Markatos and Dramitinos 1996] Markatos, E. P. and Dramitinos, G. Implementation of a Reliable Remote Memory Pager. In *Proc. of the USENIX 1996 Annual Technical Conference*, pages 177–189, January 1996.
- [Mehl 1992] Mehl, H. Distributed shared Memory: A Survey. Technical Report SFB124-33/92, Computer Science Department, University of Kaiserslautern, Germany, December 1992.
- [Melo and Muntean 1994] Melo, A. and Muntean, T. Programming with Shared Data in Parallel Loosely Coupled Machines: The Shared Virtual Memory Approach. In *Proc. of the IEEE/USP Int'l Workshop on High Performance Computing*, pages 129–142, March 1994.
- [Michael *et al.* 1997] Michael, M. M., Nanda, A. K., Lim, B.-H., and Scott, M. L. Coherence Controller Architectures for SMP-Based CC-NUMA Multiprocessors. In *Proc. of the 24th Annual Int'l Symp. on Computer Architecture (ISCA'97)*, pages 219–228, June 1997.
- [Milenkovic and Milutinovic 1998] Milenkovic, A. and Milutinovic, V. Lazy Prefetching. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, January 1998.
- [Milutinovic *et al.* 1997] Milutinovic, V., Milenkovic, A., and Sheaffer, G. The Cache Injection/Cofetch Architecture: Initial Performance Evaluation. In *Proc. of the Fifth Int'l Symp. on Modeling, Analysis, and Simulation of Computer and Telecommunication Systems*, pages 63–64, January 1997.
- [Minnich and Pryor 1992] Minnich, R. G. and Pryor, D. V. A Radiative Heat Transfer Simulation on a SPARCStation Farm. In *Proc. of the First IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-1)*, pages 124–132, September 1992.

- [Minnich and Pryor 1993] Minnich, R. G. and Pryor, D. V. Radiative Heat Transfer Simulation on a SPARCStation Farm. *Concurrency: Practice and Experience*, 5(4):345–357, June 1993.
- [Mizrahi *et al.* 1989] Mizrahi, H. E., Baer, J.-L., Lazowska, E. D., and Zahorjan, J. Introducing Memory into the Switch Elements of Multiprocessor Interconnection Networks. In *Proc. of the 16th Annual Int'l Symp. on Computer Architecture (ISCA'89)*, pages 158–166, May 1989.
- [Mohindra and Ramachandran 1991] Mohindra, A. and Ramachandran, U. A Survey of Distributed Shared Memory in Loosely-coupled Systems. Technical Report GIT-CC-91/01, College of Computing, Georgia Institute of Technology, January 1991.
- [Mohindra and Ramachandran 1994] Mohindra, A. and Ramachandran, U. A Comparative Study of Distributed Shared Memory System Design Issues. Technical Report GIT-CC-94/35, College of Computing, Georgia Institute of Technology, August 1994.
- [Moore *et al.* 1998] Moore, R., Klauer, B., and Waldschmidt, K. Automatic Scheduling for Cache Only Memory Architectures: Extended Abstract. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, January 1998.
- [Morris *et al.* 1997] Morris, J., Gregg, R. R., Herbert, D., and McCoull, J. Reducing Overheads in Distributed Shared Memory Systems. In *Proc. of the 30th Hawaii Int'l Conf. on System Sciences (HICSS-30)*, January 1997.
- [Mueller 1997a] Mueller, F. Distributed Shared Memory Threads: DSM-Threads. In *Proc. of the Workshop on Run-Time Systems for Parallel Programming*, pages 31–40, April 1997.
- [Mueller 1997b] Mueller, F. On the Design and Implementation of DSM-Threads. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'97)*, pages 315–324, June 1997.
- [Mueller 1997c] Mueller, F. Supporting Ada 95 Passive Partitions in a Distributed Environment. In *Proc. of the Ada-Europe Int'l Conf. on Reliable Software Technologies*, pages 218–229, June 1997.
- [Mueller 1999] Mueller, F. Adaptive DSM—Runtime Behavior via Speculative Data Distribution. In *Proc. of the 3rd Workshop on Runtime Systems for Parallel Programming (RTSPP)*, April 1999.
- [Mukherjee *et al.* 1995] Mukherjee, S. S., Sharma, S. D., Hill, M. D., Larus, J. R., Rogers, A., and Saltz, J. Efficient Support for Irregular Applications on Distributed-Memory Machines. In *Proc. of the Fifth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'95)*, pages 68–79, July 1995.
- [Mukherjee *et al.* 1996] Mukherjee, S. S., Falsafi, B., Hill, M. D., and Wood, D. A. Coherent Network Interfaces for fine-Grain Communication. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 247–258, May 1996.
- [Mukherjee and Hill 1998] Mukherjee, S. S. and Hill, M. D. Using Prediction to Accelerate Coherence Protocols. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Muller *et al.* 1995a] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. The Application of Skewed-Associative Memories to Cache Only Memory Architectures. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 150–154, August 1995.
- [Muller *et al.* 1995b] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. Hiding Miss Latencies with Multithreading on the Data Diffusion Machine. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 178–185, August 1995.
- [Muller *et al.* 1996] Muller, H. L., Stallard, P. W. A., and Warren, D. H. D. Multitasking and Multithreading on a Multiprocessor with Virtual Shared Memory. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, pages 212–221, February 1996.

- [Muller *et al.* 1998] Muller, H. L., Raina, S., Stallard, P. W. A., and Warren, D. H. D. Parallel Calibrated Emulation as a Technique for Evaluating Parallel Architectures. *Int'l Journal of Computer Systems Science and Engineering*, 13(1):17–25, January 1998.
- [Muntean and Melo 1993] Muntean, T. and Melo, A. PAROS: A Generic Multi Virtual Machines Parallel Operating System. In *Proc. of the Int'l Conf. on Parallel Computing (ParCo93)*, pages 319–328, September 1993.
- [Nakajo *et al.* 1997] Nakajo, H., Ichikawa, A., and Kaneda, Y. An Implementation and Evaluation of a Distributed Shared-Memory System on Workstation Clusters Using Fast Serial Links. In *Proc. of the Int'l Symp. on High Performance Computing (ISHPC'97)*, pages 143–158, November 1997.
- [Nakajo *et al.* 1998] Nakajo, H., Tanaka, H., Nakanishi, Y., Kohata, M., and Kaneda, Y. Distributed Shared-Memory System for a Workstation Cluster with a High-speed Serial Interface. In *Proc. of the High-Performance Computing and Networking Europe 1998 (HPCN'98)*, pages 588–597, April 1998.
- [Nalumasu and Gopalakrishnan 1998] Nalumasu, R. and Gopalakrishnan, G. Deriving Efficient Cache Coherency Protocols through Refinement. In *Proc. of the IPPS/SPDP'98 Workshops*, pages 857–870, March 1998.
- [Nanri *et al.* 1995] Nanri, T., Sato, H., and Shimasaki, M. Implementing a Portable SPMD Shared Memory Model Parallel Language in a Distributed Computing Environment. In *Proc. of the Int'l Symp. on Parallel and Distributed Supercomputing*, pages 243–242, 1995.
- [Nanri *et al.* 1997] Nanri, T., Sato, H., and Shimasaki, M. Using Cache Optimizing Compiler for Managing Software Cache on Distributed Shared Memory System. In *High Performance Computing on the Information Superhighway (HPC Asia'97)*, pages 312–318, April 1997.
- [Narazaki *et al.* 1998] Narazaki, S., Hamachi, H., Shimokawa, T., Yoshida, N., and Ushijima, K. Dynamic Copy Allocation Scheme for Distributed Resource Sharing Based on Meta-level Computation. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 829–834, July 1998.
- [Nash and Dew 1994] Nash, J. M. and Dew, P. M. Scalable Data Sharing on a Message Passing Machine. In *Proc. of the 1994 World Transputer Congress*, pages 718–738, September 1994.
- [Navronicolos and Roth 1992] Navronicolos, M. and Roth, D. Efficient, Strongly Consistent Implementations of Shared Memory. In *Proc. of the 6th Int'l Workshop on Distributed Algorithms (WDAG'92)*, November 1992.
- [Nayfeh *et al.* 1996] Nayfeh, B. A., Olukotun, O. A., and Singh, J. P. The Impact of Shared-Cache Clustering in Small-Scale Shared-Memory Multiprocessors. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Nebro *et al.* 1997] Nebro, A. J., Pimentel, E., and Troya, J. M. Applying Distributed Shared Memory Techniques for Implementing Distributed Objects. In *Proc. of the 11th European Conf. on Object Oriented Programming (ECOOP'97)*, pages 499–506, July 1997.
- [Nguyen and Srinani 1992] Nguyen, T. M. and Srinani, V. P. A Dynamic Memory Management Scheme for Shared Memory Multiprocessors. In Dubois, M. and Thakkar, S. S., editors, *Scalable Shared Memory Multiprocessors*. Kluwer Academic Publishers, 1992.
- [Nieplocha and Harrison 1996] Nieplocha, J. and Harrison, R. J. Shared Memory NUMA Programming on I-WAY. In *Proc. of the Fifth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-5)*, August 1996.
- [Nieplocha and Harrison 1997] Nieplocha, J. and Harrison, R. J. Shared Memory Programming in Meta-computing Environments: The Global Array Approach. *The Journal of Supercomputing*, 11(2):119–136, October 1997.

- [Nisbet and Ford 1996] Nisbet, A. P. and Ford, R. W. Spinning on Coherency: A New SVM Optimization for Write-Invalidate. In *Proc. of the High-Performance Computing and Networking Europe 1996 (HPCN'96)*, April 1996.
- [Nitzberg and Lo 1991] Nitzberg, B. and Lo, V. Distributed Shared Memory: A Survey of Issues and Algorithms. *IEEE Computer*, 24(8):52–60, August 1991.
- [Oguchi *et al.* 1996] Oguchi, M., Aida, H., and Saito, T. A Proposition and Evaluation of DSM Models Suitable for a Wide Area Distributed Environment Realized on High Performance Networks. *IEICE Trans. on Communications*, E75-B(2):153–162, February 1996.
- [Oi and Ranganathan 1998] Oi, H. and Ranganathan, N. A Comparative Study of Bidirectional Ring and Crossbar Interconnection Networks. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 883–890, July 1998.
- [O'Reilly *et al.* 1995] O'Reilly, P., Leiden, S., and Antell, T. Operating System Support for High Speed Network Management and Control. In *IEEE MILCOM'95*, volume 2, pages 678–681, November 1995.
- [Osawa and Yuba 1998] Osawa, N. and Yuba, T. Lazy and Differential Relication in a Recoverable Distributed Shared Memory System. In *Proc. of the High-Performance Computing and Networking Europe 1998 (HPCN'98)*, pages 698–707, April 1998.
- [Oskin *et al.* 1998] Oskin, M., Sherwood, T., Hensley, J., Yeh, S., and Chong, F. T. Sharing Data in Page-Based Intelligent Memory. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Paas and Scholtyssik 1997] Paas, S. M. and Scholtyssik, K. Efficient Distributed Synchronization within an all-software DSM System for Clustered PCs. In *Proc. of the 1st Workshop on Cluster-Computing, TU Chemnitz*, November 1997.
- [Paas *et al.* 1998] Paas, S. M., Bommel, T., and Scholtyssik, K. Win32 API Emulation on UNIX for Software DSM. In *Proc. of the 2nd USENIX Windows NT Symposium*, August 1998.
- [Pagnia and Theel 1998] Pagnia, H. and Theel, O. Sacrificing True Distribution for Gaining Access Efficiency of Replicated Shared Objects. In *Proc. of the 31st Hawaii Int'l Conf. on System Sciences (HICSS-31)*, volume VII, January 1998.
- [Pai *et al.* 1996] Pai, V. S., Ranganathan, P., Adve, S. V., and Harton, T. An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVII)*, pages 12–23, October 1996.
- [Parashar 1999] Parashar, M. An Integrated Framework for Interactive Simulation. In *Proc. of the 1998 IEEE Information Technology Conf.: Information Environment for the Future*, September 1999.
- [Parasuram *et al.* 1994] Parasuram, Y., Stabler, E., and Chin, S.-K. Parallel Implementation of BDD Algorithms Using a Distributed Shared Memory. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume I, pages 16–25, January 1994.
- [Pears and Francis 1990] Pears, A. N. and Francis, R. S. Execution Implications of Distributed Shared Memory. In *Proc. of the Australian UNIX User Group (AUUG-90)*, pages 183–194, 1990.
- [Pears and Francis 1991] Pears, A. N. and Francis, R. S. On the Plausibility of Distributed Virtual Memory. In *Proc. of the 14th Australian Computer Science Conf. (ACSC-14)*, pages 28.1–28.11, February 1991.
- [Pears and Francis 1993a] Pears, A. N. and Francis, R. S. How Much Consistency is a Good Thing? (Distributed Shared Memory). In *Proc. of the 16th Australian Computer Science Conf. (ACSC-16)*, pages 301–309, February 1993.
- [Pears and Francis 1993b] Pears, A. N. and Francis, R. S. Barrier Semantics in Very Weak Memory. In *Proc. of Parallel Architectures and Languages Europe (PARLE'93)*, pages 728–731, June 1993.

- [Pears 1993] Pears, A. N. Odin: A Study in Single Address Space Multi-Computing. PhD thesis, La Trobe University, Australia, December 1993.
- [Pears and Francis 1994a] Pears, A. N. and Francis, R. S. Preserving Barrier Semantics in Loosely Coherent Memory Systems. Technical Report CS-TR-14/94, Department of Computer Science and Computer Engineering, La Trobe University, 1994.
- [Pears and Francis 1994b] Pears, A. N. and Francis, R. S. Odin: Performance of a DSM Design. Technical Report CS-TR-17/94, Department of Computer Science and Computer Engineering, La Trobe University, 1994.
- [Pears *et al.* 1996] Pears, A. N., de Rijk, M. J., and Wagon, N. Improving the Performance of Weak Consistency Protocols. In *Proc. of the 29th Hawaii Int'l Conf. on System Sciences (HICSS29)*, January 1996.
- [Pears 1996a] Pears, A. N. Odin: Implications and Performance of a Novel DSM Design. In *11th Int'l Conf. on Systems Engineering (ICSE'96)*, January 1996.
- [Pears 1996b] Pears, A. N. Odin: Design and Evaluation of a Single Address Space Multiprocessor. In *Proc. of the IASTED Int'l Conf. on Modelling and Simulation*, pages 57–62, August 1996.
- [Perez-Cortes *et al.* 1995] Perez-Cortes, E., Dechamboux, P., and Han, J. Generic Support for Synchronization and Consistency in Arias. In *Fifth Workshop on Hot Topics in Operating Systems (HotOS-V)*, pages 113–118, May 1995.
- [Perkovic and Keleher 1996] Perkovic, D. and Keleher, P. Online Data-Race Detection via Coherency Guarantees. In *Proc. of the 2nd Symp. on Operating Systems Design and Implementation (OSDI'96)*, pages 47–57, October 1996.
- [Perkovic and Keleher 1998] Perkovic, D. and Keleher, P. A Protocol-Centric Approach to On-The-Fly Race Detection. *IEEE Trans. on Parallel and Distributed Systems*, 1998.
- [Perkovic and Keleher 1999] Perkovic, D. and Keleher, P. Responsiveness without Interrupts. In *Proc. of the 13th ACM-SIGARCH Int'l Conf. on Supercomputing (ICS'99)*, June 1999.
- [Petersen 1993] Petersen, K. Operating System Support for Modern Memory Hierarchies. PhD thesis, Dept. of Computer Science, Princeton University, October 1993.
- [Petersen and Li 1995] Petersen, K. and Li, K. Multiprocessor Cache Coherence Based on Virtual Memory Support. *Journal of Parallel and Distributed Computing*, 29(2):158–178, September 1995.
- [Plakal *et al.* 1998a] Plakal, M., Sorin, D. J., Condon, A. E., and Hill, M. D. Lamport Clocks: Verifying A Directory Cache-Coherency Protocol. In *Proc. of the Seventh Workshop on Scalable Shared Memory Multiprocessors*, June 1998.
- [Plakal *et al.* 1998b] Plakal, M., Sorin, D. J., Condon, A. E., and Hill, M. D. Lamport Clocks: Verifying A Directory Cache-Coherency Protocol. In *Proc. of the 10th ACM Annual Symp. on Parallel Algorithms and Architectures (SPAA'98)*, pages 67–76, June 1998.
- [Pong *et al.* 1998] Pong, F., Browne, M., Aybay, G., Nowatzky, A., and Dubois, M. Design Verification of the S3.mp Cache-Coherent Shared-Memory System. *IEEE Transactions on Computers*, 47(1):135–140, 1998.
- [Popp *et al.* 1996] Popp, R. L., Pattipati, K. R., Bar-Shalom, Y., and Gassner, R. R. High-Performance Distributed-Memory Multitarget Tracking. In *Proc. of the Fifth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-5)*, August 1996.
- [Protic *et al.* 1995] Protic, J., Tomasevic, M., and Milutinovic, V. A Survey of Distributed Shared Memory Systems. In *Proc. of the 28th Hawaii Int'l Conf. on System Sciences (HICSS-28)*, volume I, pages 74–84, January 1995.

- [Protic *et al.* 1996] Protic, J., Tomasevic, M., and Milutinovic, V. Distributed Shared Memory: Concepts and Systems. *IEEE Parallel and Distributed Technology*, 4(2):63–71, summer 1996.
- [Protic *et al.* 1997] Protic, J., Tomasevic, M., and Milutinovic, V. Tutorial on Distributed Shared Memory Concepts and Systems. IEEE-CS Press, 1997.
- [Qiu and Dubois 1998] Qiu, X. and Dubois, M. Options for Dynamic Address Translation in COMAs. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Raina 1992] Raina, S. Virtual Shared Memory: A Survey of Techniques and Systems. Technical Report CSTR-92-36, Dept. of Computer Science, University of Bristol, 1992.
- [Raina 1993] Raina, S. Emulation of a Virtual Shared Memory Architecture. PhD thesis, Department of Computer Science, University of Bristol, September 1993.
- [Rajamani 1997] Rajamani, K. Automatic Data Aggregation for Software Distributed Shared Memory Systems. Master's thesis, Department of Computer Science, Rice University, February 1997.
- [Rajamony and Cox 1997] Rajamony, R. and Cox, A. L. Optimally Synchronizing DOACROSS Loops on Shared Memory Multiprocessors. In *Proc. of the 1997 Int'l Conf. on Parallel Architectures and Compilation Techniques (PACT'97)*, November 1997.
- [Rawdon *et al.* 1991] Rawdon, M., Delery, H., Driskill, R., Blakes, E., and Fleisch, B. D. Evaluation Tools for Distributed Shared Memory. In *Proc. of the IEEE SOUTEASTCON'91*, pages 198–200, April 1991.
- [Rehn and Pizka 1999] Rehn, C. and Pizka, M. BOPS: Balancing Objects and Pages in a Shared Space. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Risau *et al.* 1996] Risau, J., Mkischl, A., and Damm, W. A RISC Approach to Weak Cache Coherence. In *Proc. of the Second Int'l Euro-Par Conf.*, volume II, pages 453–456, August 1996.
- [Rockhold and Peterson 1994] Rockhold, R. L. and Peterson, J. L. Operating System Support for Shared Memory Clusters. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume II, pages 86–95, January 1994.
- [Ronnse and Zwaenepoel 1997] Ronnse, M. and Zwaenepoel, W. Execution Replay for TreadMarks. In *Proc. of the 5th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'97)*, pages 343–350, January 1997.
- [Sandhu and Sevcik 1995] Sandhu, H. S. and Sevcik, K. An Analytic Study of Dynamic Hardware and Software Cache Coherence Strategies. In *Proc. of the Joint Int'l Conf. on Measurement and Modeling of Computer Systems*, May 1995.
- [Sardesai *et al.* 1998] Sardesai, S., McLaughlin, D., and Dasgupta, P. Distributed Cactus Stacks: Runtime Stack-Sharing Support for Distributed Parallel Programs. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 57–65, July 1998.
- [Sarkar 1995] Sarkar, V. Data Consistency in Distributed Memory Systems. In *Proc. of the 1995 ICPP Workshop on Challenges for Parallel Processing*, pages 124–132, August 1995.
- [Savic *et al.* 1995] Savic, S., Tomasevic, M., Milutinovic, V., Gupta, A., Natale, M., and Gertner, I. Improved RMS for the PC Environment. *Journal of Microprocessors and Microsystems*, 19(10):609–619, December 1995.
- [Savva and Nanya 1995] Savva, A. and Nanya, T. Gracefully Degrading Systems Using Bulk-Synchronous Parallel Model with Randomized Shared Memory. In *Proc. of the 25th Annual Int'l Symp. on Fault-Tolerant Computing (FTCS-25)*, June 1995.
- [Scales and Gharachorloo 1997] Scales, D. J. and Gharachorloo, K. Towards Transparent and Efficient Software Distributed Shared Memory. In *Proc. of the 16th ACM Symp. on Operating Systems Principles (SOSP-16)*, October 1997.

- [Scheurich and Dubois 1988] Scheurich, C. and Dubois, M. Dynamic Page Migration in Multiprocessors with Distributed Global Memory. In *Proc. of the 8th Int'l Conf. on Distributed Computing Systems (ICDCS-8)*, pages 162–169, June 1988.
- [Scheurich and Dubois 1989] Scheurich, C. and Dubois, M. Dynamic Page Migration in Multiprocessors with Distributed Global Memory. *IEEE Computer*, 38(8):1154–1163, August 1989.
- [Schmidt *et al.* 1999] Schmidt, E. R., Tapus, C., Kiniry, J., and Zimmerman, D. M. Distributed Scalable Server Architecture for Multi-User Environments. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Schoen 1994] Schoen, F. The LoGo Structuring Scheme for Distributed Shared Memory Systems. In *Proc. of the 1st Int'l Conf. on Massively Parallel Computing Systems (MPCS'94)*, pages 408–412, May 1994.
- [Schoettner *et al.* 1998] Schoettner, M., Traub, S., and Schulthess, P. A Transactional DSM Operating System in Java. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume I, pages 99–106, July 1998.
- [Schoinas 1997] Schoinas, I. Fine-Grain Distributed Shared Memory on Clusters of Workstations. PhD thesis, Computer Sciences Department, University of Wisconsin-Madison, December 1997.
- [Schoinas *et al.* 1998] Schoinas, I., Falsafi, B., Hill, M. D., Larus, J. R., and Wood, D. A. Sirocco: Cost-Effective Fine-Grain Distributed Shared Memory. In *Proc. of the Int'l Conf. on Parallel Architectures and Compilation Techniques (PACT'98)*, October 1998.
- [Schroeder and Fleisch 1996] Schroeder, W. H. and Fleisch, B. D. Architecture of the Oasis Shared Virtual Memory System. Technical Report UCR-CS-96-4, Dept. of Computer Science, University of California at Riverside, April 1996.
- [Schroeder 1996] Schroeder, W. H. Architecture of the Oasis Shared Virtual Memory System. Master's thesis, Dept. of Computer Science, University of California at Riverside, June 1996.
- [Schulz and Hellwagner 1998] Schulz, M. and Hellwagner, H. Global Virtual Memory based on SCI-DSM. In *Proc. of the European Multimedia, Multiprocessor Systems and Electronic Commerce Conference (SCI Europe '98)*, September 1998.
- [Schuster and Shalev 1998] Schuster, A. and Shalev, L. Using Remote Access Histories for Thread Scheduling in Distributed Shared Memory Systems. In *Proc. of the 12th Int'l. Symp. on Distributed Computing (DISC98)*, September 1998.
- [Seidel and Gardiner 1996] Seidel, K. and Gardiner, P. Structured Development of a Virtual Shared Memory System. *Formal Aspects of Computing*, 8(1):67–85, 1996.
- [Seidmann 1999] Seidmann, T. Multicast-based Runtime Systems for Highly Efficient Causally Consistent Software-only DSM. In *Proc. of the 3rd Workshop on Runtime Systems for Parallel Programming (RT-SPP)*, April 1999.
- [Shah and Fleisch 1998] Shah, S. and Fleisch, B. D. A Comparison of DSM Coherence Protocols using Program Driven Simulation. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, July 1998.
- [Shalev 1997] Shalev, L. Load Sharing in Distributed Shared Memory Systems. Master's thesis, Computer Science Department, Technion-Israel Institute of Technology, November 1997.
- [Shan and Singh 1998] Shan, H. and Singh, J. P. Parallel Tree Building on a Range of Shared Address Space Multiprocessors: Algorithms and Application Performance. In *Proc. of the First Merged Symp. IPPS/SPDP 1998*, pages 475–484, March 1998.
- [Shapiro 1994] Shapiro, M. A Binding Protocol for Distributed Shared Objects. In *Proc. of the 14th Int'l Conf. on Distributed Computing Systems (ICDCS-14)*, pages 134–141, June 1994.

- [Shapiro and Ferriera 1995] Shapiro, M. and Ferriera, P. Larchant-RDOSS: A Distributed Shared Persistent Memory and Its Garbage Collector. In *Proc. of the 9th Int'l Workshop on Distributed Algorithms (WDAG'95)*, pages 198–214, September 1995.
- [Shatdal and Naughton 1993] Shatdal, A. and Naughton, J. F. Using Shared Virtual Memory for Parallel Join Processing. In *Proc. of the Int'l Conf. on Management of Data (SIGMOD'93)*, pages 119–128, May 1993.
- [Shi *et al.* 1997] Shi, W., Hu, W., and Tang, Z. An Innovative Implementation for Directory-based Cache Coherence in Shared Memory Multiprocessors. *ACM Computer Architecture News*, 25(5):2–9, December 1997.
- [Shi and Tang 1998] Shi, W. and Tang, Z. Using Confidence Intervals to Evaluating Distributed Shared Memory Systems. *IEEE TCCA Newsletter*, pages 3–10, June 1998.
- [Shi and Ma 1999] Shi, W. and Ma, J. High Efficient Parallel Computation of Resonant Frequencies of Waveguided Loaded Cavities on JIAJIA Software DSM System (Poster). In *Proc. of the High-Performance Computing and Networking Europe 1999 (HPCN'99)*, April 1999.
- [Shieh *et al.* 1996a] Shieh, C.-K., Mac, S.-C., Ueng, J.-C., and Lai, A.-C. Providing a Parallel File System on Cohesion. In *Proc. of the 1996 Int'l Conf. on Computer Systems Technology for Industrial Applications*, pages 96–103, December 1996.
- [Shieh *et al.* 1996b] Shieh, C.-K., Ueng, J.-C., Mac, S.-C., and Liang, T.-Y. Multi-threaded Design for a Distributed Shared Memory System. In *Proc. of the Int'l Conf. on Distributed Systems, Software Engineering and Database Systems*, pages 248–255, December 1996.
- [Shieh *et al.* 1995] Shieh, C.-K., Mac, S.-C., and Ueng, J.-C. Improving the Performance of Distributed Shared Memory Systems via Parallel File Input/Output. *The Journal of Systems and Software*, 44(1):3–15, May 1995.
- [Silla *et al.* 1998] Silla, F., Malumbres, M. P., Duato, J., Dai, D., and Panda, D. K. Impact of Adaptivity on the Behavior of Networks of Workstations under Bursty Traffic. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 88–95, August 1998.
- [Silva *et al.* 1996] Silva, L. M., Silva, J. G., and Chapple, S. Implementing Distributed Shared Memory on top of MPI: the DSMPI Library. In *Proc. of the 4th EUROMICRO Workshop on Parallel and Distributed Processing (PDP'96)*, pages 50–57, January 1996.
- [Simons *et al.* 1994] Simons, B., Sarkar, V., Breternitz, M., and Lai, M. An Optimal Asynchronous Scheduling Algorithm for Software Cache Consistency. In *Proc. of the 27th Hawaii Int'l Conf. on System Sciences (HICSS-27)*, volume II, pages 502–511, January 1994.
- [Singh *et al.* 1994] Singh, J. P., Joe, T., Gupta, A., and Hennessy, J. L. An Empirical Comparison of the Kendall Square Research KSR-1 and Stanford DASH Multiprocessors. In *Proc. of Supercomputing'93*, pages 214–225, November 1994.
- [Singh *et al.* 1999] Singh, J. P., Bilas, A., Jiang, D., and Zhou, Y. Limits to the Performance of Software Shared Memory: A Layered Approach. In *Proc. of the 5th IEEE Symp. on High-Performance Computer Architecture (HPCA-5)*, January 1999.
- [Sivalingam and Dowd 1995] Sivalingam, K. M. and Dowd, P. W. A Multilevel WDM Access Protocol for an Optically Interconnected Multiprocessor System. *Journal of Lightwave Technology*, 13(11):2152–2167, November 1995.
- [Smith and Farber 1991] Smith, J. M. and Farber, D. J. Traffic Characteristics of a Distributed Memory System. *Computer Networks and ISDN Systems*, 22(2):143–154, September 1991.

- [Sorin *et al.* 1998] Sorin, D. J., Pai, V. S., Adve, S. V., Vernon, M. K., and Wood, D. A. Analytic Evaluation of Shared-memory Parallel Systems with ILP Processors. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Sricharan and Govindarajan 1999] Sricharan, V. and Govindarajan, R. Study of Cache and TLB Performance in a DVSM System. In *Proc. of the 1st Workshop on Software Distributed Shared Memory (WSDSM'99)*, June 1999.
- [Stenstrom 1990] Stenstrom, P. A Survey of Cache Coherence Schemes for Multiprocessors. *IEEE Computer*, 23(6):12–24, June 1990.
- [Stenstrom *et al.* 1997] Stenstrom, P., Brorsson, M., Dahlgren, F., Grahn, H., and Dubois, M. Boosting the Performance of Shared Memory Multiprocessors. *IEEE Computer*, 30(7):63–70, July 1997.
- [Sterbenz and Parulkar 1990a] Sterbenz, J. P. G. and Parulkar, G. M. AXON: Network Virtual Storage Design. *ACM Computer Communications Review*, 20(2):50–65, April 1990.
- [Sterbenz and Parulkar 1990b] Sterbenz, J. P. G. and Parulkar, G. M. Axon Network Virtual Storage for High Performance Distributed Applications. In *Proc. of the 10th Int'l Conf. on Distributed Computing Systems (ICDCS-10)*, May 1990.
- [Sterbenz *et al.* 1990] Sterbenz, J. P. G., Parulkar, G. M., and Gurudatta, M. AXON: A High Speed Communication Architecture for Distributed Applications. In *Proc. of IEEE INFOCOM'90*, pages 415–425, June 1990.
- [Strumpfen 1996] Strumpfen, V. Software-Based Communication Latency Hiding for Commodity Workstation Networks. In *Proc. of the 1996 Int'l Conf. on Parallel Processing (ICPP'96)*, August 1996.
- [Sudo *et al.* 1997] Sudo, Y., Suzuki, S., and Shibayama, S. Distributed-thread Scheduling Methods for Reducing Page-thrashing. In *Proc. of the Sixth IEEE Int'l Symp. on High Performance Distributed Computing (HPDC-6)*, pages 356–364, August 1997.
- [Sun *et al.* 1998] Sun, C., Huang, Z., Lei, W.-J., and Sattar, A. Towards Transparent Selective Sequential Consistency in Distributed Shared Memory Systems. In *Proc. of the 18th Int'l Conf. on Distributed Computing Systems (ICDCS-18)*, pages 572–581, May 1998.
- [Swanson *et al.* 1998] Swanson, M., Stroller, L., and Carter, J. B. Increasing TLB Reach Using Superpages Backed by Shadow Memory. In *Proc. of the 25th Annual Int'l Symp. on Computer Architecture (ISCA'98)*, June 1998.
- [Takesue 1998] Takesue, M. Schemes for Reducing Communication Latency in Regular Computations on DSM Multiprocessors. In *Proc. of the 1998 Int'l Conf. on Parallel Processing (ICPP'98)*, pages 164–171, August 1998.
- [Tam *et al.* 1990] Tam, M.-C., Smith, J. M., and Farber, D. J. A Taxonomy-Based Comparison of Several Distributed Shared Memory Systems. *ACM Operating Systems Review*, 24(3), July 1990.
- [Tanenbaum *et al.* 1994] Tanenbaum, A. S., Kaashoek, M. F., and Bal, H. E. Using Broadcasting to Implement Distributed Shared Memory Efficiently. In Casavant, T. L. and Singhal, M., editors, *Readings in Distributed Computing Systems*, pages 387–408. IEEE Computer Society Press, 1994.
- [Tanenbaum 1995] Tanenbaum, A. S. *Distributed Operating Systems*, chapter 6, pages 289–375. Prentice-Hall, Inc., 1995.
- [Thitikamol and Keleher 1997] Thitikamol, K. and Keleher, P. Multi-Threading and Remote Latency in Software DSMs. In *Proc. of the 17th Int'l Conf. on Distributed Computing Systems (ICDCS-17)*, May 1997.
- [Thitikamol and Keleher 1998] Thitikamol, K. and Keleher, P. Per-Node Multithreading and Remote Latency. *IEEE Transactions on Computers*, 47(4):414–426, April 1998.

- [Thitikamol and Keleher 1999a] Thitikamol, K. and Keleher, P. Thread Migration and Communication Minimization in DSM Systems. *Proc. of the IEEE, Special Issue on Distributed Shared Memory*, 87(3):487–497, March 1999.
- [Thitikamol and Keleher 1999b] Thitikamol, K. and Keleher, P. Communication-Intensive Parallel Applications and Non-Dedicated Environments. In *Proc. of the 3rd Workshop on Runtime Systems for Parallel Programming (RTSPP)*, April 1999.
- [Thitikamol and Keleher 1999c] Thitikamol, K. and Keleher, P. Active Correlation Tracking. In *Proc. of the 19th Int'l Conf. on Distributed Computing Systems (ICDCS-19)*, May 1999.
- [Tsanakas *et al.* 1992] Tsanakas, P., Papakonstantinou, G., and Efthivoulidis, G. Distributed Shared-Memory Implementation for Multitransputer Systems. *Information and Software Technology*, 34(8):499–506, August 1992.
- [Tseng 1995] Tseng, C.-W. Communication Analysis for Shared and Distributed Memory Machines. In *Proc. of the Workshop on Compiler Optimizations on Distributed Memory Systems*, October 1995.
- [Tumuluri and Choudhary 1996] Tumuluri, C. and Choudhary, A. N. Scalable Software Latency Hiding Schemes: Evaluation of the Poststore and Prefetch Operations. In *Proc. of the Second Int'l Euro-Par Conf.*, volume II, pages 498–491, August 1996.
- [Uehara *et al.* 1995] Uehara, K., Inohara, S., Miyazawa, H., Yamamoto, K., Hara, M., and Masuda, T. A Framework for Customizing Transactions in Persistent Object Management for Advanced Applications. In *Proc. of the Fourth Int'l Workshop on Object Orientation in Operating Systems (IWOOS'95)*, pages 84–903, August 1995.
- [Uehara *et al.* 1996] Uehara, K., Miyazawa, H., Yamamoto, K., Inohara, S., and Masuda, T. A Framework for Customizing Coherence Protocols of Distributed File Caches. In *Proc. of the 16th Int'l Conf. on Distributed Computing Systems (ICDCS-16)*, pages 83–90, May 1996.
- [Ueng *et al.* 1993] Ueng, J. C., Shieh, C. K., Mac, S. C., Lin, C. C., Lin, W. H., and Chen, K. L. Design considerations of a Distributed Shared Memory System Cohesion. In *Proc. of the National Computer Symp.*, pages 893–900, December 1993.
- [Ueng *et al.* 1997] Ueng, J. C., Shieh, C. K., Liang, T. Y., and Chang, J. B. Efficient Synchronization Mechanisms for Multithreaded Distributed Shared Memory Systems. In *Proc. of the National Computer Symp.*, pages E158–E163, December 1997.
- [Venkatesh and Kumar 1994] Venkatesh, S. and Kumar, M. A Survey of Distributed Shared Memory Systems. *Computer Science & Informatics*, 24(3):1–14, 1994.
- [Venkatesulu and Gonsalves 1995] Venkatesulu, D. and Gonsalves, T. A. A Queueing Model of Distributed Shared Memory. In *Proc. of the IFIP TC6 Conference 1994*, pages 265–278, January 1995.
- [Verghese *et al.* 1996] Verghese, B., Devine, S., gupta, A., and Rosenblum, M. Operating System Support for Improving Data Locality on CC-NUMA Compute Servers. In *Proc. of the 7th Symp. on Architectural Support for Programming Languages and Operating Systems (ASPLOSVII)*, pages 279–289, October 1996.
- [Vlassov and Thorelli 1997] Vlassov, V. and Thorelli, L.-E. A Synchronizing Shared Memory: Model and Programming Implementation. In *Proc. of the 4th European PVM/MPI Users' Group Meeting*, pages 158–166, November 1997.
- [Voelker *et al.* 1998] Voelker, G., Andreson, E., Kimbrel, T., Feeley, M., Chase, J., Karlin, A., and Levy, H. Implementing Cooperative Prefetching and Caching in a Global Memory System. In *Proc. of the 1998 ACM SIGMETRICS Conference on Performance Measurement, Modeling, and Evaluation*, pages 33–43, June 1998.

- [Voliotis *et al.* 1995] Voliotis, K., Manis, G., Thanos, A., Tsanakas, P., and Papakonstantinou, G. Facilitating the Development of Portable Parallel Applications on Distributed Shared Memory Systems. In *Proc. of the 1995 2nd Int'l Conf. on Programming Models for Massively Parallel Computers*, pages 176–183, October 1995.
- [Wang 1995] Wang, P. Solving A Highly Irregular Problem on Distributed Shared Memory Systems. In *Proc. of the 1995 ICPP Workshop on Challenges for Parallel Processing*, August 1995.
- [Weberpals and de Dinechin 1993] Weberpals, H. and de Dinechin, F. D. Analysis of Parallel Algorithms for a Shared Virtual Memory Computer. In *Proc. of the Int'l Conf. on Parallel Computing (ParCo93)*, pages 719–723, September 1993.
- [Wiess *et al.* 1998] Wiess, C., Knopp, J., and Hellwagner, H. Implementing Automatic Coordination on Networks of Workstations. In *Proc. of the 3rd Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'98)*, pages 72–81, March 1998.
- [West *et al.* 1997] West, R., Schwan, K., Tacic, I., and Ahamad, M. Exploiting Temporal and Spatial Constraints on Distributed Shared Objects. In *Proc. of the 17th Int'l Conf. on Distributed Computing Systems (ICDCS-17)*, May 1997.
- [Wilkinson *et al.* 1992] Wilkinson, T. J., Stiemerling, T., Osmon, P., Saulsbury, A., and Kelly, P. Angel: A Proposed Multiprocessor Operating System Kernel. Technical Report TCU/CS/1992/10, Dept. of Computer Science, City University, U.K., 1992.
- [Windisch 1996] Windisch, H.-M. The Distributed Programming Language INSEL—Concepts and Implementation. In *Proc. of the 1st Int'l Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'96)*, April 1996.
- [Xia and Torrellas 1996] Xia, C. and Torrellas, J. Improving the Performance of the Data Memory Hierarchy for Multiprocessor Operating Systems. In *Proc. of the 2nd IEEE Symp. on High-Performance Computer Architecture (HPCA-2)*, February 1996.
- [Xu *et al.* 1997] Xu, Z., Larus, J. R., and Miller, B. R. Shared-Memory Performance Profiling. In *Proc. of the Sixth ACM SIGPLAN Symp. on Principles and Practice of Parallel Programming (PPOPP'97)*, pages 240–251, June 1997.
- [Yamada *et al.* 1995] Yamada, S., Tanaka, S., and Maruyama, K. A Message-Coupled Architecture (MESCAR) for Distributed Shared-Memory Multiprocessors. In *Proc. of the 1995 Int'l Conf. on Parallel Processing (ICPP'95)*, volume I, pages 19–23, August 1995.
- [Yamamoto and Kudoh 1998] Yamamoto, J. and Kudoh, T. Analysis of Communication Traffic of Shared Memory Based Programs. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, volume II, pages 843–850, July 1998.
- [Yamashita 1995] Yamashita, H. Data-cyclic Shared Memory (DCSM) in Distributed Environments. *IEICE Trans. on Communications*, E78-B(11):1372–1379, November 1995.
- [Yang *et al.* 1998a] Yang, Z., Sun, C., and Sattar, A. Consistent Global States of Mobile Distributed Computations. In *Proc. of the Int'l Conf. on Parallel and Distributed Processing Techniques and Applications (PDPTA'98)*, July 1998.
- [Yang *et al.* 1998b] Yang, Z., Sun, C., and Sattar, A. Guaranteed Mutually Consistent Checkpointing in Distributed Computations. In *Proc. of the 4th Asian Computing Science Conference (ASIAN'98)*, December 1998.
- [Yeung *et al.* 1996] Yeung, D., Kubiawicz, J., and Agarwal, A. MGS: A Multigrain Shared Memory System. In *Proc. of the 23rd Annual Int'l Symp. on Computer Architecture (ISCA'96)*, pages 45–55, May 1996.

- [Y. Yi and Yeom 1998] Y. Yi, T. P. and Yeom, H. Y. A Causal Logging Scheme for Lazy Release Consistent Distributed Shared Memory Systems. In *Proc. of the 1998 Int'l Conf. on Parallel and Distributed Systems (ICPADS'98)*, December 1998.
- [You *et al.* 1996] You, J., Zhou, W. P., and Cohen, H. A. Parallel Image Matching on Distributed Shared Memory Network. In *Proc. of the First Int'l Conf. on Visual Information Systems*, pages 394–402, February 1996.
- [Yu and Cox 1997] Yu, W. M. and Cox, A. L. Java/DSM: a Platform for Heterogeneous Computing. In *Proc. of Java for Computational Science and Engineering—Simulation and Modeling Conf.*, pages 1213–1224, June 1997.
- [Zeisset 1993] Zeisset, S. Evaluation and Enhancement of the Paragon Multiprocessor's Shared Virtual Memory System. Master's thesis, TU Munich, November 1993.
- [Zeisset *et al.* 1996] Zeisset, S., Tritscher, S., and Mairandres, M. A New Approach to Distributed Memory Management in the Mach Microkernel. In *Proc. of the USENIX 1996 Annual Technical Conference*, pages 205–217, January 1996.
- [Zekauskas *et al.* 1994] Zekauskas, M. J., Sawdon, W. A., and Bershad, B. N. Software Write Detection for a Distributed Shared Memory. In *Proc. of the 1st Symp. on Operating Systems Design and Implementation (OSDI'94)*, pages 87–100, November 1994.
- [Zhu and Watson 1997a] Zhu, H. and Watson, I. Identifying Critical Loads in Real Programs for Decoupled VSM Systems. In *Proc. of the Third Int'l Euro-Par Conf.*, pages 302–305, August 1997.
- [Zhu and Watson 1997b] Zhu, H. and Watson, I. Compiler Support for Decoupled Virtual Shared Memory Systems. In *Proc. of the 16th Annual ACM Symp. on Principles of Distributed Computing (PODC'97)*, page 282, August 1997.
- [Zhu *et al.* 1997a] Zhu, W., Liang, T.-Y., Shieh, C.-K., and Lai, A.-C. Two Task Mapping/Scheduling Methods for DSM Systems—Simulated Annealing Mapping vs. Dynamic Loop Scheduling. In *Proc. of the 20th Australasian Computer Science Conf. (ACSC'97)*, pages 209–217, February 1997.
- [Zhu *et al.* 1997b] Zhu, W., Liang, T.-Y., and Shieh, C.-K. A New Approach for Task Clustering. In *Proc. of the 1997 IEEE Int'l Conf. on Intelligent Processing Systems*, volume 1, pages 538–542, October 1997.
- [Zhu *et al.* 1997c] Zhu, W., Liang, T.-Y., and Shieh, C.-K. Using Extended Neural Network Map Tasks. In *Proc. of the 1997 IEEE Int'l Conf. on Systems, Man, and Cybernetics, Computational Cybernetics and Simulation*, volume 3, pages 2927–2932, October 1997.
- [Zhuang *et al.* 1998] Zhuang, Y. C., Shieh, C. K., and Liang, T. Y. Centralized Load Balance on Distributed Shared Memory Systems. In *Proc. of the Fourth Workshop on Compiler Techniques for High-Performance Computing (CTHPC'98)*, pages 166–174, March 1998.
- [Zimmermann and Kumm 1993] Zimmermann, W. and Kumm, H. On the Implementation of Virtual Shared Memory. In *Proc. of the 1993 1st Int'l Conf. on Programming Models for Massively Parallel Computers*, pages 172–178, September 1993.