Dynamic Average Modeling of Front-End Diode Rectifier Loads Considering Discontinuous Conduction Mode and Unbalanced Operation

IEEE Task Force on Dynamic Average Modeling

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Abstract-Electric power distribution systems of many commercial and industrial sites often employ variable frequency drives and other loads that internally utilize dc. Such loads are often based on front-end line-commutated rectifiers. The detailed switch-level models of such rectifier systems can be readily implemented using a number of widely available digital programs and transient simulation tools, including the Electromagnetic Transient (EMT)-based programs and Matlab/Simulink. To improve the simulation efficiency for the system-level transient studies with a large number of such subsystems, the so-called dynamic average models have been utilized. This paper presents the average-value modeling methodologies for the conventional three-phase (six-pulse) front-end rectifier loads. We demonstrate the system operation and the dynamic performance of the developed average models in discontinuous and continuous modes, as well as under balanced and unbalanced operation.

Index Terms—Average-value modeling, digital simulation, linecommutated converters, operational modes, rectifiers.

I. INTRODUCTION

T HE conventional three-phase (six-pulse) line-commutated rectifiers are commonly found as the input stage in low-to medium-power variable frequency drives and motor loads that are widely used in industrial and commercial applications [1]–[7]. These loads are often referred to as the front-end rectifier loads [8], [9], and may appear in large numbers in distribution systems, industrial facilities [10], distributed generation [11], transportation [12], as well as arc furnaces [13], [14]. A typical configuration considered in this paper is shown in Fig. 1. Depending on the application, the source may be a distribution feeder (or transformer) as in Case I, or a rotating ma-

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Fig. 1. Typical configuration of a front-end rectifier load system.

chine (generator) as in Case II. The ac filter network may take various configurations depending on application and cost constraints, wherein phase-shifting transformers, passive or active filters may be considered to reduce the harmonics. Commonly used passive filters include series choke inductors and the shunt filters that are tuned to lower harmonics (typically 5th and 7th) [15], [16].

The modeling of rectifier systems has been of particular interest to the power engineering community for a long time [17], [18]. The detailed modeling of power systems that contain rectifier loads may be readily carried out using digital programs such as the Alternate Transients Program (ATP) [19], EMTP-RV [20], PSCAD/EMTDC [21], Matlab/Simulink [22], etc. The main challenge of using the detailed models is the increased computational complexity which leads to longer simulation times especially when the overall system contains many rectifiers and switching modules.

Dynamic average modeling has been utilized as a mean of removing the high-frequency switching from the model while preserving the lower frequency dynamics [23]. This Task Force paper describes the variations of the six-pulse rectifier system depicted in Fig. 1 that are commonly encountered in practical applications leading to discontinuous and continuous conduction modes (DCM and CCM), respectively. The basic approaches to develop appropriate dynamic average models are also presented. The transient performance of several example models is demonstrated for various operating conditions, including light and heavy loading conditions, discontinuous and continuous modes, as well as balanced and unbalanced ac side. The dynamic model of front-end rectifiers with unbalanced input voltages is particularly desirable when studying single-phase faults and voltage sags [24].

II. MODES OF OPERATION

To set the stage for discussing the system operation and modeling, a simplified circuit diagram for the front-end rectifier load

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Fig. 2. Simplified circuit diagram of a typical three-phase front-end rectifier load system.

system considered in this paper is depicted in Fig. 2. Since the presence of shunt filters does not change the development of dynamic average models in general, only series filters are considered here. The ac network is represented by its Thevenin equivalent voltages e_{abcs} , series resistance r_{th} and inductance L_{th} . The impedance of an optional series ac filter is represented by r_{ac} , L_{ac} . The combined equivalent impedance of the ac subsystem is denoted by r_s and L_s , respectively. The dc filter represented in Fig. 2 by r_{dc} , L_{dc} , and C is also optional and may be partially present in the system.

If the system is fed from a synchronous generator (Fig. 1 Case II), the machine can be represented using the voltage-behind-reactance formulation, which results in a circuit similar to Fig. 2 (but possibly with coupled and/or variable equivalent inductances) [25]–[27]. In many cases, such as battery charging [28], [29] and variable-speed-drive applications [8], [9], the dc filter inductor may be omitted, whereas the capacitor is included to smooth the dc bus voltage. The ac filter inductor must often be included in order to reduce the ac-side current harmonics.

To represent an equivalent energy dissipating load on the dc side in Fig. 2, a simple resistor R_L is connected to the dc bus [28], [30], [31]. Although such an assumption ignores the interharmonics [32], it is deemed acceptable in lower power applications [33], [34], and is considered sufficient for the purpose of this paper.

In this system, a discontinuous conduction mode (DCM) operation is typically observed at light load. This mode is frequently encountered in the front-end rectifiers of low- to medium power variable frequency drives [30], [32], where the ac filter is often not used (or is very small) but there is a significant capacitor on the dc bus. The corresponding waveforms are illustrated in Fig. 3(a). There, six equal switching intervals exist within a single electrical cycle. In DCM, each switching interval is divided into two subintervals. During the conduction subinterval, t_{cond} , two diodes are conducting and two of the phases carry the dc bus current in opposite directions. At some point, the line-to-line voltage in these two phases becomes smaller than the dc bus voltage and the current reaches zero and remains zero for the rest of the interval-hence discontinuous mode. This subinterval is denoted by $t_{
m dcm}$ in Fig. 3(a). During $t_{\rm dcm}$, all diodes are off. The dc load is, meanwhile, being fed from the dc capacitor as observed in the dc bus voltage waveform v_{dc} . The switching pattern in the DCM is therefore 2-0.



Fig. 3. Typical current and voltage waveforms of the six-pulse rectifier. (a) Operation in DCM. (b) Operation in CCM.

If the combined value of the source and ac filter inductances becomes sufficiently large [30], [35], or the load on the dc bus is sufficiently increased, the dc bus current becomes continuous—hence continuous mode. In this case, as the load varies

| THREE-FHASE (0-FULSE) RECTIFIER | | | | | | | |
|---------------------------------|------------|--------------------------------|--|--|--|--|--|
| Operational | Conduction | Commutation | | | | | |
| Modes | Pattern | Angle | | | | | |
| DCM | 2-0 | $\mu = 0^{\circ}$ | | | | | |
| CCM-1 | 2-3 | $0^{\circ} < \mu < 60^{\circ}$ | | | | | |
| CCM-2 | 3 | $\mu = 60^{\circ}$ | | | | | |
| CCM-3 | 3-4 | $60^\circ < \mu < 120^\circ$ | | | | | |

TABLE I OPERATIONAL MODES OF THE CONVENTIONAL THREE-PHASE (6-PULSE) RECTIFIER

from a light load to a short circuit, three different switching patterns can be observed, resulting in three distinct continuous conduction modes (CCM) of operation [36]. The typical waveforms of the phase currents and the corresponding dc bus voltages are shown in Fig. 3(b). The operational modes are summarized in Table I, together with the conduction pattern and the commutation angle.

Within CCM-1 [see Fig. 3(b), top plot], each switching interval is divided into two subintervals referred to as commutation and conduction [37]. During the conduction subinterval $t_{\rm cond}$, only two diodes conduct; whereas during the commutation subinterval $t_{\rm com}$, corresponding to the commutation angle $\mu = \omega_e \cdot t_{\rm com}$, the current is being switched from one phase to another and three diodes conduct. Therefore, a conduction pattern of 2–3 diodes is observed within each 60 electrical degrees, and $0^\circ < \mu < 60^\circ$.

The mode CCM-2 [see Fig. 3(b), middle plot], may be achieved by further increasing the load current. In this mode, the commutation angle μ increases and reaches 60°, resulting in the disappearance of the conduction subinterval. Consequently, there will be three diodes carrying current throughout the switching intervals. Hence, the conduction pattern becomes just 3 (or 3-3).

If the load current is further increased, the commutation angle μ starts to increase, resulting in the third mode, CCM-3 [see Fig. 3(b), bottom plot]. This changes the switching pattern to 3–4 conducting diodes. Note that this mode contains a topology with four simultaneously conducting diodes, which short-circuits the output as observed in Fig. 3(b), (bottom plot). This last mode rarely occurs in practical systems, but it is possible in rotating machines and brushless exciters with very large inductances [38].

Certain variations in topology of the system of Fig. 2 may prevent the occurrence of some of the operational modes. For instance, without the dc filter inductor, the CCM-3 mode cannot occur and the system could only operate in DCM, CCM-1, or CCM-2. Comparing the phase currents in DCM depicted in Fig. 3(a), it can also be concluded that such currents will inject significant harmonics into the network. To avoid operation in DCM, medium and large drives with front-end rectifiers often install additional ac filters (series inductors and shunt filters) to shift the operation to CCM-1 or CCM-2.

III. DYNAMIC AVERAGE MODELING

The dynamic average models for the six-pulse converters can be generally developed using two main approaches (i.e., analytical [37], [39]–[43] and parametric [44], [45]). Using the first



Fig. 4. Relationship among the variables in the converter and the arbitrary reference frames.

approach, the system variables are analytically averaged over a prototypical switching interval [37], and the system state and algebraic equations are derived. The fast averaging over a prototypical switching interval T_s is defined as

$$\overline{f}(t) = \frac{1}{T_s} \int_{t-T_s}^t f(\tau) d\tau \tag{1}$$

where f is a network variable (e.g., voltage or current) and \overline{f} is the so-called fast average of f. While the definition (1) may be directly applied to the dc variables, the ac variables first have to be transformed into an appropriate synchronously rotating reference frame [37]. Here, we adopt the qd-reference frame convention where the q axis is leading the d axis by 90° [37]. In this reference frame, the resulting qd variables in steady state are composed of a constant (dc) component and a ripple. The averaging of these variables using (1) removes the switching ripple but preserves the slower (dc) variations of the transformed variables. The converter reference frame is typically chosen wherein the d axis component of voltage is zero as depicted in Fig. 4.

The relationship between the voltages in the converter and arbitrary reference frames may be written as

$$\begin{bmatrix} v_{qs}^c \\ 0 \end{bmatrix} = \begin{bmatrix} \cos(\phi_c) & \sin(\phi_c) \\ -\sin(\phi_c) & \cos(\phi_c) \end{bmatrix} \begin{bmatrix} v_{qs}^a \\ v_{ds}^a \end{bmatrix}.$$
 (2)

The angle between the converter and arbitrary reference frames may also be deduced from Fig. 4 as

$$\phi_c = \tan^{-1} \left(\frac{v_{ds}^a}{v_{qs}^a} \right). \tag{3}$$

Table II lists various dynamic average models present in the literature for the six-pulse system together with the list of features included in each model. The first challenge in deriving the AVMs analytically is the existence of various operational modes discussed previously. The AVM is typically considered for the most common mode of operation (i.e., CCM-1). In order to extend such models to all modes, the corresponding equations for each mode must be derived and averaged using a similar procedure. Moreover, the boundary conditions for each of the modes should be established. Therefore, there will be an AVM valid

| Models | steady-state/ dynamic | dynamic order (full/reduced) | DCM, CCMs (1, 2, 3) | ac filter (r_{ac}, L_{ac}) | $\frac{dc \text{ filter}}{(r_{dc}, L_{dc}, C)}$ | variable ac inductance (yes/no) |
|------------|--------------------------|---------------------------------|------------------------|------------------------------|---|------------------------------------|
| [37] | dynamic | reduced | CCM 1 | L _{ac} | r_{dc}, L_{dc}, C | no |
| [46] | dynamic | reduced | CCM 2 | r_{ac}, L_{ac} | С | no |
| [47] | dynamic | reduced | CCM 2 | L_{ac} | С | no |
| [39], [40] | dynamic | reduced | CCM 1 | r_{ac}, L_{ac} | r_{dc} , L_{dc} , C | no |
| [41] | steady-state | N/A | CCM 1 | - | r_{dc}, L_{dc}, C | yes |
| [42] | dynamic | reduced | CCM 1 | - | r_{dc}, L_{dc}, C | yes |
| [43] | dynamic | reduced | CCM 1 | - | С | yes |
| [44] | dynamic | full | CCM 1 | - | С | yes |
| [38] | dynamic | reduced | CCMs 1, 2, 3 | - | r_{dc}, L_{dc}, C | yes |
| [45] | dynamic | full | CCMs 1, 2, 3 | - | r_{dc}, L_{dc}, C | yes |

 TABLE II

 Average Models of the Three-Phase Rectifier Systems

for each operational mode that is in the range of interest. These models can then be "switched" as the system changes modes, which makes this approach additionally challenging.

Other challenges of the analytical approach include the difficulty of establishing closed-form explicit equations describing the system in more complicated configurations (e.g., machine-fed converter systems) (Case II in Fig. 1) [41]-[43]. In many cases, the final model will be implicit and would require an iterative solution. Analytically derived AVMs have been formulated in [37], [39], [40], and [41]–[43] for voltage-sourceand machine-fed converters, respectively. The model [37], for example, in addition to only being valid in CCM-1 mode, also neglects the resistance on the ac side r_s in Fig. 2. Subsequent improvements have been done in [39] to include the ac-side resistance and improve the model dynamics. The particular case of three-phase rectifier feeding constant-voltage loads has been addressed in [46] and [47]. A parametric approach has been considered in [44] and [45]. An extension of this method to more general cases and topologies is rather straightforward. Recent work also includes the average modeling of power-electronic systems using polynomial chaos basis [48].

IV. EXAMPLES OF BASIC AVERAGE-VALUE MODELS

Among various models provided in Table II, three basic models are considered in this paper.

A. Classical Reduced-Order Model (AVM-1)

The classical model [37] requires the state equation that describes the dc bus current dynamics as

$$\frac{d\bar{i}_{\rm dc}}{dt} = \frac{\frac{3\sqrt{3}}{\pi}\sqrt{2}E - \left(\frac{3}{\pi}\omega_e L_s + r_{\rm dc}\right)\bar{i}_{\rm dc} - v_C}{2L_s + L_{\rm dc}} \tag{4}$$

where E is the rms value of the phase voltage, and v_C is the dc bus voltage on the filter capacitor.

To establish the average q and d axes components of the phase currents, the dc current is assumed constant and equal to its average value during the switching interval. The current dynamics disappear, resulting in a reduced-order model. The phase currents are then expressed during conduction and commutation subintervals and are averaged, respectively. The result of this procedure yields the following equations:

$$\overline{i}_{qs,com}^{c} = \frac{-2\sqrt{3}}{\pi} \overline{i}_{dc} \left[\sin\left(\mu - \frac{5\pi}{6}\right) + \sin\left(\frac{5\pi}{6}\right) \right]
- \frac{3}{\pi} \frac{\sqrt{2E}}{\omega_{e}L_{s}} (\cos\mu - 1)
+ \frac{3}{4\pi} \frac{\sqrt{2E}}{\omega_{e}L_{s}} (\cos(2\mu) - 1),$$
(5)
$$\overline{i}_{ds,com}^{c} = \frac{-2\sqrt{3}}{\pi} \overline{i}_{dc} \left[\cos\left(\frac{5\pi}{6}\right) - \cos\left(\mu - \frac{5\pi}{6}\right) \right]
- \frac{3}{\pi} \frac{\sqrt{2E}}{\omega_{e}L_{s}} \sin\mu + \frac{3}{4\pi} \frac{\sqrt{2E}}{\omega_{e}L_{s}} (\sin(2\mu) + 2\mu), (6)$$

$$\overline{i}_{ds}^{c} = \frac{-2\sqrt{3}\pi}{\pi} \left[-\frac{\sqrt{3}\pi}{4\pi} \left[-\frac{\sqrt{3}\pi}{4\pi} \right] \left[-\frac{5\pi}{4\pi} \left[-\frac{\sqrt{3}\pi}{4\pi} \right] \right]$$

$$\bar{i}_{\rm qs,cond}^c = \frac{-2\sqrt{3}}{\pi} \bar{i}_{\rm dc} \left[\sin\left(\frac{i\pi}{6}\right) - \sin\left(\mu + \frac{5\pi}{6}\right) \right], \quad (7)$$

$$\overline{i}_{\rm ds,cond}^c = \frac{-2\sqrt{3}}{\pi} \overline{i}_{\rm dc} \left[\cos\left(\mu + \frac{5\pi}{6}\right) - \cos\left(\frac{7\pi}{6}\right) \right]. \tag{8}$$

The final averaged ac-side currents are obtained as

$$\overline{i}_{qs}^c = \overline{i}_{qs,com}^c + \overline{i}_{qs,cond}^c,$$
(9)
$$\overline{i}_{ds}^c = \overline{i}_{ds,com}^c + \overline{i}_{ds,cond}^c.$$
(10)

The model defined by (4)–(10) is referred to as AVM-1.

B. Improved Reduced-Order Model (AVM-2)

A similar model has been derived in [39]; where instead of assuming a constant value for the dc current during the switching interval, this current is assumed to change as

$$i_{\rm dc}(\theta) = i_{\rm dc0} + k \cdot \left(\theta - \frac{\mu}{2}\right) \tag{11}$$

where i_{dc0} is the average value of i_{dc} during the commutation period, and k is the derivative $di_{dc}/d\theta_e$ during this period of

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time. The effect of ac-side resistance has been also partly taken into account. The resulting model has the following form:

$$\begin{bmatrix} \frac{\pi}{3} - \mu + \frac{3\mu^2}{4\pi} r_s + \frac{4\pi - 3\mu}{2\pi} L_s + \frac{\pi}{3} - \mu}{2\omega_e} r_{\rm dc} + L_{\rm dc} \end{bmatrix} \frac{di_{\rm dc}}{dt}$$
$$= \frac{3\sqrt{3}}{\pi} \sqrt{2}E \left(1 - \frac{r_s}{\omega_e L_s} \frac{\mu - \sin\mu}{2} \right)$$
$$- \left(\frac{2\pi - 3\mu}{\pi} r_s + \frac{3}{\pi} \omega_e L_s + r_{\rm dc} \right) i_{\rm dc0} - v_C \qquad (12)$$
$$\bar{i}_{qs}^c = -\frac{2\sqrt{3}}{\pi} i_{\rm dc0} \cos\mu + \frac{\sqrt{3}}{\pi} k \left(-\sin\mu + \frac{\pi}{3} \right)$$

$$+\frac{3}{\pi}\frac{\sqrt{2}E}{\omega_e L_s}\left(\cos\mu - \frac{\cos(2\mu)}{4} - \frac{3}{4}\right)$$
(13)

$$\bar{i}_{ds}^{c} = -\frac{2\sqrt{3}}{\pi} i_{dc0} \sin\mu + \frac{\sqrt{3}}{\pi} k \left(\cos\mu + 1 - \frac{\sqrt{3}\pi}{3} \right) + \frac{3}{\pi} \frac{\sqrt{2}E}{\omega_e L_s} \left(\sin\mu - \frac{\sin(2\mu)}{4} - \frac{\mu}{2} \right).$$
(14)

The model defined by (12)–(14) is referred to as AVM-2. Both AVM-1 and AVM-2 utilize the same commutation angle

$$\mu = \cos^{-1} \left(1 - \frac{\sqrt{2}\omega_e L_s}{\sqrt{3}E} \overline{i}_{dc} \right). \tag{15}$$

C. Parametric Average-Value Model (PAVM)

In parametric approach [45], the rectifier switching cell is considered as an algebraic block that relates the dc voltage and current \bar{v}_{dc} , \bar{i}_{dc} to the ac voltages and currents $\bar{\mathbf{v}}_{qds}^c$ and $\bar{\mathbf{i}}_{qds}^c$ through the respective parametric functions as

$$\left\| \bar{\mathbf{v}}_{qds}^c \right\| = \alpha(.) \bar{v}_{dc},\tag{16}$$

$$\overline{i}_{\rm dc} = \beta(.) \left\| \overline{\mathbf{i}}_{qds}^c \right\| \tag{17}$$

where $\alpha(.)$ and $\beta(.)$ are algebraic functions of the loading conditions. To complete this model, the angle between the vectors $\bar{\mathbf{v}}_{ads}^c$ and $\bar{\mathbf{i}}_{ads}^c$ is calculated based on Fig. 4 as

$$\phi(.) = \tan^{-1} \left(\frac{\overline{i}_{ds}^a}{\overline{i}_{qs}^a} \right) - \tan^{-1} \left(\frac{\overline{v}_{ds}^a}{\overline{v}_{qs}^a} \right).$$
(18)

Deriving closed-form analytical expressions for $\alpha(.)$, $\beta(.)$, and $\phi(.)$ is impractical. Instead, these functions may be extracted by using the simulation and expressed in terms of dynamic impedance of the switching cell defined as [45]

$$z = \frac{\bar{v}_{\rm dc}}{\left\| \mathbf{\bar{i}}_{qds}^c \right\|}.$$
(19)



Fig. 5. Example of parametric average-value model implemented in PSCAD. (a) PAVM block together with controllable sources and interfacing ports. (b) AVM module interfaced with external ac and dc subsystems.

V. EXAMPLE OF AVM IMPLEMENTATION IN PSCAD

In this paper, PSCAD/EMTDC, EMTP-RV, and Matlab/ Simulink have been used to implement the considered models. The results predicted by all packages are essentially identical, provided the time step and solver properties are chosen appropriately. The system parameters are summarized in the Appendix.

To illustrate the use of AVM in EMTP-type packages, an example implementation of the PAVM in PSCAD is shown in Fig. 5. The block shown in Fig. 5(a) contains (16)-(19) and the transformations between qd and abc coordinates. To interface the AVM with the external ac network, three controllable voltage sources are used whose values are determined by (16) transformed into *abc* coordinates. The dc side is interfaced using a controllable current source whose value is determined by (17). The values of the ac currents and the dc voltage are read from the network solution and taken as the inputs into the algebraic block, wherein the ac currents are transformed into qd coordinates for use in (17). The subsystem of Fig. 5(a) is encapsulated into a single module block that is then interconnected using its nodes with the external network as shown in Fig. 5(b). This AVM block can then replace the detailed switching rectifier module within a larger ac and dc network (which may include ac filters, for example, shunt harmonic filters, etc.). Fig. 5(b) also depicts the timed breaker blocks that are used to implement the system changes.

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Fig. 6. Transient response of the six-pulse rectifier system in DCM.

VI. DYNAMIC PERFORMANCE UNDER BALANCED CONDITIONS

A. Operation in DCM

The rectifier system is assumed to operate without the ac filter but with the dc capacitor in order to enable the DCM. In the following study, the system initially operates in steady state in DCM with load $R_L = 35 \Omega$. At t = 0.3 s, the load resistance is stepped to $R_L = 70 \Omega$, which forces the system deeper into the DCM operation at a lighter load. The transient responses obtained by the considered detailed and average models are shown in Fig. 6. The analytical models AVM-1 and AVM-2 predict the dc and ac currents with reasonable accuracy, including the change in operating conditions. The PAVM shows somewhat higher damping than the other models but with a superior prediction of the dc voltage.

B. Operation in CCM

To investigate the performance in CCM, the ac series filter is added. In the following study, the system is assumed to start from zero initial conditions in CCM-1 with the load R_L = 11.9 Ω . At t = 0.05 s, the load is stepped to $R_L = 2 \Omega$ forcing the system into the CCM-2 [see Fig. 3(b)]. The resulting transient response predicted by various models is shown in Fig. 7. This figure clearly shows that although AVM-1 and AVM-2 have been derived for CCM-1, their transient response does not exactly follow the dynamic response of the detailed model. The improved model AVM-2 predicts the transient slightly better than the classical model AVM-1. The PAVM perfectly follows the initialization transient including the overshoot oscillations. At t = 0.05 s, the rectifier system undergoes another transient and transitions into CCM-2. Both AVM-1 and AVM-2 clearly do not follow this transition very well and predict higher dc voltage and current. However, the response of the full-order



Fig. 7. Transient response of the six-pulse rectifier system with the dc capacitor.

PAVM model is very much consistent with the transient and new steady state predicted by the detailed model.

VII. PERFORMANCE UNDER UNBALANCED CONDITIONS

In this section, the performance of the detailed and average models under an unbalanced condition is demonstrated. It is very desirable to have the average models that are capable of accurately predicting the result of the ac network unbalance and its impact on the ac and dc sides of the system in DCM and CCM.

A. Unbalanced Operation in DCM

For implementing the DCM operation, the ac input filter is removed and the dc capacitor is added to the rectifier system. In the following study, the rectifier system initially operates under a balanced condition with a resistive load of $R_L = 20 \Omega$. This operating point is close to the boundary between DCM and CCM-1. At t = 0.08 s, a phase shift of 45° in the c-phase voltage e_{cs} is introduced, making the three phases asymmetric. This change in input voltage throws the rectifier system into unbalanced operation in DCM. Next, at t = 0.11 s, the load is stepped to $R_L = 50 \Omega$ making the DCM operation even lighter. The corresponding transient responses are shown in Figs. 8 and 9. As can be seen in Fig. 8, the heavy asymmetry of the input ac voltages leads to a pronounced change in the conduction pattern of the rectifier diodes, making the ac currents particularly spiky and uneven among the phases. The resulting dc current and voltage are shown in Fig. 9. It can be observed in Figs. 8 and 9 that the models AVM-1 and AVM-2 do not predict this condition well by producing larger ripple in dc voltage and current. At the same time, PAVM appears to predict the dc variables with much greater accuracy, closely resembling the peaks and



Fig. 8. Transient response of the rectifier system ac phase currents to a change in ac voltages leading to unbalanced operation among the phases in DCM.



Fig. 9. Transient response of the rectifier system dc current and voltage to a change in ac voltages leading to unbalanced operation in DCM.

fluctuation produced by the detailed simulation of the rectifier system.

B. Unbalanced Operation in CCM

To implement the CCM, the input ac filter is added to the system, and the dc capacitor is removed. In the following study, the rectifier is assumed to start in CCM-1 with a balanced three-phase source and a resistive load $R_L = 15 \Omega$. At t = 0.03 s, the



Fig. 10. Transient response of the rectifier system ac phase currents in CCM under unbalanced ac input voltages.



Fig. 11. Transient response of the dc current and voltage of the rectifier system in CCM under unbalanced ac input voltages.

magnitude of e_{cs} is reduced by half leading to unbalanced operation among the rectifier phases. Then, at t = 0.06 s, the load is stepped to $R_L = 5 \Omega$, which changes the mode to CCM-2. The resulting ac phase currents predicted by all models are illustrated in Fig. 10. The corresponding dc bus current and voltage are shown in Fig. 11. Due to limited space, only the results of PAVM and the detailed model are illustrated. The results of PAVM obtained using PSCAD, EMTP-RV, and Simulink are all superimposed, and are essentially identical. The PAVM predicts the unbalanced operation of dc and ac variables quite well.

| Model | Step Size, Δt | # Time Steps | |
|---------------------|-----------------------|--------------|--|
| Detailed - PSCAD | 50 µs | 2,001 | |
| Detailed - Simulink | 1 <i>ms</i> (max) | 1,042 | |
| PAVM - PSCAD | 200 µs | 501 | |
| PAVM – EMTP-RV | 300 µs | 334 | |
| PAVM - Simulink | 1 <i>ms</i> (max) | 247 | |

We also compare the time steps needed for simulation by various models. The number of time steps taken by the considered models for the study of Figs. 10 and 11 is summarized in Table III. In PSCAD, the detailed model is run by using the typical EMTP time step of 50 μ s. In Simulink, a variable-step solver ODE15s has been used which dynamically adjusts the time-step size and may therefore require fewer time steps as seen in Table III. Here, the maximum allowable time step of 1 ms and absolute and relative tolerances of 1e-4 have been used. As seen in Table III, the PAVMs implemented in PSCAD, EMTP-RV, and Simulink require much fewer time steps and are therefore significantly faster than the respective detailed models. For consistency, the PAVM-Simulink model was run with the same solver, ODE15s, step size limits, and tolerances as the corresponding detailed model. To obtain a similar result, the PAVM in PSCAD and EMTP-RV had to use a time step of 200 and 300 μ s, respectively.

VIII. CONCLUDING REMARKS

This paper gives an overview of dynamic average modeling of the front-end rectifier load systems. The typical configurations of the three-phase front-end rectifier with and without the smoothing ac choke inductor, and variations of the dc filter, are also discussed and shown to result in the two most common operating modes (i.e., DCM and CCM). The models AVM-1 and AVM-2 can be used quite effectively for the transient studies in CCM-1 under balanced as well as unbalanced conditions among the ac phases. The PAVM model is shown to have good accuracy over a wider range covering DCM and all submodes of CCM, under balanced and unbalanced input ac voltages. The presented models have been implemented in PSCAD/EMTDC, EMTP-RV, and Matlab/Simulink, which are commonly used for conducting transient studies. The presented models, studies, and the literature review will be useful to the researchers and practicing engineers who are actively using digital programs and transient simulation tools for modeling and analyzing power systems containing front-end rectifiers.

APPENDIX

Medium power front-end rectifier system parameters: $\sqrt{3}E = 480$ V, $f_e = 60$ Hz, $r_{th} = 0.01 \Omega$, $L_{th} = 500 \mu$ H, $r_{ac} = 0.091 \Omega$, $L_{ac} = 9.545$ mH, $r_{dc} = 0$, $L_{dc} = 0$, $C = 500 \mu$ F.

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