Parallel relaxation-based joint dynamic state estimation of large-scale power systems

ISSN 1751-8687 Received on 2nd July 2015 Revised on 9th September 2015 Accepted on 29th September 2015 doi: 10.1049/iet-gtd.2015.0808 www.ietdl.org

Engineering and Technology

Journals

The Institution of

Hadis Karimipour, Venkata Dinavahi 🖂

Department of Electrical and Computer Engineering, University of Alberta, Edmonton, Alberta T6G 2V4, Canada E-mail: dinavahi@ece.ualberta.ca

Abstract: Massive amounts of data generated in large-scale grids poses a formidable challenge for real-time monitoring of power systems. Dynamic state estimation which is a prerequisite for normal operation of power systems involves the time-constrained solution of a large set of equations which requires significant computational resources. In this study, an efficient and accurate relaxation-based parallel processing technique is proposed in the presence of phasor measurement units. A combination of different types of parallelism is used on both single and multiple graphic processing units to accelerate large-scale joint dynamic state estimation simulation. The estimation results for both generator and network states verify that proper massive-thread parallel programming makes the entire implementation scalable and efficient with high accuracy.

1 Introduction

Dynamic state estimation (DSE) [1] which is paramount for secure monitoring and control of a power system to ensure its stability and reliability, possesses the ability to predict the system states in advance and within a short time interval. By modelling the time varying nature of the system to provide a full dynamic view of its behaviour, DSE alleviates losses under drastic changes during load fluctuations or network switching. Continued growth in demand followed by system development and complex interconnections within the new smart grid paradigm has led to significant operational and control problems. These problems necessitate the need for major changes in computational resources for real-time action by system operators in energy control centres which are hard to achieve using traditional measurements provided by supervisory control and data acquisition system (SCADA) [2]. Advancements in high-performance computing (HPC) [3] such as graphic processing unit (GPU) technology along with the increasing deployment of high-speed time-synchronised phasor measurement units (PMUs) [4] in wide areas provide the opportunity for real-time monitoring of the dynamic states, e.g., generator rotor angles and generator speed, in addition to the static network states of voltage magnitudes and phase angles.

Conventional methods to improve the cycle time for state estimation include the following main strategies:

Most of the approaches dealing with DSE try to improve the computational performance of the steady-state estimation process

[17-20] which only provides a series of snapshots of system conditions, where the dynamic transition between the snapshots is overlooked. However, few researchers focused on the dynamic parameters of the synchronous generator, which plays a vital role in a power system [21-24].

Although the above approaches tried to improve the process of state estimation, they all have their own drawbacks. Performance degradation may occur in complexity reduction approaches due to the constraints of the number of measurements or due to neglecting the model details. The main issue with hierarchical methods is the communication overhead between subsystems and the delay caused by coordination stage. In addition, in both distributed and hierarchical methods the observability of the subsystems is the underlying assumption which may not be feasible in practice. Both distributed and parallel approaches accelerate the computational process; however, they mostly decompose the original system randomly neglecting the effect of subsystems on each other which may result in inaccurate estimation.

Moreover, in almost all of the aforementioned work, DSE has been performed for either a single machine or a small power system. In large-scale power systems with detailed modelling of grid components, a key issue in the design of dynamic state estimator based on dynamic models is the requirement for high-performance computation resources.

From computing source point-of-view supercomputers, multiprocessor networks and various types of parallel processing architectures such as multiple-instruction multiple-data, single-instruction multiple-data (SIMD), and distributed memory, have already been employed for power system analysis [25, 26]. All of these approaches accelerate the simulation process to some extent; however, they are limited by some important factors such as cost, system size, programmability, and communication issues.

The objective of this paper is to explore DSE using extended Kalman filter (EKF) in large-scale power systems utilising detailed synchronous generator modelling which complicates the estimation problem resulting in a high computational burden. Recently, GPUs have tremendously accelerated many HPC applications as well as power system transient stability, electromagnetic transients, and power flow analysis [27–30]. This paper proposes a heterogeneous parallel multi-GPU and multi-core CPU implementation of large-scale DSE based on an accurate and robust relaxation method to estimate both generator (dynamic) and network (static) states of the power system. Coarse-grained parallelism using relaxation

[•] *Complexity reduction* to alleviate the computational burden using reduced order model, lower dimensional measurement data or partial update of the Jacobian matrix [5–7].

[•] *Hierarchical two-level state estimation* which decomposes the whole system into several independent subsystems wherein each subsystem uses its own measurement set to estimate the states locally and then sends the estimated states to a centralised coordinator [8–10].

[•] *Distributed state estimation* where each subsystem can run its local state estimation and exchange data between the substations without the central coordinator [11–14].

[•] *Parallel state estimation* utilises distributed computation on multiple computational servers by decomposing the problem into several subproblems running the estimation process simultaneously in all the subsystems [15, 16].

method along with its implementation on a multi-GPU computational server where each of the individual GPUs has a fine-grained parallel architecture enables significant acceleration of the DSE process. Relaxation method had been previously used in other area such as simulation of very large-scale integration (VLSI) circuits [31], and power system transient stability simulation [32].

The first nontrivial task in relaxation-based joint DSE (RJDSE) is domain decomposition. There are two main approaches used for system decomposition in DSE. Approaches which randomly or geographically [11-13] partition the system are stymied by the computational load-balancing problem and may result in inaccuracy due to neglecting the effect of subsystems. Other methods which rely on the graph theory are highly complex for implementation on large-scale systems [33, 34]. In this work, based on the slow coherency method the original system is decomposed into subsystems in which tightly coupled variables are grouped together, along with equal distribution of computation load on multiple GPUs guaranteed by relaxation method. Also, bad data analysis can be run locally in parallel with the state estimation.

Since traditional measurement sets using SCADA cannot provide adequate data to capture the dynamic nature of the power systems, PMUs with higher refresh rate than SCADA are used in this paper. Considering rapid deployment of PMUs in power systems, it is expected that the entire network will be fully observable only using PMUs in the near future. Through careful PMU placement [35–37], it is possible to make the whole network observable using minimum number of PMUs to obtain a redundant enough measurement set. To reduce data transfer and for practicability optimal placement of PMUs is considered which also improves the security of the system.

In summary, the main contributions of the proposed approach are as follows:

• Parallel multi-GPU implementation of joint generator and network DSE.

• Application of relaxation method which distributes equal workload among all the processors and eliminates the need for all subsystem to be observable.

• Domain decomposition based on coherency method which reduces the effect of non-overlapping decomposition on the accuracy.

• Eliminating central coordinator which reduces the communication time between the subsystems.

• Distributed localised bad data analysis in parallel with state estimation.

The organisation of this paper is as follows. Section 2 provides formulation and state estimation model used in this work. Section 3 explains the proposed RJDSE method. The simulation results are provided in Section 4 followed by conclusion in Section 5.

2 Formulation and state-space model

Joint dynamic and static state estimation for a multi-machine power system can be mathematically described as follows

$$\begin{cases} f(\dot{\mathbf{x}}, \mathbf{x}, t, \mathbf{u}) = \mathbf{0}, & x_{g}(t_{0}) = x_{g0} \\ g(\mathbf{x}, t, \mathbf{u}) = \mathbf{0}, & (1) \\ h(\mathbf{x}, \mathbf{Z}, \boldsymbol{\varepsilon}) = \mathbf{0}, & x_{n}(t_{0}) = x_{n0} \end{cases}$$

where x is the vector of state variable including x_g , the dynamic state of generator and, x_n , the static state of the network. x_0 is the initial values of state variables. f(.) describe the non-linear dynamic behaviour of the generators, g(.) model the output function, and h(.) is the non-linear function of network measurement. u and Zrepresent the output vector and network measurements vector, respectively. t represents the simulation time. For a system with mgenerators and n buses, there are $9 \times m + 2n$ elements in vector x: 9 states per generator, and n voltage magnitudes, n phase angles. Bold notation refers to vectors and matrices.

The detailed ninth-order state-space model of a single generator including automatic voltage regulator (AVR) and power system stabiliser (PSS) used in this work which includes two windings on the *d*-axis (one excitation field and one damper) and two damper windings on the *q*-axis can be written as follows [38]

$$\begin{aligned} \dot{\mathbf{x}}_{g1}(t) &= \omega_{R} \cdot \mathbf{x}_{g2}(t), \\ \dot{\mathbf{x}}_{g2}(t) &= \frac{1}{2S} \left[T_{e}(t) + T_{m} - D \cdot \mathbf{x}_{g2}(t) \right], \\ \dot{\mathbf{x}}_{g3}(t) &= \omega_{R} \cdot \left[e_{fd}(t) - R_{fd} \cdot I_{fd}(t) \right], \\ \dot{\mathbf{x}}_{g4}(t) &= -\omega_{R} \cdot R_{1d} \cdot I_{1d}(t), \\ \dot{\mathbf{x}}_{g5}(t) &= -\omega_{R} \cdot R_{1q} \cdot I_{1q}(t), \\ \dot{\mathbf{x}}_{g6}(t) &= -\omega_{R} \cdot R_{2q} \cdot I_{2q}(t), \\ \dot{\mathbf{x}}_{g7}(t) &= \frac{1}{T_{R}} \left[v_{t} - \mathbf{x}_{g7}(t) \right], \\ \dot{\mathbf{x}}_{g8}(t) &= \frac{1}{T_{2}} \left[T_{1} K_{\text{stab}} \cdot \dot{\mathbf{x}}_{g2} - \mathbf{x}_{g8} + \left(1 - \frac{T_{1}}{T_{W}} \right) \alpha \right], \\ \dot{\mathbf{x}}_{g9}(t) &= \frac{1}{T_{4}} \left[\beta - \mathbf{x}_{g9} \right]. \end{aligned}$$

 $T_{\rm m}$ and $T_{\rm e}$ represent the mechanical input torque and the electrical output torque, respectively. $e_{\rm fd}$ and $I_{\rm fd}$ are field voltage and current. $I_{\rm 1d}$, $I_{\rm 1q}$, and $I_{\rm 2q}$ describe *d*- and *q*-axis currents. For the whole system, according to the aforementioned formulations the $9 \times m$ vector of state variables $x_{\rm g}$ of the synchronous generator is given as

$$\dot{\mathbf{x}}_{g} = [\boldsymbol{\delta}, \boldsymbol{\Delta\omega}, \boldsymbol{\psi}_{\mathrm{f}d}, \boldsymbol{\psi}_{\mathrm{1}d}, \boldsymbol{\psi}_{\mathrm{1}q}, \boldsymbol{\psi}_{2q}, \boldsymbol{v}_{1}, \boldsymbol{v}_{2}, \boldsymbol{v}_{3}]^{\mathrm{T}}, \qquad (3)$$

where δ and $\Delta \omega$ represent vectors of rotor speed and angle, respectively. ψ_{fd} , ψ_{ld} , ψ_{lq} , ψ_{2q} represent vectors of rotor flux linkages and v_1 , v_2 , v_3 are vectors of exciter voltages; v_1 represents the vector of terminal voltage which can be calculated as the network state. For a system with *m* generators, all of aforementioned vectors are $m \times 1$. Fig. 1 shows AC5A type excitation system [39].

The output electrical torque $T_{\rm e}$ of the machine can be written as

$$T_{\rm e} = [L_{ad}'' - L_{aq}'']I_d I_q - \left[\frac{\psi_{\rm fd}}{L_{\rm fd}} + \frac{\psi_{d1}}{L_{d1}}\right]L_{ad}'' I_q + \left[\frac{\psi_{q1}}{L_{q1}} + \frac{\psi_{q2}}{L_{q2}}\right]L_{aq}'' I_d.$$
(4)

where ω_R , S, D, R_{fd} , R_{1d} , R_{1q} , R_{2q} , L_{fd} , L_{d1} , L_{q1} , L_{q2} , L''_{ad} , L''_{aq} , T_R , T_w , T_1 , T_2 , and K_{stab} are constant system parameters whose definition can be found in [38].

The continuous-time differential equations were discretised using the Trapezoidal integration method and then linearised resulting in

$$x_{g}^{t+\tau} - x_{g}^{t} = \frac{\tau}{2} [f(x_{g}, t+\tau, u) + f(x_{g}, t, u)],$$
(5)

$$\boldsymbol{x}_{g}^{t+\tau} = \boldsymbol{F}_{\boldsymbol{x}_{g}^{t}}\boldsymbol{x}_{g}^{t} + \boldsymbol{\eta}^{t}, \, \boldsymbol{\eta}^{t} \sim N(0, \, \boldsymbol{\mathcal{Q}}^{t}), \tag{6}$$

where $F_{\mathbf{x}_{g}^{t}} = \partial f / \partial \mathbf{x}_{g}^{t} |_{\mathbf{x}_{g}^{t}}$ represents the 9 × 9 state transition between two time steps. τ is the integration time-step. $\boldsymbol{\eta}$ and \boldsymbol{Q} are 9 × 1 linearisation error and 9 × 9 error covariance matrix, respectively. The resulting linear algebraic equations are solved to obtain the generator states.

The non-linear measurement function $\gamma(.)$ can be written as

$$\boldsymbol{Z} = \boldsymbol{\gamma}(\boldsymbol{x}_n) + \boldsymbol{\varepsilon}, \, \boldsymbol{\varepsilon} \sim N(0, \, \boldsymbol{R}), \tag{7}$$

where $\gamma(.)$ and ε , are the nonlinear network measurement functions,

IET Gener. Transm. Distrib., 2016, Vol. 10, Iss. 2, pp. 452–459 This is an open access article published by the IET under the Creative Commons Attribution License (http://creativecommons.org/licenses/by/3.0/)



Fig. 1 Synchronous generator excitation system with AVR and PSS

and uncorrelated Gaussian network measurement errors, respectively. x_n represents the vector of network state including voltage magnitude (v) and phase angle (ϕ). To minimise the weighted least-squares error of network state estimation the following objective function should be minimised [40]

$$\boldsymbol{J}(\boldsymbol{x}_{n}) = [\boldsymbol{Z} - \boldsymbol{\gamma}(\boldsymbol{x}_{n})]^{\mathrm{T}} \boldsymbol{R}^{-1} [\boldsymbol{Z} - \boldsymbol{\gamma}(\boldsymbol{x}_{n})]. \tag{8}$$

In a system with *n* buses and *l* branch, **R** is the error covariance matrix of network measurement with the maximum size of $(2n + 2l+1) \times (2n+2l+1)$. Also, **Z**, $\gamma(.)$, and ε have the maximum size of $(2n+2l+1) \times 1$. Satisfying first order optimality condition and substituting the first-order Taylor's expansion of $\gamma(.)$ around x_{n0} in (8), the following equation can be solved iteratively to find the static state of the system.

$$\boldsymbol{\Gamma}^{\mathrm{T}}(\boldsymbol{x}_{\mathrm{n}})\boldsymbol{R}^{-1}\boldsymbol{\Gamma}(\boldsymbol{x}_{\mathrm{n}})\boldsymbol{\Delta}(\boldsymbol{x}_{\mathrm{n}}) = \boldsymbol{\Gamma}^{\mathrm{T}}(\boldsymbol{x}_{\mathrm{n}})\boldsymbol{R}^{-1}[\boldsymbol{Z}-\boldsymbol{\gamma}(\boldsymbol{x}_{\mathrm{n}})], \qquad (9)$$

$$\Delta(x_{\rm n}) = \lambda(x_{\rm n}, \varepsilon), \tag{10}$$

where $\Gamma = \partial \gamma / \partial x_n$ is the Jacobian matrix with the maximum size of $(2n+2l+1) \times 2n$ and $\Delta(\mathbf{x}) = \mathbf{x}_n - \mathbf{x}_{n0}$ is the $2n \times 1$ static state mismatch vector.

For a single generator, using the measurement and estimated states at the time instant *t*, the predicted value $\tilde{x}_{o}^{t+\tau}$ can be formulated as

$$\tilde{\mathbf{x}}_{g}^{t+\tau} = \mathbf{F}_{t} \hat{\mathbf{x}}_{g}^{t}, \quad (\mathbf{x}_{g}^{t} - \hat{\mathbf{x}}_{g}^{t}) \sim N(0, \, \boldsymbol{\rho}_{t}),$$

$$\tilde{\boldsymbol{\rho}}_{(t+\tau)} = \mathbf{F}_{t} \boldsymbol{\rho}_{t} \mathbf{F}_{t}^{T} + \mathbf{L}_{t} \mathbf{Q}_{t} \mathbf{L}_{t}^{T}, \quad (\mathbf{x}_{g}^{t} - \tilde{\mathbf{x}}_{g}^{t}) \sim N(0, \, \tilde{\boldsymbol{\rho}}_{t}),$$
(11)

where $L_t = \partial f / \partial \eta|_{x'_t}$, ρ and $\tilde{\rho}$ are 9 × 9 error covariance matrices for estimated and predicted values, respectively. \tilde{x}_g represent the predicted state and \hat{x}_g is the estimated state.

The updated state through EKF can be written as

$$\hat{\boldsymbol{x}}_{g}^{l+\tau} = \tilde{\boldsymbol{x}}_{g}^{l+\tau} + \boldsymbol{K}_{(t+\tau)}^{\boldsymbol{a}} \boldsymbol{e}_{(t+\tau)}$$
(12)

where K^a and e are 9×9 Kalman gain vector and 9×1 estimation error vector, respectively. The same formulation will stand for network state estimation as well.

3 Relaxation-based joint dynamic state estimation

3.1 Optimal PMU placement

Proper measurement set is an important factor, which affects both accuracy and speed of any estimation method. For higher accuracy and sample rates, PMUs are selected for this work. PMU measures voltage phasor of the bus and currents of all branches connected to it. It is not necessary to install PMU in all of the buses for full observability. Optimal PMU placement for full observability of the system can be written as a linear programming problem to minimise following objective function in a system with n buses:

$$J(x) = \sum_{i=1}^{n} I_i \times p_i,$$

subject to: $\sum_{j=1}^{n} c_{i,j} \times p_i \ge 1$ at bus *i* (13)

where $c_{i,j}$ is the element of connectivity matrix which is 1 if bus *i* and bus *j* are connected, and 0 otherwise. I_i is the cost of PMU installation at bus *i* and p_i is the binary variable equal to 1 or 0 depending on whether PMU is installed on bust *i* or not. In order to handle multiple contingency and to increase the reliability of the system in presence of bad data, backup conventional data are considered in proper location along with critical measurements identification [41, 42].

3.2 Domain decomposition

Domain decomposition refers to any technique that divides a system of equations into several subsets that can be solved individually using conventional numerical methods. The preliminary step for applying RJDSE on GPU is to partition the system into interdependent subsystems while making the dependency between any two subsystems weak enough to ignore their interconnection. It is already shown that the rate of convergence in relaxation method is highly dependent on the method of partitioning [43]. In power system analysis after a large disturbance in the system some generators lose their synchronism with the network. Considering the coherency characteristic which reflects the level of dependency between generators, the proposed method partitions the system into several areas in which generators are in step together or coherent. This type of decomposition is independent of size of the system, disturbance, level of complexity and location of disturbance [44]. For efficient parallelisation, load balancing is also considered in domain decomposition. Fig. 2 shows a power system decomposed into J subsystems. Decomposition based on the coherency approach and equal load work criteria, divides the full set of equation into several independent function running in separate GPUs (functional parallelism).

3.3 Relaxation

Relaxation facilitates the parallel solution of the small subsystems, and the exchange of computational data between them. Each subsystem is solved for its local variables, while other subsystems are considered constant or relaxed during the time-step. At the end of each time-step the global variables of all subsystems are exchanged for the next time-step. Using parallelism inherent in the relaxation method, the proposed RJDSE offers a coarse grained parallelisation as a top level algorithm which should be implemented before using a numerical method for solving the system of equations. Utilising equal load distribution among subsystems RJDSE reduces the complexity resulting in faster computations which also makes it efficient for implementation in a multi-GPU architecture.

3.4 Hierarchy of parallelism for RJDSE

At the data-parallel level equations are expressed based on the unique SIMD-based architecture of GPU (fine-grained parallelism). Instead of using single element values vectors or matrices of them are used. In addition, all matrix-matrix and matrix-vector which include many independent *for* loops are implemented in a fully parallel manner. CUDA which is the general-purpose programming model for the GPU hardware was used in this work. The entire simulation code was written in C++ integrated with CUDA using CUBLAS and CUSPARSE libraries [45, 46] using double precision floating point.

After partitioning the system, the following set of equations can describe the dynamics of each subsystem

$$\begin{cases} f(x_1^{l}, \dots, x_i^{l+\tau}, \dots, x_J^{l}, \dot{x}_1^{l}, \dots, \dot{x}_i^{l+\tau}, \dots, \dot{x}_J^{l}, t, \\ u_1^{l}, \dots, u_i^{l+\tau}, \dots, u_J^{l}) = \mathbf{0} \\ g(x_1^{l}, \dots, x_i^{l+\tau}, \dots, x_J^{l}, t, u_1^{l}, \dots, u_i^{l+\tau}, \dots, u_J^{l}) = \mathbf{0} \\ h(x_1^{l}, \dots, x_i^{l+\tau}, \dots, x_J^{l}, Z_1^{l}, \dots, Z_i^{l+\tau}, \dots, Z_J^{l}, \varepsilon) = \mathbf{0} \end{cases}$$
(14)

where indices L and G stand for local and global variables, respectively. Equation (14) is solved in parallel and iteratively for all subsystems. After each iteration, the global state variables are exchanged and updated between all interconnected subsystems. To calibrate the results, an overall loop based on the Gauss–Jacobi algorithm is applied outside the solutions of subsystems. Since the Gauss–Jacobi algorithm only uses the previously computed values for the solution of each subsystem, all computations of subsystems can be processed in parallel.

In summary, fine-grained parallelism is performed inside the functional parallelism, and functional parallelism is a subset of coarse-grained parallelism (Fig. 3). In the best-case scenario, coarse-grained parallelism by dividing the system into J subsystems reduce the execution time to τ/J ; using functional parallelism and J_1 independent tasks results in execution time of $\tau/J.J_1$; and finally utilising J_0 independent matrix–matrix, matrix–vector, and other type of fine-grained parallelism execution time can be further reduced to $\tau/J.J_1.J_0$. However, in reality the speedup is less than this considering the different costs of parallelisation and data transfer to GPU. The algorithm starts at the top level with Gauss–Jacobi iteration. By functional parallelism, almost equal tasks are assigned to each GPU. The iteration starts at the same time and in parallel inside all GPUs. Inside each



Fig. 2 Original power system decomposed into J subsystems for RJDSE implementation

iteration, fine-grained parallelism is used to accelerate the process. After each iteration, only estimated states of boundary buses are exchanged. If the Gauss-Jacobi algorithm convergence is not satisfied, then new iterations will be performed. Since the subsystems are fully independent of each other, after each time-step the results of network estimation will be transferred to the generator state estimation model. The same procedure is performed for generator dynamic state estimation. While network estimation is working on estimation for the time-step $t + \tau$, generator state estimation is working for the time-step t, simultaneously. Thus, the network estimation is always one step ahead of the generator estimation. The results will be checked for bad data identification (BDI) after each time-step of network estimation. In case of bad data, only the subsystem affected by bad data will repeat state estimation instead of the whole system. Fig. 4 shows the complete flowchart of the RJDSE algorithm implementation on a multi-GPU architecture controlled by η CPU threads.



Fig. 3 Hierarchy of parallelism in RJDSE, τ : integration time-step, t: simulation time

IET Gener. Transm. Distrib., 2016, Vol. 10, Iss. 2, pp. 452–459 This is an open access article published by the IET under the Creative Commons Attribution License (http://creativecommons.org/licenses/by/3.0/)



Fig. 4 Flowchart of RJDSE implementation on multi-GPU architecture with η CPU threads



Fig. 5 Schemes represent

a Overall block diagram of the proposed RJDSE method *b* Time progression of RJDSE on GPU

4 Large-scale DSE case study

The results of relaxation-based joint state estimation implementation on multiple GPUs are demonstrated in this section. The accuracy of the simulation has been verified using the $PSS/E^{(B)}$ software.

4.1 Test system preparation

Several case studies were built and modelled on $PSS/E^{(R)}$ using the IEEE 39 bus benchmark system. Larger test systems were constructed by duplicating the 39-bus system and adding interconnections to explore the efficiency of the proposed method. The initial condition for generator states was chosen based on the steady-state conditions.

To generate PMU data, the rectangular components of voltages and currents were obtained by sampling at regular time intervals, and then a Gaussian noise was added to each component. It is assumed that the PMU refresh rate is 30/s. Since the total vector error of PMU measurements should be less than 1% under steady-state conditions [47], both amplitude and phase angles are randomly distorted and the error of resultant vector is checked with the threshold of 1% until satisfied.

Given a PMU at a bus, it is assumed that the bus voltage phasor and all current phasors along lines connected to that bus will be available. For all case studies, PMUs are located with the considerations given Section 3.1. For example, for Case 1 test system PMUs are located at buses {2, 6, 9, 10, 13, 14, 17, 19, 22, 23, 25, 29, 34}. The error covariance matrices considered for the simulations are $\mathbf{R} = (0.1)^2 * I(n*n)$ and $\mathbf{Q} = (0.05)^2 * I(n*n)$, where \mathbf{I} represents the identity matrix, and \mathbf{n} is the number of states. The convergence threshold is considered 1×10^{-4} for all of the states. Fig. 5a shows the overall block diagram of the proposed method.

4.2 Implementation of RJDSE on multiple GPUs

Case 1 which is the IEEE 39-bus system has been partitioned into four subdomains satisfying both computational load balancing and coherency characteristic of the generators. Similarly, all the large test cases are partitioned. The simulation starts by initialisation on the CPU. After that, the measurement set corresponding to each subsystem was transferred to GPU assigned for that specific subsystem. All the subsystems start the simulation at the same time. After each iteration, boundary data was exchanged among subsystems. Once network state was converged, the results were used for generator state estimation. Network and generator state estimation was running simultaneously (Fig. 5*b*).

4.3 Accuracy analysis and BDI

Accuracy of the proposed method was evaluated under both normal and emergency conditions. A temporary three-phase fault is considered at t = 3 s which is cleared after 100 ms. The normalised Euclidian norm (NEN) of the state estimation is defined as a factor to evaluate the accuracy using:

$$\boldsymbol{x}_{\text{NEN}} = \frac{\|\boldsymbol{x} - \hat{\boldsymbol{x}}\|}{\sqrt{\dim(\boldsymbol{x})}},\tag{15}$$

where x_{NEN} is the normalised Euclidian norm of the estimation error, and dim(x) is the dimension of vector x. x and \hat{x} are vector of true states and estimated states, respectively. Results of simulation on all case studies are shown in Fig. 6. Also, the estimated states for Case 1 are shown in Fig. 7. As shown the maximum of the average errors for all case studies are less than 0.001 p.u. In summary, all the simulation results proves that proposed method is able to accurately capture the dynamic behaviour of the system. There are small differences compared to PSS/E (actual) results which are due to the fact that the order of block execution in each GPU grid is undefined in the kernel definition. Therefore, it leads to slightly different results if different CUDA blocks perform calculations on overlapping portions of data.



Fig. 6 Normalised Euclidian norm of the estimation error using RJDSE

IET Gener. Transm. Distrib., 2016, Vol. 10, Iss. 2, pp. 452–459 This is an open access article published by the IET under the Creative Commons Attribution License (http://creativecommons.org/licenses/by/3.0/)



Fig. 7 Generator state estimation and error of estimation in RJDSE

The simulation results were followed by BDI process employing normalised residuals test as follows [48]

$$r_i^{\rm N} = \frac{|r_i|}{s_{ii}} \le \kappa, \tag{16}$$

where r_i^N is the largest normalised residual and σ_{ii} is the standard deviation of the *i*th component of the residual vector. The largest normalised residuals with threshold of $\kappa = 3$ were considered as bad data. Here the bad data refers to measurements with gross errors. Once bad data is identified corresponding measurement was updated by deducting gross error ($R_{ii}(\sigma_{ii}r_i)$) from bad data. Using the updated measurements state estimation was repeated only for the subsystems which were affected by bad data. For large-scale systems, this localisation of bad data can save lots of time which can in turn accelerate the state estimation process.

4.4 Complexity analysis and speedup

To demonstrate the performance of the proposed RJDSE in terms of speed-up, the test systems described in Table 1 were used to perform simulation on TeslaTMS2050 server from NVIDIA[®] with 4 GPUs, and 448 cores in each unit of GPU. This device contains 14

Case	No. of buses	No. of gen.	T ^{Ex.} CPU, s	T ^{Ex.} S.GPU, s	Τ ^{Ex.} M.GPU, s
1	39	10	0.6	0.4	0.4
2	78	20	1.9	1.1	0.9
3	156	40	4.8	2.16	1.45
4	312	80	21.1	6.9	4.3
5	624	160	45.2	12.6	7.3
6	1248	320	95.3	28.9	15.3
7	2496	640	290	43.1	22.2
8	4992	1280	475	62.8	30.6

streaming multiprocessors, each with 32 streaming processors, an instruction unit, and on-chip memory [33]. CUDA version 5.0 with compute capability 2.0 is used for programming. The CPU is the quad-core Intel[®] Xeon[™] E5-2620 with 2.0 GHz core clock and 32 GB memory, running 64-bit Windows 7[®] operating system.

Generally, when a system with N buses is partitioned into M subsystems, each subdomain has approximately N/M buses. Assume that solving a linear system with iterative method has complexity of $O(N^{\alpha})$ where $\alpha \ge 1$. Using the domain decomposition technique, the complexity of solving each subsystem is $O((N/M)^{\alpha})$ which results in the complexity of $O((N)^{\alpha/M})^{\alpha-1})$ for the entire system. It may not be realistic to expect the same speedup in practice; however, the results clearly indicate the advantages of domain decomposition in accelerating DSE. As can be seen from Table 1 and Fig. 8 the percentage of required execution times increases faster in single-GPU ($T_{\rm M.GPU}^{\rm Ex}$) implementation



Fig. 8 Percentage of execution time for varying test cases on single- and multi-GPU simulators

which shows the higher complexity of this method. The comparison between CPU-only execution time $(T_{CPU}^{Ex.})$ and GPU execution time also verify that exploiting parallelism using GPUs can results in significant acceleration in the DSE process. It should also be noted that a better PMU placement by reducing the size of the problem and data transfer may also result in faster state estimation.

5 Conclusions

This paper presented a parallel relaxation-based joint state estimation approach for DSE of power systems. Using the traditional SE method, the size and computational cost of the simulator is usually prohibitive, especially for simulating large-scale systems. As the size of the system increases the amount of data collected in the network grows exponentially which leads to a slow and expensive state estimation process. computationally The motivation behind this work is to test the feasibility of a fully parallel method that could alleviate these limitations.

The proposed methodology: (i) uses only local measurements for DSE in each subsystem which reduce the size of the problem and leads to major reduction in data communication; (ii) takes advantage of massive multi-GPU parallelisation which reduces the execution time significantly; (iii) can be applied to any dynamic system regardless of complication and the type of states or parameters that are needed to be estimated; (iv) localises the effects of bad data to subsystems; (v) does not require either local observability or a central coordinator.

Result comparisons verified the accuracy and efficiency of the proposed method. In addition, the performance of the slow coherency method as the partitioning tool was analysed, and it was concluded that for different fault locations in the system, results derived from this method had lower amounts of error.

The proposed method is general and extensible to any number of GPUs connected in a cluster. Results show that more GPUs can reduce expected computation time.

6 Acknowledgment

Financial support from the Natural Science and Engineering Research Council of Canada (NSERC) is gratefully acknowledged.

7 References

- Valverde, G., Terzija, V.: 'Unscented kalman filter for power system dynamic state 1
- estimation', *IET Gener. Transm. Distrib.*, 2011, **5**, (1), pp. 29–37 Queiroz, C., Mahmood, A., Tari, Z.: 'SCADASim—a framework for building SCADA simulations', *IEEE Trans. Smart Grid*, 2011, **2**, (4), pp. 589–597 2
- Green, R.C., Wang, L., Alam, M.: 'Applications and trends of high performance computing for electric power systems: focusing on smart grid', *IEEE Trans.* 3 Smart Grid, 2013, 4, (2), pp. 922-931
- Chakrabarti, S., Kyriakides, E., Ledwich, G., et al.: 'Inclusion of PMU current 4 phasor measurements in a power system state estimator', *IET Gener. Transm. Distrib.*, 2010, **4**, (10), pp. 1104–1115 Korres, G.N., Contaxis, G.C.: 'Application of a reduced model to a distributed state estimator'. Proc. IEEE Power and Engineering Society, 2000, vol. 2, pp. 999–1004
- 5
- Zhang, J., Welch, G., Bishop, G.: 'LoDiM: a novel power system state estimation method with dynamic measurement selection'. Proc. IEEE Power and Engineering
- Society, July 2011, pp. 1–7 Seshadri Sravan Kumar, V., Dhadbanjan, T.: 'State estimation in power systems 7 using linear model infinity norm-based trust region approach', *IET Gener. Transm. Distrib.*, 2013, **7**, (5), pp. 500–510 Zhao, L., Abur, A.: 'Multiarea state estimation using synchronized phasor
- measurements', IEEE Trans. Power Syst., 2005, 20, (2), pp. 611-617
- Gómez-Expósito, A., de la Villa Jaén, A.: 'Two-level state estimation with local measurement pre-processing', *IEEE Trans. Power Syst.*, 2009, **24**, (2), pp. 676–684 0
- Risso, M., Rubiales, A.J., Lotito, P.A.: 'Hybrid method for power system state 10 estimation', *IEEE Trans. Power Appl. Syst.*, 2015, **9**, (7), pp. 636–643 Jiang, W., Vittal, V., Heydt, G.T.: 'Diakoptic state estimation using phasor
- 11 measurement units', IEEE Trans. Power Syst., 2008, 23, (4), pp. 1580-1589 12
- Korres, G.N.: 'A distributed multiarea state estimation', IEEE Trans. Power Syst., 2011, 26, (1), pp. 73-84 13
- Xie, L., Choi, D.H., Kar, S., et al.: 'Fully distributed state estimation for wide-area monitoring systems', *IEEE Trans. Smart Grid*, 2012, **3**, (3), pp. 1154–1169 Kekatos, V., Giannakis, G.: 'Distributed robust power system state estimation',
- 14 IEEE Trans. Power Syst., 2013, 28, (2), pp. 1617-1626

- 15 Abur, A., Tapadiy, P.: 'Parallel state estimation using multiprocessors', Electr. Power Syst. Res., 1990, 18, (1), pp. 67-73
- Falcao, D.M., Wu, F.F., Murphy, L.: 'Parallel and distributed state estimation', IEEE Trans. Power Syst., 1995, 10, (2), pp. 724-730
- Mandal, J.K., Sinha, A.K., Roy, L.: 'Incorporating nonlinearity of measurement 17 function in power system dynamic state estimation', IET Gener. Transm. Distrib., 1995, 142, (3), pp. 289-296
- 18 Shih, K.R., Huang, S.J.: 'Application of a robust algorithm for dynamic state estimation of a power system', IEEE Trans. Power Syst., 2002, 17, (1), pp. 141-147
- Caro, E., Conejo, A.J.: 'State estimation via mathematical programming: a 19 comparison of different estimation algorithms', IET Gener. Transm. Distrib., 2012, 6, (6), pp. 545-553
- Aminifar, F., Shahidehpour, M., Fotuhi-Firuzabad, M., et al.: 'Power system dynamic state estimation with synchronized phasor measurements', IEEE Trans. Instrum. Meas., 2014, 63, (2), pp. 352-363
- Ghahremani, E., Kamwa, I.: 'Dynamic state estimation in power system by applying the extended Kalman filter with unknown inputs to phasor measurements', *IEEE Trans. Power Syst.*, 2011, **11**, (4), pp. 655–661
- Wang, S., Gao, W., Meliopoulos, A.P.S.: 'An alternative method for power system 22 dynamic state estimation based on unscented transform', IEEE Trans. Power Syst., 2012, 27, (2), pp. 942-950
- Zhang, J., Welch, G., Bishop, G., et al.: 'A two-stage Kalman filter approach for 23 robust and real-time power system state estimation', IEEE Trans. Sust. Energy, 2014, 5, (2), pp. 629-636
- 24 Zhou, N., Meng, D., Huang, Z., et al.: 'Dynamic state estimation of a synchronous machine using PMU data: a comparative study', IEEE Trans. Smart Grid, 2015, 6, (1), pp. 450–460
- Happ, H.H., Pottle, C., Wirgau, K.A.: 'An assessment of computer technology for large scale power system simulation'. Proc. Power Industry Computer Application Conf., May 1979, pp. 316–324
- 26 Brasch, F.M., Van Ness, J.E., Kang, S.C.: 'Simulation of a multiprocessor network for power system problems', IEEE Trans. Power Appl. Syst., 1982, 101, (2), pp. 295-301
- Jalili-Marandi, V., Dinavahi, V.: 'SIMD-based large-scale transient stability 27 simulation on the graphics processing unit', IEEE Trans. Power Syst., 2010, 25, (3), pp. 1589-1599
- (3), pp. 1589–1599 Jalili-Maradi, V., Zhou, Z., Dinavahi, V.: 'Large-scale transient stability simulation of electrical power systems on parallel GPUs', *IEEE Trans. Parall. Distrib. Syst.*, 2012, 23, (7), pp. 1255–1266 Zhou, Z., Dinavahi, V.: 'Parallel massive-thread electromagnetic transient simulation on G-PU', *IEEE Trans. Power Deliv.*, 2014, 29, (3), pp. 1045–1053 28
- 30 Li, Z., Donde, V.D., Tournier, J.C., et al.: 'On limitations of traditional multi-core and potential of many-core processing architectures for sparse linear solvers used in large-scale power system applications'. Proc. IEEE Power and Engineering Society, July 2011, pp. 1-8
- 31 White, J.K., Sangiovani-Vincentelli, A.L.: 'Relaxation techniques for the simulation of VLSI circuits' (Kluwer, Boston, MA, 1987)
- 32 Crow, M.L., Ilic, M.: 'The parallel implementation of the waveform relaxation method for transient stability simulations', IEEE Trans. Power Syst., 1990, 5, (3), pp. 922–932
- Xie, L., Choi, D.H., Kar, S.: 'Cooperative distributed state estimation: local 33 observability relaxed'. Proc. IEEE Power and Engineering Society, July 2011, pp. 1-11
- Weng, Y., Li, Q., Negi, R., et al.: 'Distributed algorithm for SDP state estimation'. 34 Proc. Innovative Smart Grid Technologies, February 2013, pp. 1-6
- Caro, E., Singh, R., Pal, B.C., et al.: 'Participation factor approach for phasor 35 measurement unit placement in power system state estimation', IET Gener. Transm. Distrib., 2012, 6, (9), pp. 922-929
- Korres, G.N., Manousakis, N.M.: 'State estimation and observability analysis for phasor measurement unit measured systems', IET Gener. Transm. Distrib., 2012, **6**, (9), pp. 902–913
- Azizi, S., Gharehpetian, G.B., Dobakhshari, A.S.: 'Optimal integration of phasor 37 measurement units in power systems considering conventional measurements', IEEE Trans. Smart Grid, 2013, 4, (2), pp. 1113-1121
- 38
- Kundur, P.: 'Power system stability and control' (McGraw-Hill, 1994) IEEE Std 421.5–2005: 'IEEE recommended practice for excitation system models 39 for power system stability studies'. Proc. IEEE Power Engineering Society, 2006, pp. 1-85
- Abur, A., Gómez-Expósito, A.: 'Power system state estimation theory and 40 implementation' (Marcel Dekker, Inc., 2004) pp. 20-25
- Azizi, S., Dobakhshari, A.S., Nezam Sarmadi, S.A., et al.: 'Optimal PMU 41 placement by an equivalent linear formulation for exhaustive search', IEEE *Trans. Smart Grid*, 2012, **3**, (1), pp. 174–182 Gómez-Expósito, A., Abur, A.: 'Generalized observability analysis and
- 42 measurement classification', IEEE Trans. Power Syst., 1998, 13, (3), pp. 1090-1096
- 43 Leondes, C.T.: 'Control and dynamic systems v42: analysis and control system' (Academic press Inc., 1991) You, H., Vittal, V., Wang, X.: 'Slow coherency-based islanding', *IEEE Trans.*
- 44 Power Syst., 2004, 19, (1), pp. 483-491
- NVIDIA: 'CUSPARSE library', NVIDIA Developer, February 2013 45
- NVIDIA: 'CUBLAS library', NVIDIA Developer, August 2014 46
- IEEE Std C37.118.1: 'IEEE standard for synchrophasor measurements for power 47 systems', December 2011, pp. 1-61 Monticelli, A., Garcia, A.: 'Reliable bad data processing for real-time state
- 48 estimation', IEEE Trans. Power Appl. Syst., 1983, 102, (5), pp. 1126-1139